ORDER NO. VSD9612MJ01B

Service Manual

Volume 2

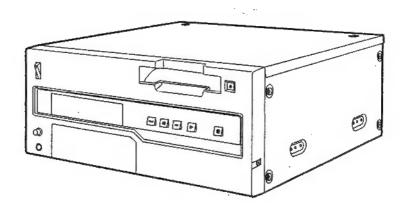
Sec. 1 Block Diagrams

Sec. 2 Schematic Diagrams

Sec. 3 Circuit Board Diagrams

Digital Video Cassette Recorder

AJ-D650E AJ-D640E



Please refer to the Service Manual Volume 1 (order No. VSD9612MJ01A) for operating instructions, service information, maintenance & mechanical adjustments, electrical adjustments and exploded views & replacement parts lists.

Panasonic

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△ WARNING

This service information is designed for experienced repair technicians only and is not designed for use by the general public. It does not contain warnings or cautions to advice non-technical individuals of potential dangers in attempting to service a product. Products powered by electricity should be serviced or repaired only by experienced professional technicians. Any attempt to service or repair the product or products dealt with in this service manual by anyone else could result in serious injury or death.

INTRODUCTION

This service manual contains technical information which will allow service personnels to understand and service the DVCPRO Studio VTR AJ-D650E/D640E.

If the part or circuit is changed or modified, this information will be followed by supplementary service manual to be filed with original manual.

Specifications

GENERAL

Power supply:

AC 220 V - 240 V ± 10%, 50 - 60 Hz

Power consumption:

Operating ambient temperature: 5°C to 40°C

Operating ambient humidity:

Weight:

Dimensions (WxHxD): Recording format:

10% to 90% (no condensation) 16 kg

424×175×415 mm **DVCPRO** format

Recording tracks: Digital video

Time code Recorded in sub-code area

Digital audio 2channels Cue Track 1 track Control (CTL) 1 track 33.854 mm/sec

Tape speed: Recording time:

General purpose

cassette: Max. 123 minutes

News-gathering

cassette; Max. 63 minutes 1/4-inch thin magnetic layer metal tape

Tape: FF/REW time: Less than 3 min (with general purpose cassette)

Less than 2 min (with news-gathering)

cassette)

Search speed: Digital slow motion: 0 to ±60× normal speed (colour) +0.75× normal speed in + direction -0.43× normal speed in - direction ±0 frame (using time code)

Editing accuracy: Tape timer accuracy: Servo lock time:

±1 frame (using continuous CTL signal) Less than 0.5 sec. (colour framing/

standby ON)

VIDEO

(Digital video)

Sampling frequencies:

Y: 13.5 MHz/PB, PR: 3.375 MHz 8 bits

Quantizing:

Error correction: Reed-Solomon product code

(Digital IN/analog component OUT)

Video bandwidth:

Y: 25 Hz to 5.5 MHz (±1.0 dB) Ps, Ps: 25 Hz to 1.3 MHz (±1.0 dB)

S/N ratio: K factor:

Better than 60 dB Less than 2%

(Analog component IN/component OUT)

Video bandwidth:

Y: 25 Hz to 5.5 MHz (-1.5 dB to +1.0 dB)

Ps. Ps: 25 Hz to 1.3 MHz (-2.0 dB to +1.0 dB)

S/N ratio: K factor:

55 dB (Typ) Less than 2%

(Analog composite IN/composite OUT)

Video bandwidth:

Y: 25 Hz to 5.5 MHz (-3.0 dB to +1.0 dB)

Y/C delay: K factor:

Better than 20 ns Less than 3%

(Video input connector)

Analog component Input:

BNCx3 (Y, Ps, Pr) Y: 1.0 Vp-p, 75Ω Ps, Ps; 0.7 Vp-p, 75Ω (100% colour bar)

Analog composite input:

BNC×2, loop-through, 75Ω on/off

S VIDEO input:

S terminal (4-pin)×1 Y: 1.0 Vp-p, 75Ω C: 0.3 Vp-p (burst), 75Ω Analog composite

Reference input:

BNC×2, loop-through, 75Ω on/off

Serial digital component input

(option):

Complies with EBU Tech. 3267-E. BNCx2, active through

(Video output connector)

Analog component output:

Analog composite output:

BNC×3 (Y, Pa, Pa) Y: 1.0 Vp-p, 75Ω Ps, Ps: 0.7 Vp-p, 75Ω (100% colour bar)

BNC×3

Video1/video2/video3 (superimpose

on/off)

S VIDEO output:

S terminal (4-pin)×1 Y: 1.0 Vp-p, 75Ω

C: 0.3 Vp-p (burst), 75Ω

Serial digital component output

Complies with EBU Tech. 3267-E,

BNCx3

(Video signals adjustment)

Video output gain: ±3 dB Video output chroma gain: +3 dB

Video output hue: ±30° Video output setup: ±100 mV Video output sync phase:

±2 //S ±180° Control from ENCODER REMOTE connector

AUDIO

(Digital audio)

Sampling frequencies:

Video output SC phase:

48 kHz Quantizing: 16 hits

Frequency response: Dynamic range:

20 Hz to 20 kHz (-1.0 dB to +0.5 dB) Better than 86 dB (1 kHz, emphasis OFF,

"A" weighted)

Distortion: Less than 0.1% (1 kHz, emphasis OFF, standard level)

Less than -80 dB (1 kHz, between

2 channels) Wow & flutter: Below measurable limits

18 dB Headroom:

De-emphasis: T1=50 //s/T2=15 //s (on/off automatic)

(Cue track)

Crosstalk:

Frequency response: 300 Hz to 6 kHz ±3 dB

(Audio input connector)

Serial digital input (option):

Analog input (CH1/CH2):

+4/0/-20/-60 dBu Digital input (CH1/CH2) (option): BNC×1, AES/EBU format

Complies with EBU Tech. 3267-E

(BNC)

(Audio output connector)

Analog output (CH1/CH2):

Digital output (CH1/CH2) (option):

XLR×2, low impedance, +4/0/-20 dBu

XLRx2, 600Ω/high Impedance selectable,

BNC×1, AES/EBU format Serial digital output (option): Complies with EBU Tech. 3267-E (BNC 75Ω)

Monitor output: Headphones:

Phonox1, 600 Ω, -8 dBV Variable level, 6 mm phone, 8Ω

Other input/output connector

Time code input: Time code output: BNC×1, 0.5 to 8 Vp-p BNC×1, 2.0 Vp-p

RS-422A input/output: RS-232C:

D-sub 9-pin, RS-422A interface D-sub 25-pin, RS-232C interface

Encoder remote: D-sub 15-pin

Weight and dimensions when shown are approximately. Specifications are subject to change without notice.

SAFETY PRECAUTIONS

GENERAL GUIDELINES

- When servicing, observe the original lead dress. If a short circuit is found, replace all parts which have been overheated or damaged by the short circuit.
- After servicing, see to it that all the protective devices such as insulation barriers, insulation papers shields are properly installed.
- After servicing make the following leakage current checks to prevent the customer from being exposed to shock hazards.

LEAKAGE CURRENT COLD CHECK

- Unplug the AC cord and connect a jumper between the two prongs on the plug.
- Measure the resistance value, with an ohm meter, between the jumpered AC plug and each exposed metallic cabinet part on the equipment such as screwheads, connectors, control shafts, etc. When the exposed metallic part has a return path to the chassis, the reading should be between 1 MΩ and 5.2 MΩ.

When the exposed metal dose not have a return path to the chassis, the reading must be ∞ .

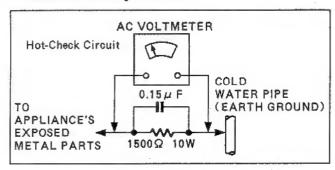


Figure 1

LEAKAGE CURRENT HOT CHECK (See Figure 1)

- Plug the AC cord directly into the AC outlet.
 Do not use an isolation transformer for this check.
- 2. Connect a 1.5 K Ω , 10 W resistor, in parallel with 0.15 μ F capacitor, between each exposed metallic part on the set an a good earth ground such as a water pipe, as shown in Figure 1.
- Use an AC voltmeter, with 1000 ohms/volt or more sensitivity, to measure the potential across the resistor.
- Check each exposed metallic part, and measure the voltage at each point.
- Reverse the AC plug in the AC outlet repeat each of the above measurements.
- 6. The potential at any point should not exceed 0.75 volts RMS. A leakage current tester (Simpson Model 229 equivalent) may be used to make the hot checks, leakage current must not exceed 1/2 milliamp. In case a measurement is outside of the limits specified, there is a possibility of a shock hazard, and the equipment should be repaired and rechecked before it is returned to the customer.

ELECTROSTATICALLY SENSITIVE (ES) DEVICES

Some semiconductor (solid state) devices can be damaged easily by static electricity. Such components commonly are called Electrostatically sensitive (ES) Devices. Examples of typical ES devices are integrated circuits and some field-effect transistors and semiconductor "chip" components. The following techniques should be used to help reduce the incidence of component damage caused by static electricity.

- Immediately before handling any semiconductor component or semiconductor-equipped assembly, drain off any electrostatic charge on your body by touching a known earth ground.
 - Alternatively, obtain and wear a commercially available discharging wrist strap device, which should be removed for potential shock reasons prior to applying power to the unit under test.
- After removing an electrical assembly equipped with ES devices, place the assembly on a conductive surface such as aluminum foil, to prevent electrostatic charge buildup or exposure of the assembly.
- Use only a grounded tip soldering iron to solder or unsolder ES devices.
- Use only an anti-static solder removal device classified as "anti-static" can generate electrical charges sufficient to damage ES devices.
- Do not use freon-propelled chemicals. These can generate electrical charges sufficient to damage ES devices.
- Do not remove a replacement ES device from its protective package until immediately before you are ready to install it. (most replacement ES devices are packaged with leads electrically shorted together by conductive foam, aluminum foil or comparable conductive material).
- Immediately before removing the protective material from the leads of replacement ES device, touch the protective material to the chassis or circuit assembly into which the device will be installed.
 - CAUTION: Be sure no power is applied to the chassis or circuit, and observe all other safety precautions.
- 8. Minimize bodily motions when handling unpackaged replacement ES devices. (Otherwise harmless mother such as the brushing together of your clothes febric or the lifting of your foot from a carpeted floor can generate static electricity sufficient to damage an ES device).

X-RADIATION

WARNING

- The potential source of X-Radiation in EVF sets is the High Voltage section and the picture tube.
- When using a picture tube test jig for service, ensure that jig is capable of handling 10kV without causing X-Radiation.
 NOTE: It is important to use an accurate periodically
- calibrated high voltage meter.

 3. Measure the High Voltage. The meter (electric type) reading should indicate 2.5kV, ± 0.15kV. If the meter indication is out of tolerance, immediate service and correction is required to prevent the possibility of premature component failure. To prevent an X-Radiation possibility, it is essential to use the specified picture tube.

IMPORTANT

"Unauthorized recording of copyrighted television programs, video tapes and other materials may infringe the right of copyright owners and be contrary to copyright laws."

■ THIS APPARATUS MUST BE EARTHED

To ensure safe operation the three-pin plug must be inserted only into a standard three-pin power point which is effectively earthed through the normal house-hold wiring.

Extension cords used with the equipment must be three-core and be correctly wired to provide connection to earth. Wrongly wired extension cords are a major cause of fatalities.

The fact that the equipment operates satisfactorily does not imply that the power point is earthed and that the installation is completely safe. For your safety, if in any doubt about the effective earthing of the power point, consult a qualified electrician.

■ DO NOT REMOVE PANEL COVER BY UN-SCREWING

To reduce the risk of electric shock, do not remove cover. No user serviceable parts inside. And do not insert fingers or any other objects into the video cassette holder.

WARNING:

TO REDUCE THE RISK OF FIRE OR SHOCK HAZARD, DO NOT EXPOSE THIS EQUIPMENT TO RAIN OR MOISTURE.

CAUTION:

TO REDUCE THE RISK OF FIRE OR SHOCK HAZARD, AND ANNOYING INTERFERENCE, USE THE RECOMMENDED ACCESSOIRES ONLY.

CAUTION:

To reduce the risk of fire or shock hazard, refer change of switch setting inside the unit to qualified service personnel.

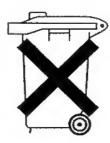
Operating precaution

Operation near any appliance which generates strong magnetic fields may give rise to noise in the video and audio singals. If this should be the case, deal with the situation by, for instance, moving the source of the magnetic fields away from the unit before operation.

is the safety information.

Attention/Attentie

- . This apparatus contains a lithium battery for memory back-up.
- For the removal of the battery at the moment of the disposal at the end
 of the service life please consult your dealer.
- · Do not throw away the battery. Instead, hand it in as hazardous waste.
- Dit apparaat bevat een lithiumbatterij voor memory back-up.
- Raadpleeg uw leverancier over de verwijdering van de batterij op het moment dat u het apparaat bij einde levensduur afdankt.
- · Gooi de batterij niet weg, maar lever hem in als KCA.



Memo

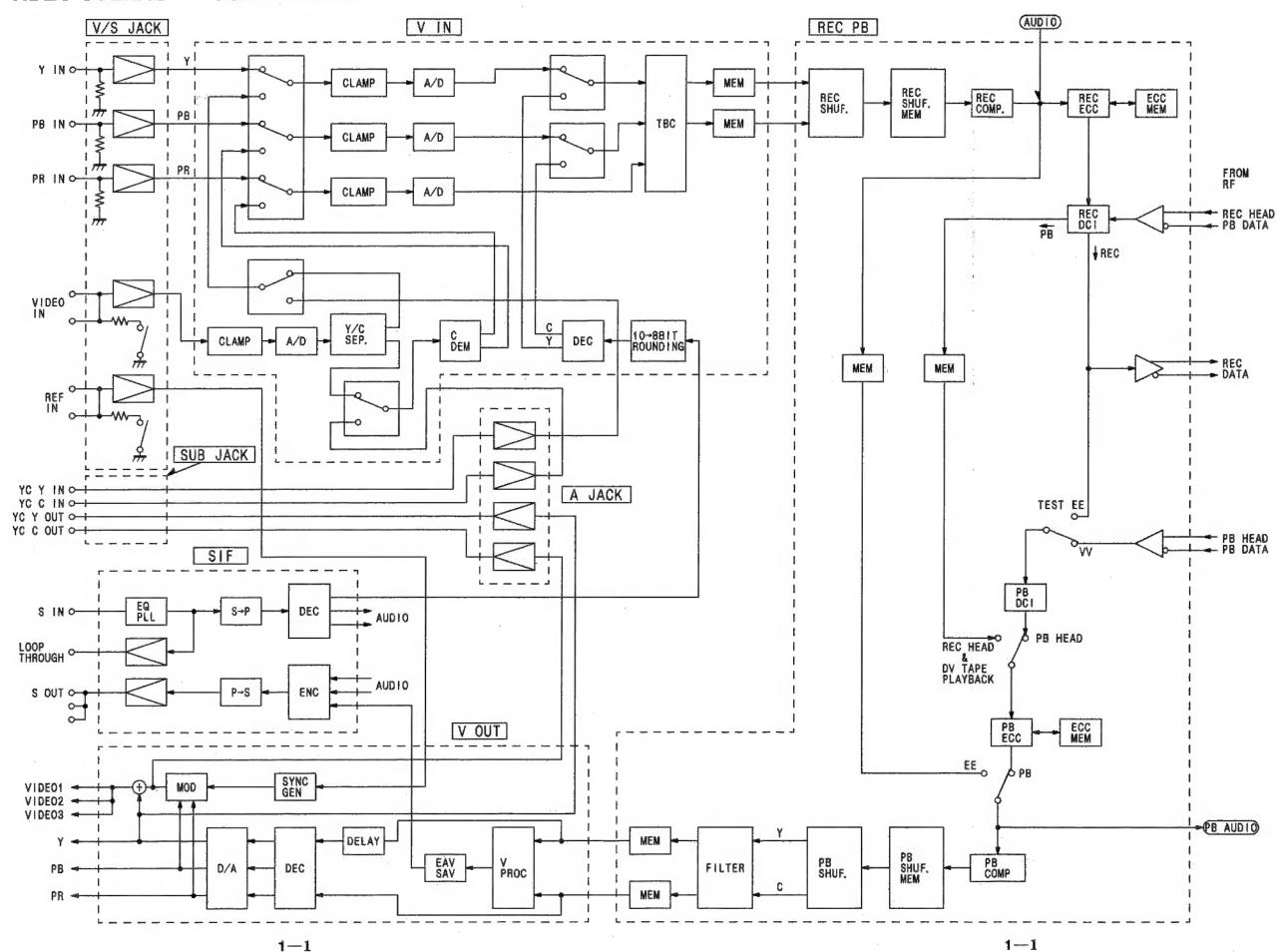
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BLOCK DIAGRAMS

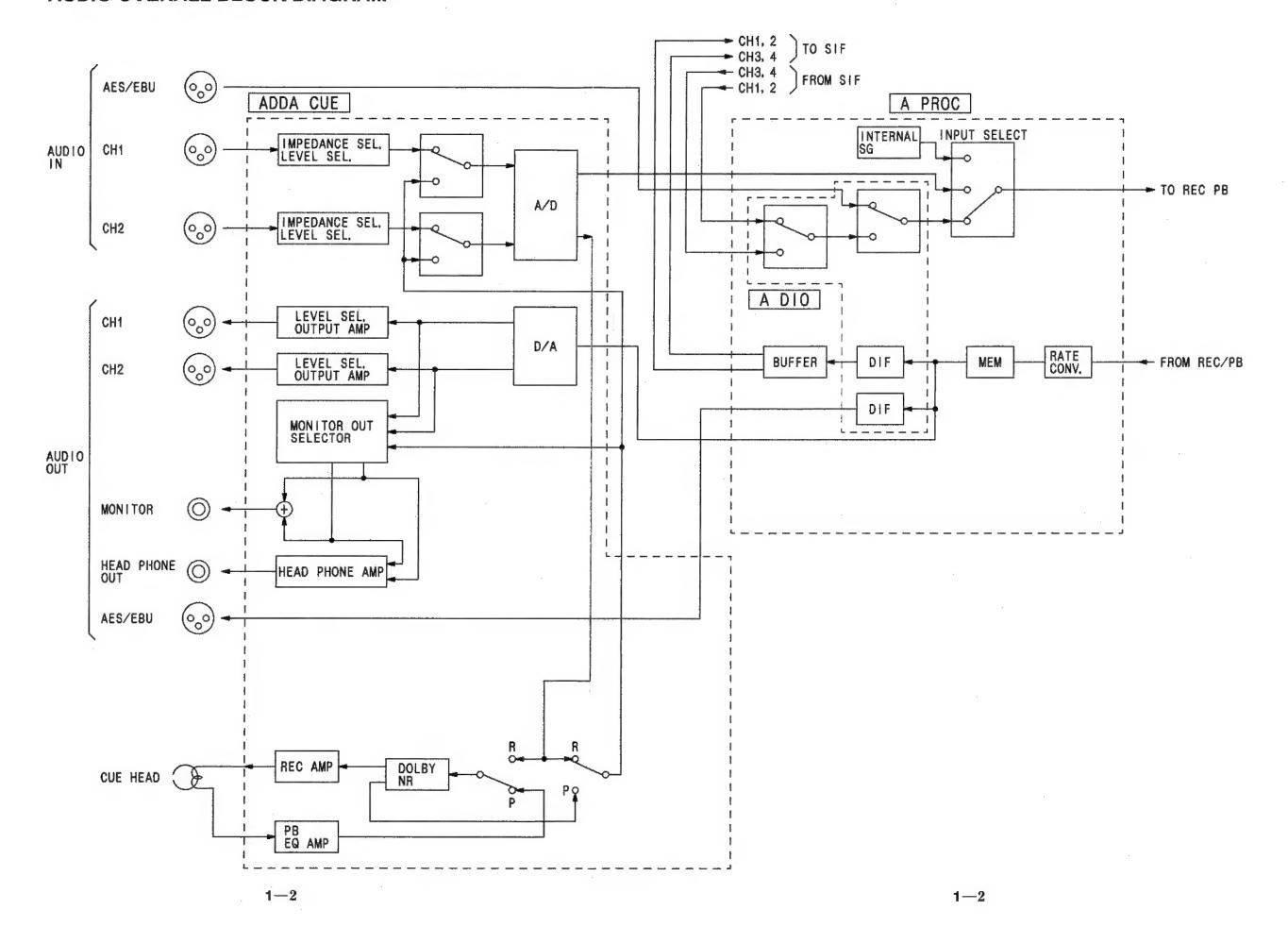
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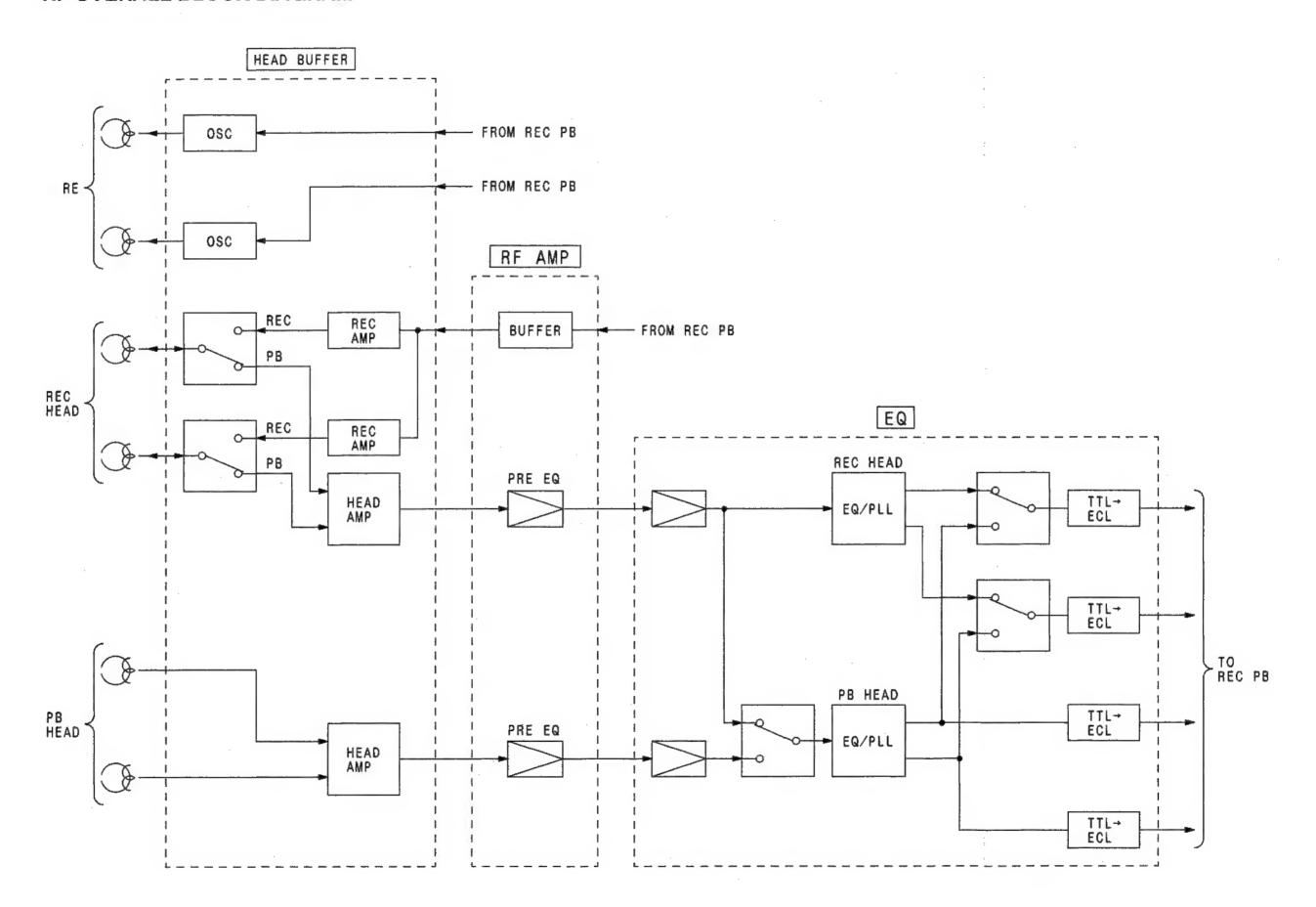
VIDEO OVERALL BLOCK DIAGRAM



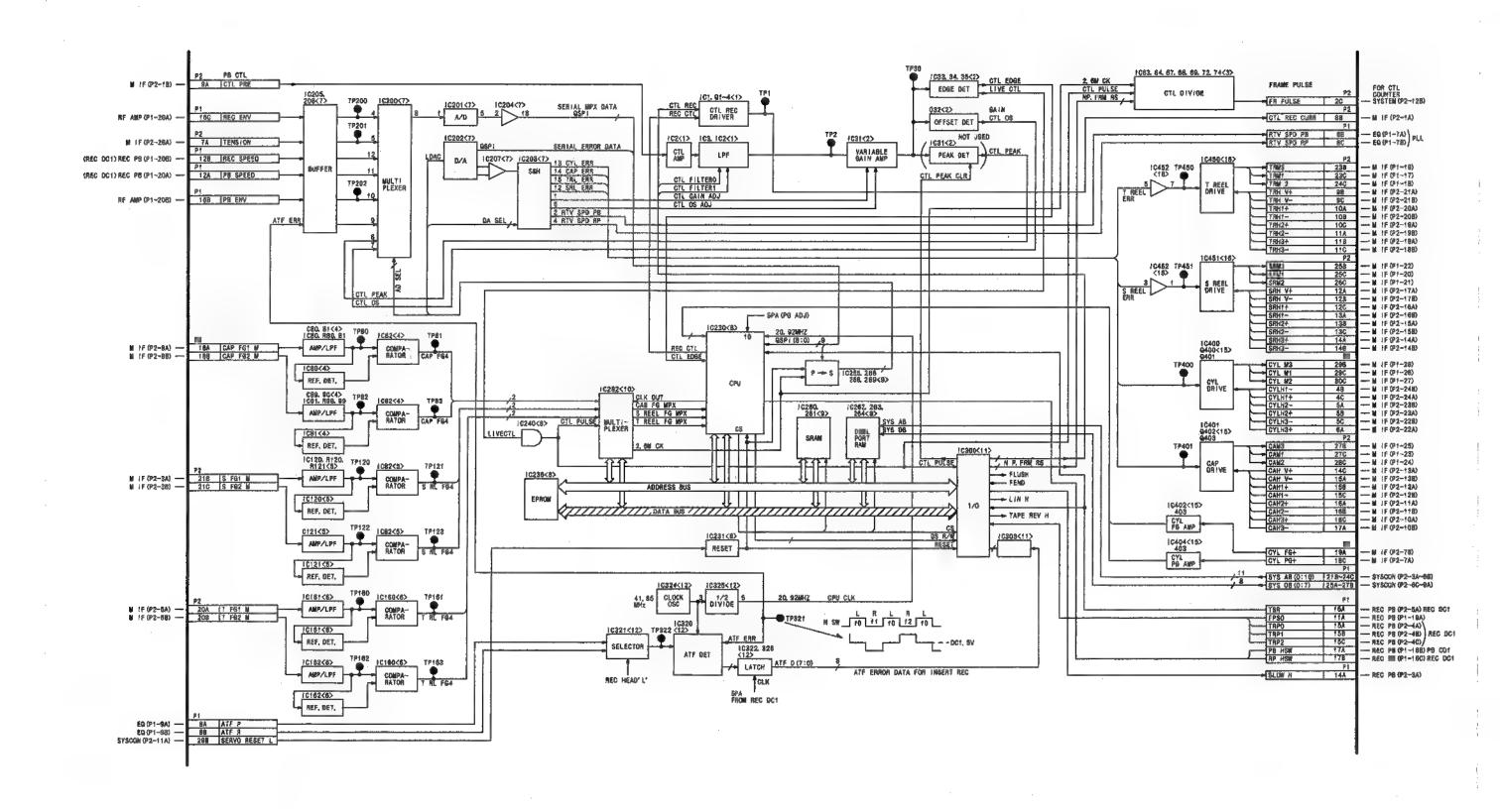
AUDIO OVERALL BLOCK DIAGRAM



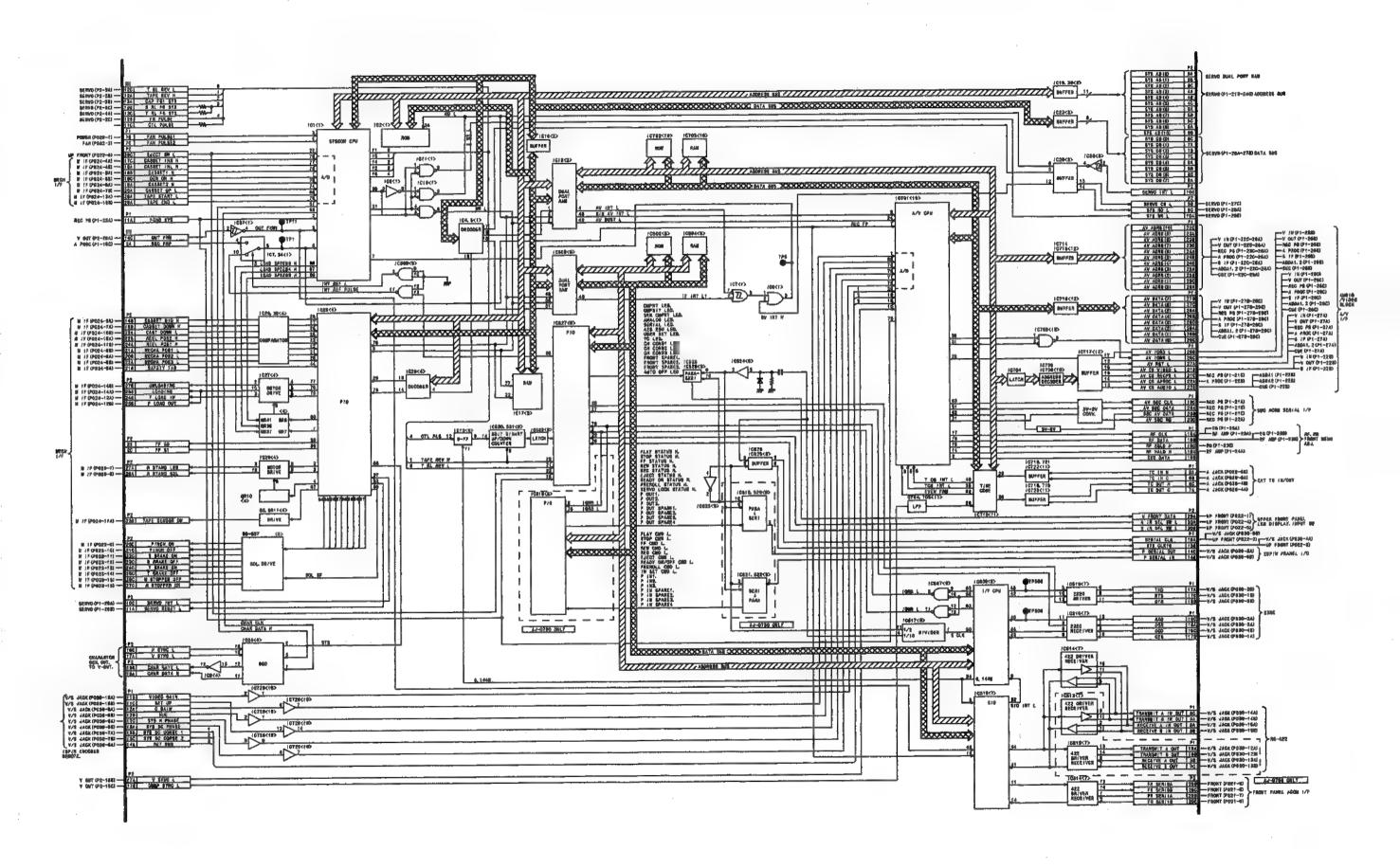
RF OVERALL BLOCK DIAGRAM



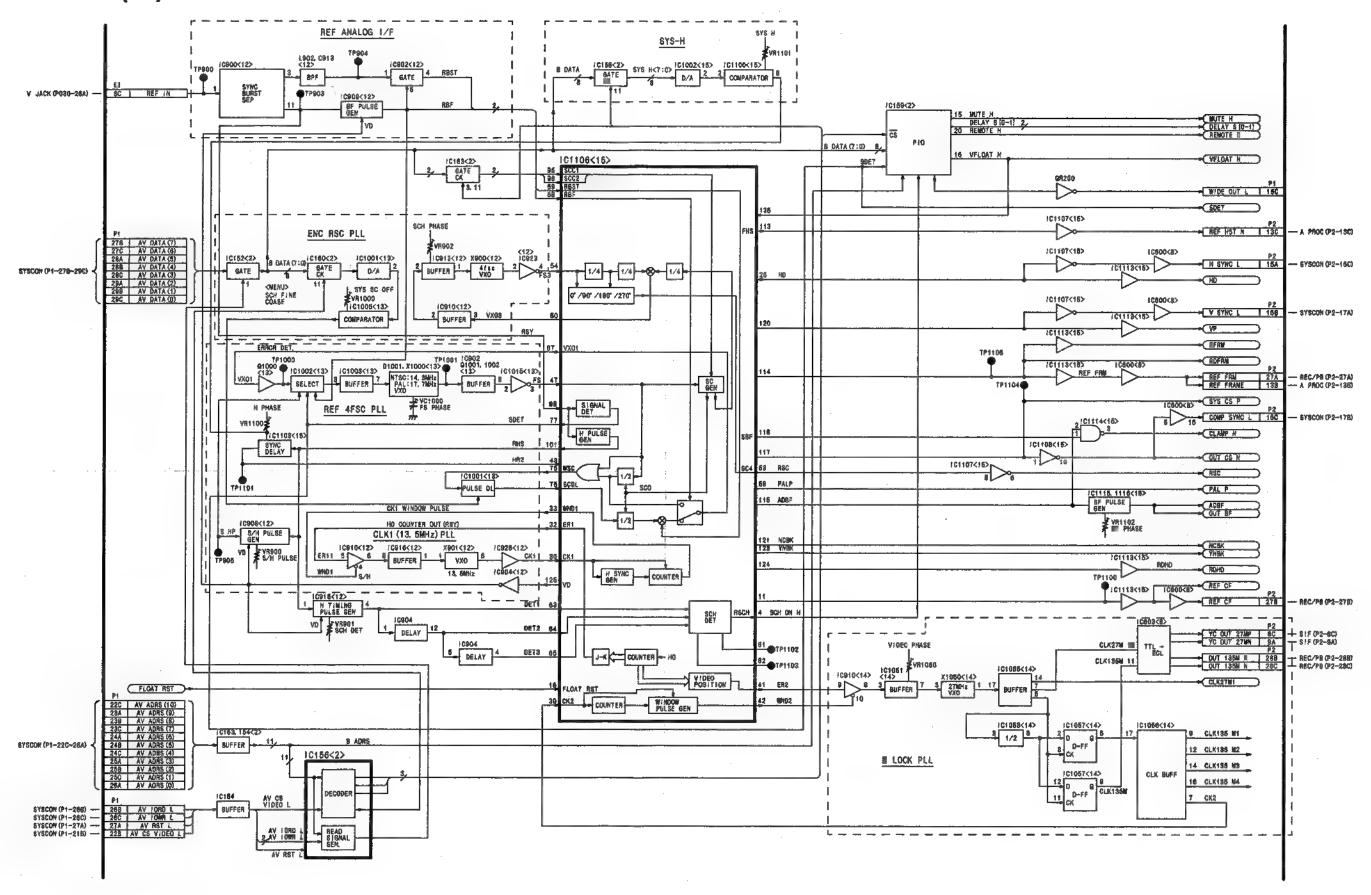
F1 SERVO BLOCK DIAGRAM



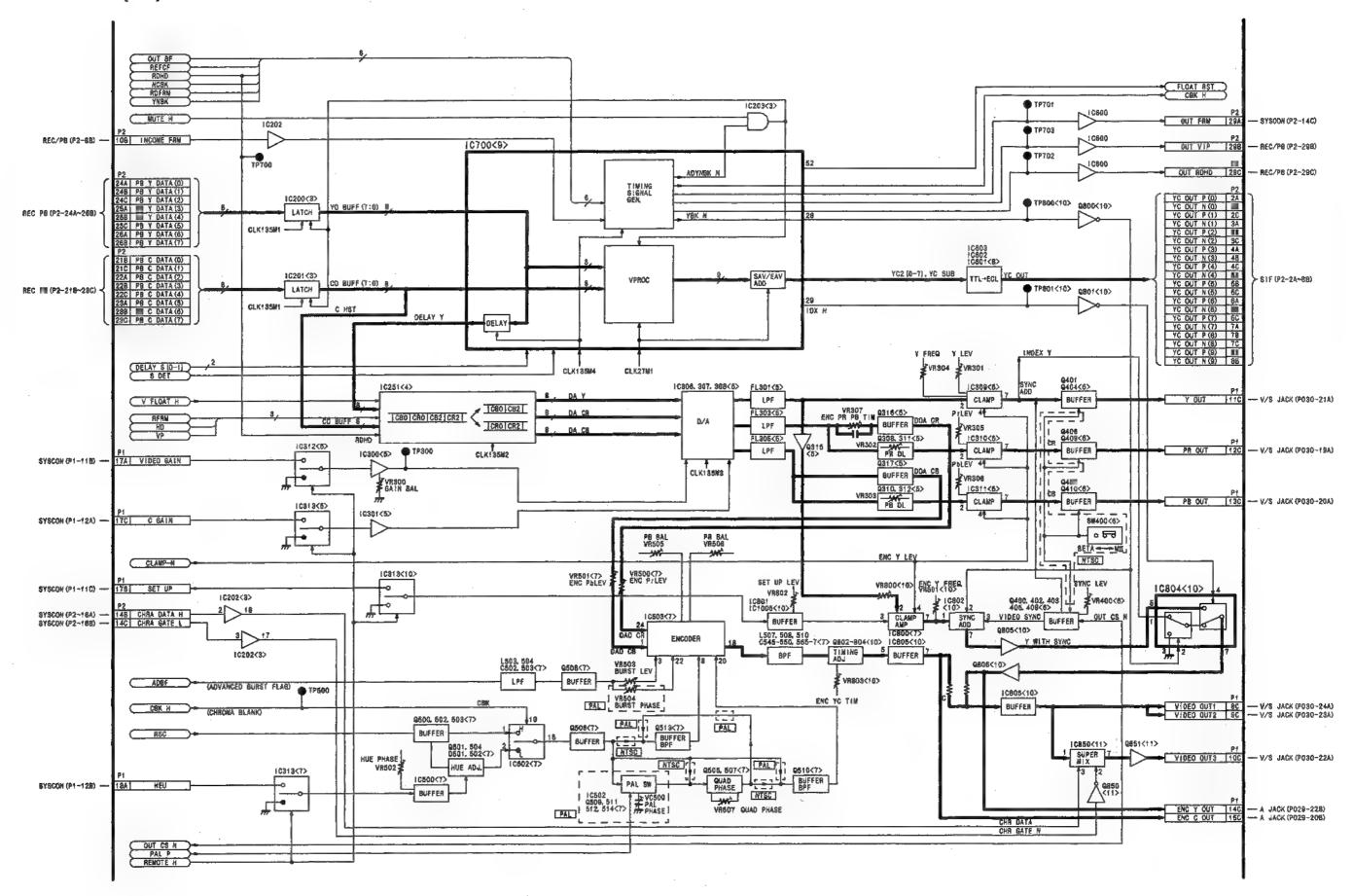
F2 SYSCON BLOCK DIAGRAM



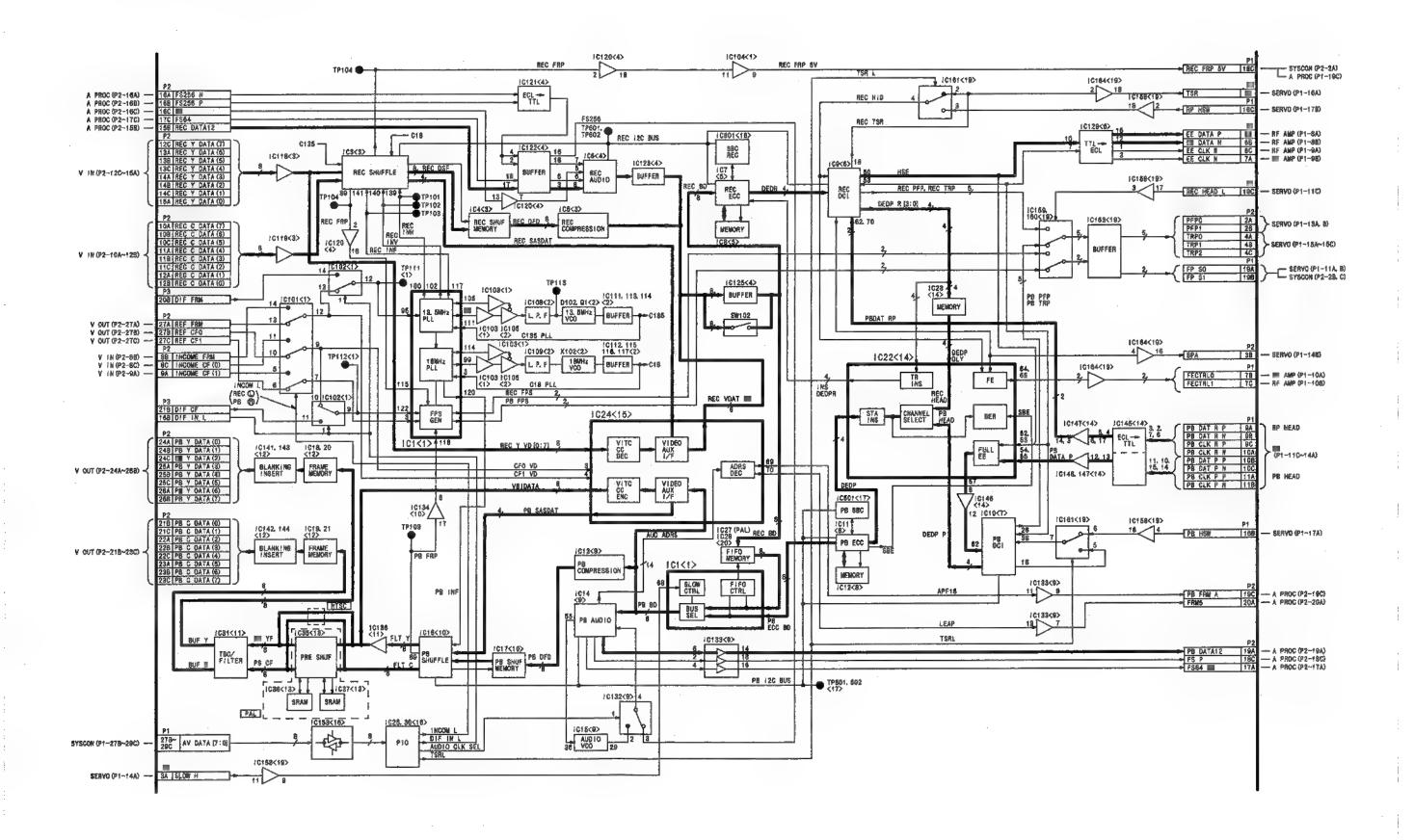
F4 V OUT (1/2) BLOCK DIAGRAM



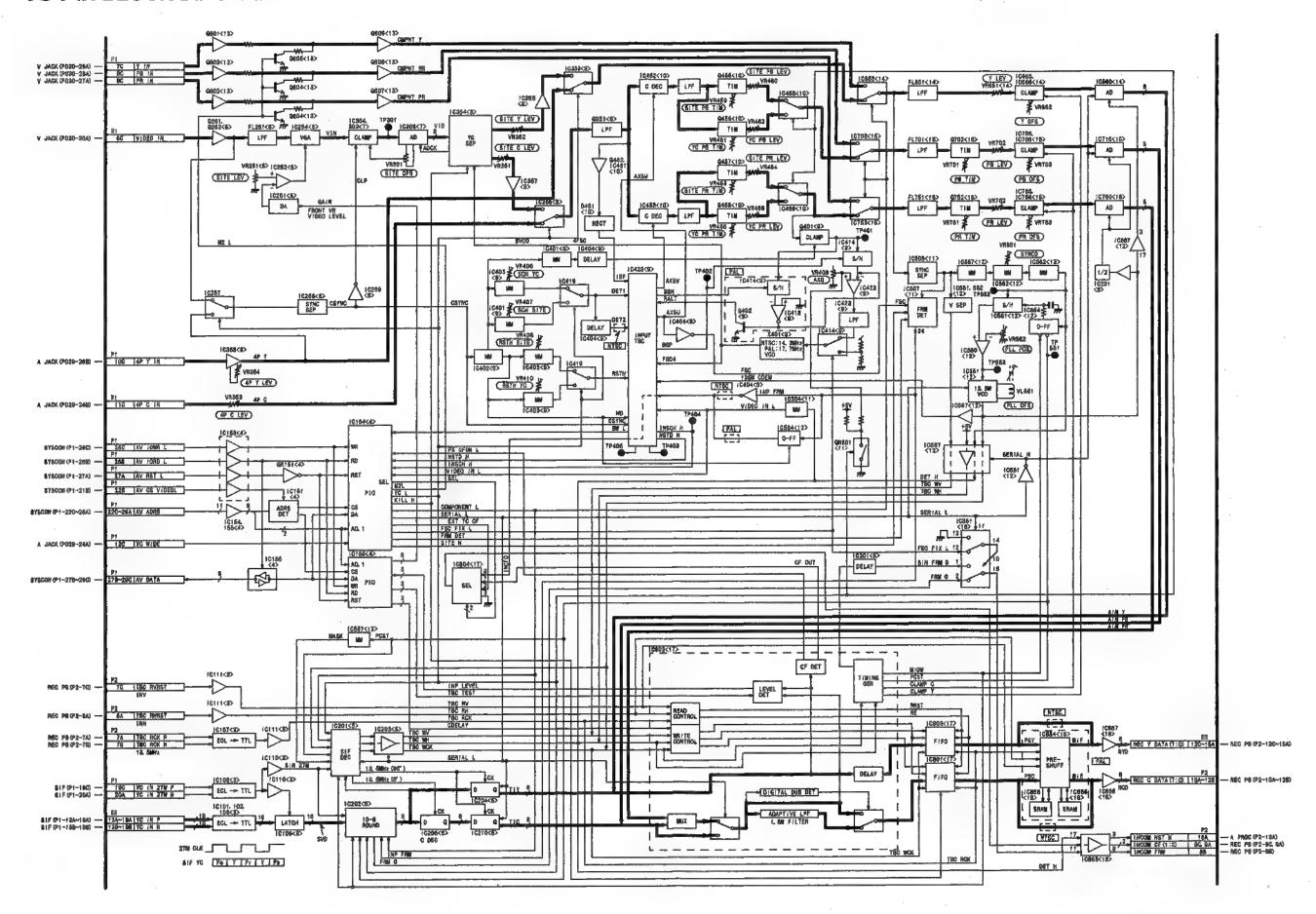
F4 V OUT (2/2) BLOCK DIAGRAM



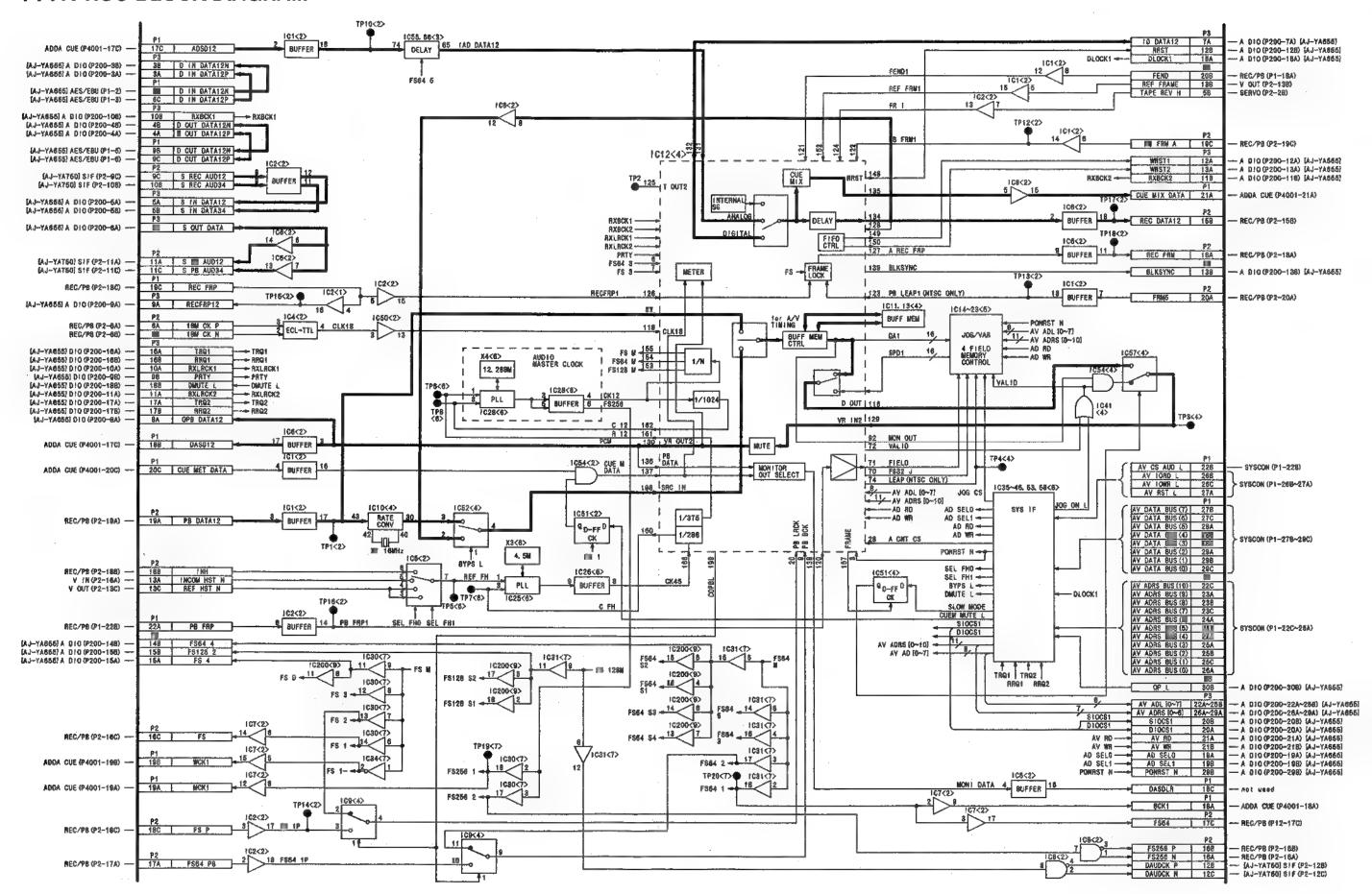
F5 REC PB BLOCK DIAGRAM



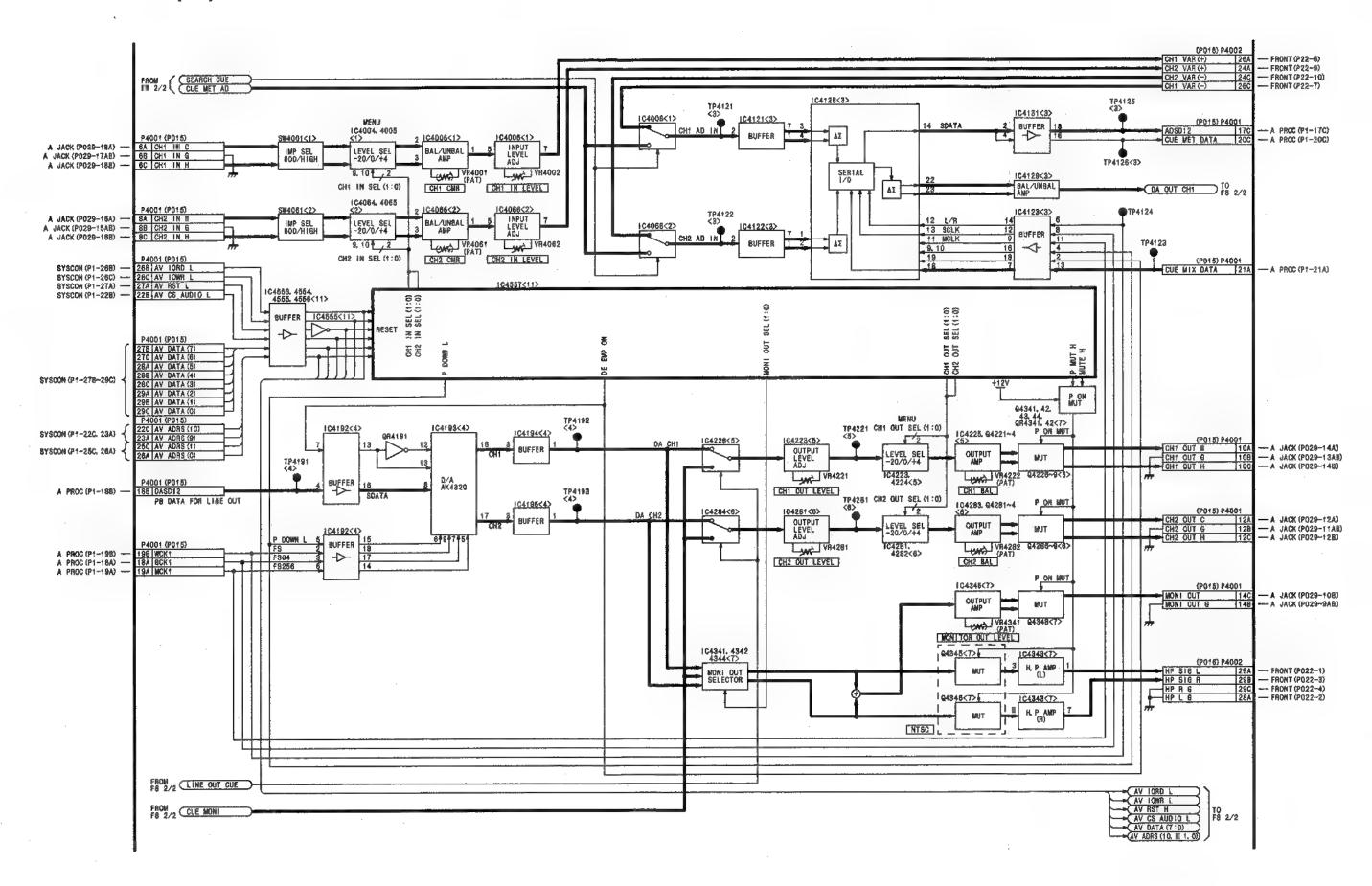
F6 V IN BLOCK DIAGRAM



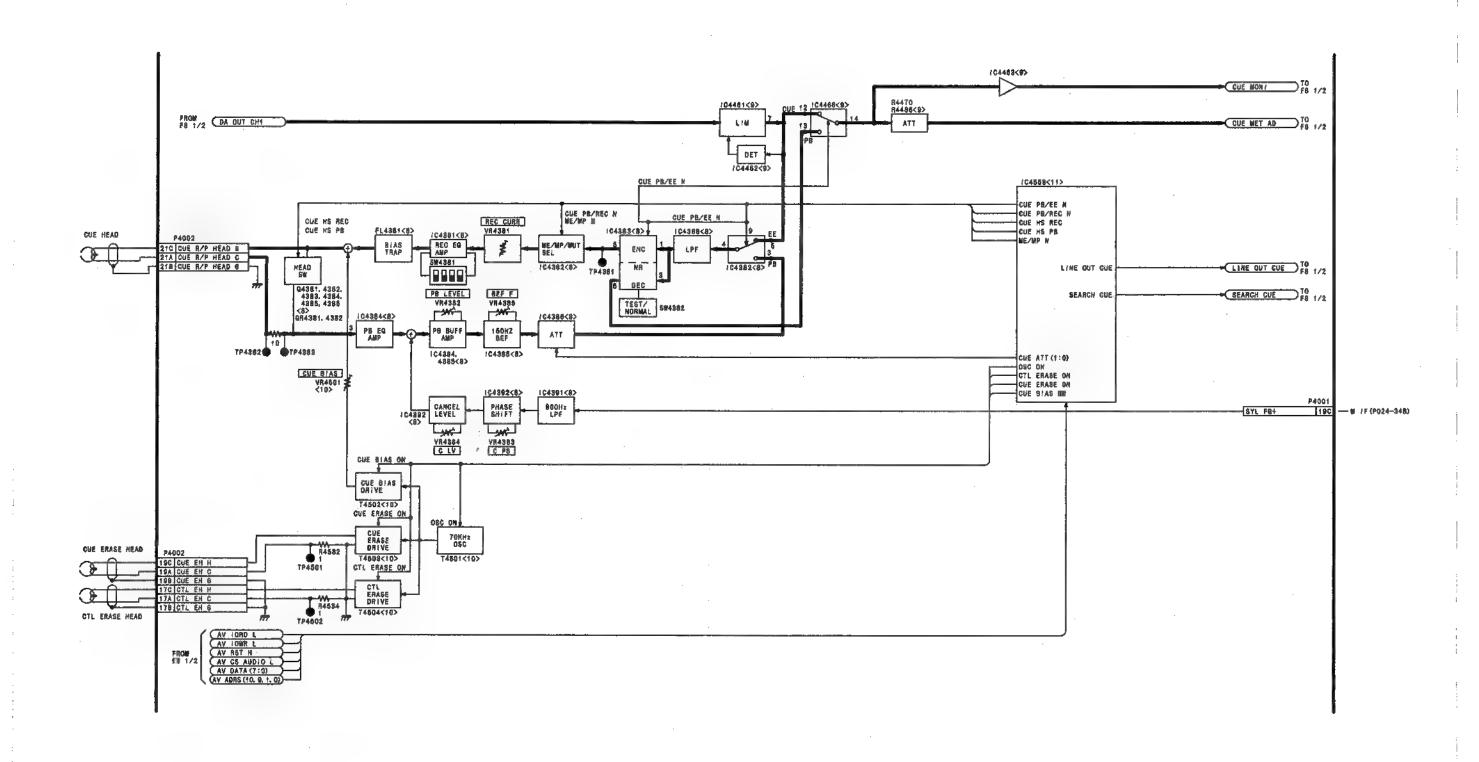
F7 A PROC BLOCK DIAGRAM



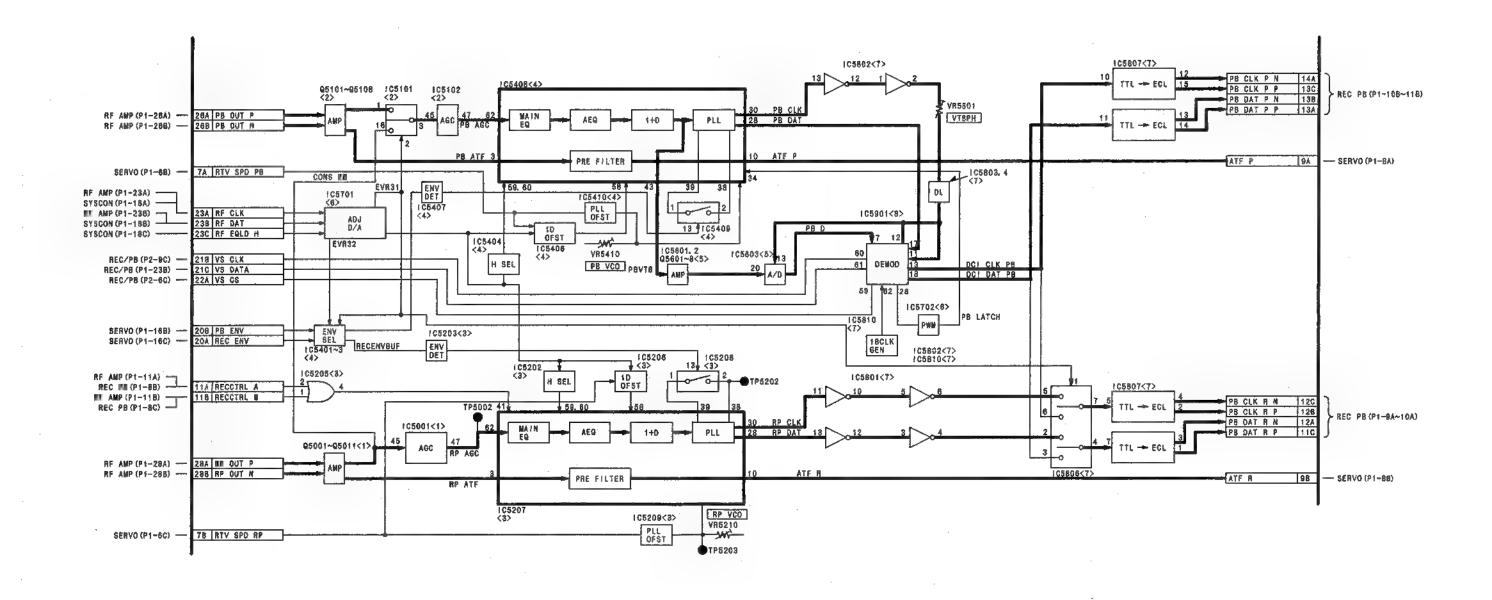
F8 ADDA CUE (1/2) BLOCK DIAGRAM



F8 ADDA CUE (2/2) BLOCK DIAGRAM



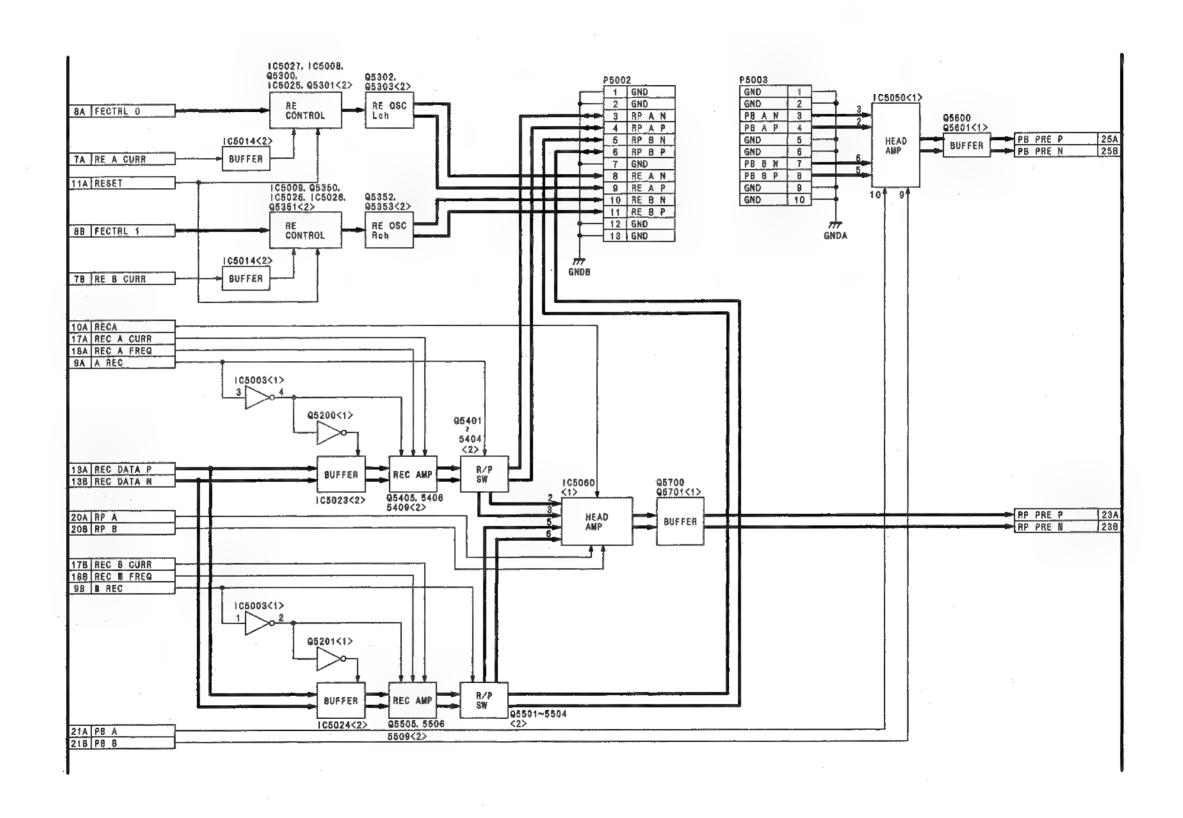
H3 EQ BLOCK DIAGRAM



H4 RF AMP BLOCK DIAGRAM Q6700, 6701, 5702, 5703, 5704<4> Q5705, 5706. L5705, 5706. C5724<4> IC5054, Q5714 Q5715, 5716<4> L5703, Q5717. 5718<4> C5714<4> 5707, 5708<4> P5003<4> --- EQ (P1-28A) 1 RP PRE P **ANTAGONISM** TRAP (34MHz) RP OUT P 28A 2ND AMP BUFFER 2 RP PRE N 3 GND RESONANCE (70kHz) CONTROL --- EQ (P1-28B) AP OUT N 288 TP5019 1C5055. TP5020<4> 95719. 5720<4> RP MAG **RP PHASE** RP A ENV DET FROM HEAD BUFFER REC ENV AP B BUFFER 20A - SERVO (P1~16C) VC5701 PB ENV 20B --- SERVO (P1-16B) VC5700 TP5015 Q5600, 5601, 5602, 5603, 5604<3> Q5605, 5606, 5607, 5608<3> L5603. C5614<3> L5605, 5606. IC5051, Q5614 Q5615, 5616<3> **<3>** Q5617. 5618<3> C5624<3> P5002<3> 1 PB PRE P ANTAGON I SM --- EQ (P1-26A) TRAP (34MHz) PB OUT P 26A 2ND AMP BUFFER RESONANCE 2 PB PRE N (70kHz) CONTROL PB OUT N 26B --- EQ (P1-268) 3 GND Q5619, 105052, TP5018<3> 5620<3> 5053<3> ENV BUFFER 1C5006<1> 105002<1> 11A REC CTRL A 11B REC CTRL 1 A REC A SW 12A **BUFFER** 120 TO HEAD BUFFER B REC 12B IC5007<12 RESET 13B SW MUE MAG Q5002. PB PHASE Q5001<1> PB A # P8 B RESET VC5600 VC5601 RECDATA P 14B RECDATA N 140 SYSCON (P1-18A) -105013<2> 105017<2> EQ (P1-23A) SYSCON (PI-188) 105016<2> TO MEAD BUFFER 23A RF CLK 15 RP MAG RP A 178 BUFFER EQ (P1-23B) 23B RF DAT 46 RP 8 170 SYSCON (P1-19A) -24A RF HALD H 188 PB A PB B 13 18C IC5016<2> 18 RP PHASE 1.9 SW5008<1> BUFFER REC B 12 10 D/A 11 CONVERTER 10 1C5014<2> 10. 11 RP A 39 105009<2> 42 105008<2> 43 IC5014<2> 7 PB MAG BUFFER 22 23 105014<2> 13 105008<2> TP5011 PB PHASE 2 201 BUFFER 12 RE B VR5014 105015<2> -9**M**(10, 11 1C5005 Q6006 -344 VR5013 RE A REC A CURR 158 REC B CURR 15C 105005 PEC A FREQ 168 Q5007 HEAD BUFFER REC # FREQ 16C RE A CURR 7B ●TP5009 RE B CURR 7C 105020<1>. **▲ TP6010** 105020<1>14 BA EE DATA P REC PB (P1-6A) -REC PB (P1-6B) 88 EE DATA N LATCH & 15 12 CW GEN 14 13 10 BUFFER BUFFER REC PB (P1-6C) -9A EE CLK P 9B EE CLK N REC PB (P1-7A) TP5001 REC PB (P1-16C) IC5021 105005<1> SERVO (P1-17B) 5022<1> 13 RP A 19A RP HSW EQ (P1-19A) REC PB (P1-168) 19B PB HSW 105003<1> SERVO (P1-17A) — EQ (P1-19B) 19C RE HSW AP B TP5002 1C5005<1> SERVO (P1-17C) ----TP5003<1> 10 PB A 105003<1> P8 8

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HEAD BUFFER BLOCK DIAGRAM



SCHEMATIC DIAGRAMS

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IMPORTANT SAFETY NOTICE

COMPONENTS IDENTIFIED WITH THE MARK \triangle HAVE THE SPECIAL CHARACTERISTICS FOR SAFETY.
WHEN REPLACING ANY OF THESE COMPONENTS, USE ONLY THE SAME TYPE.

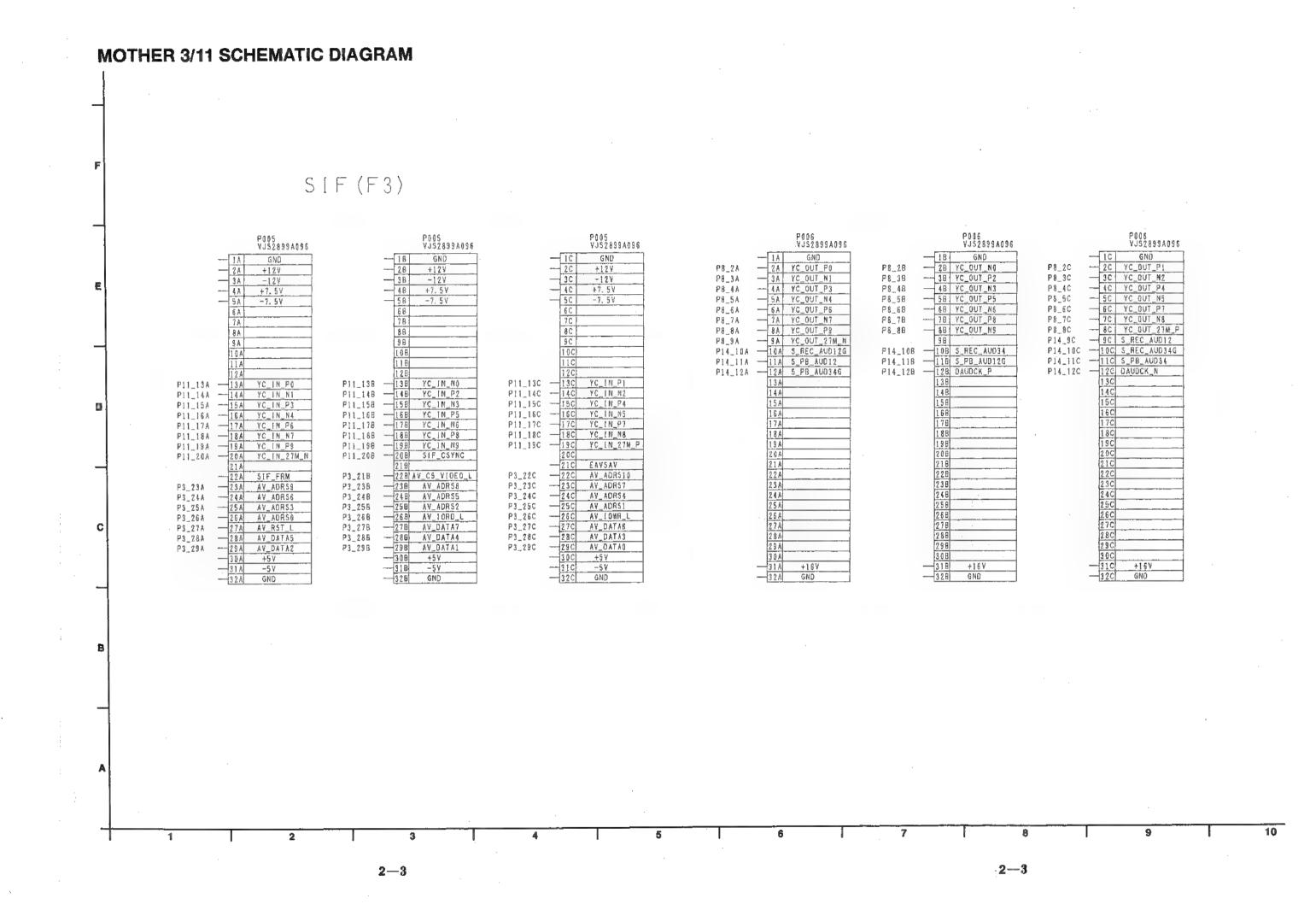
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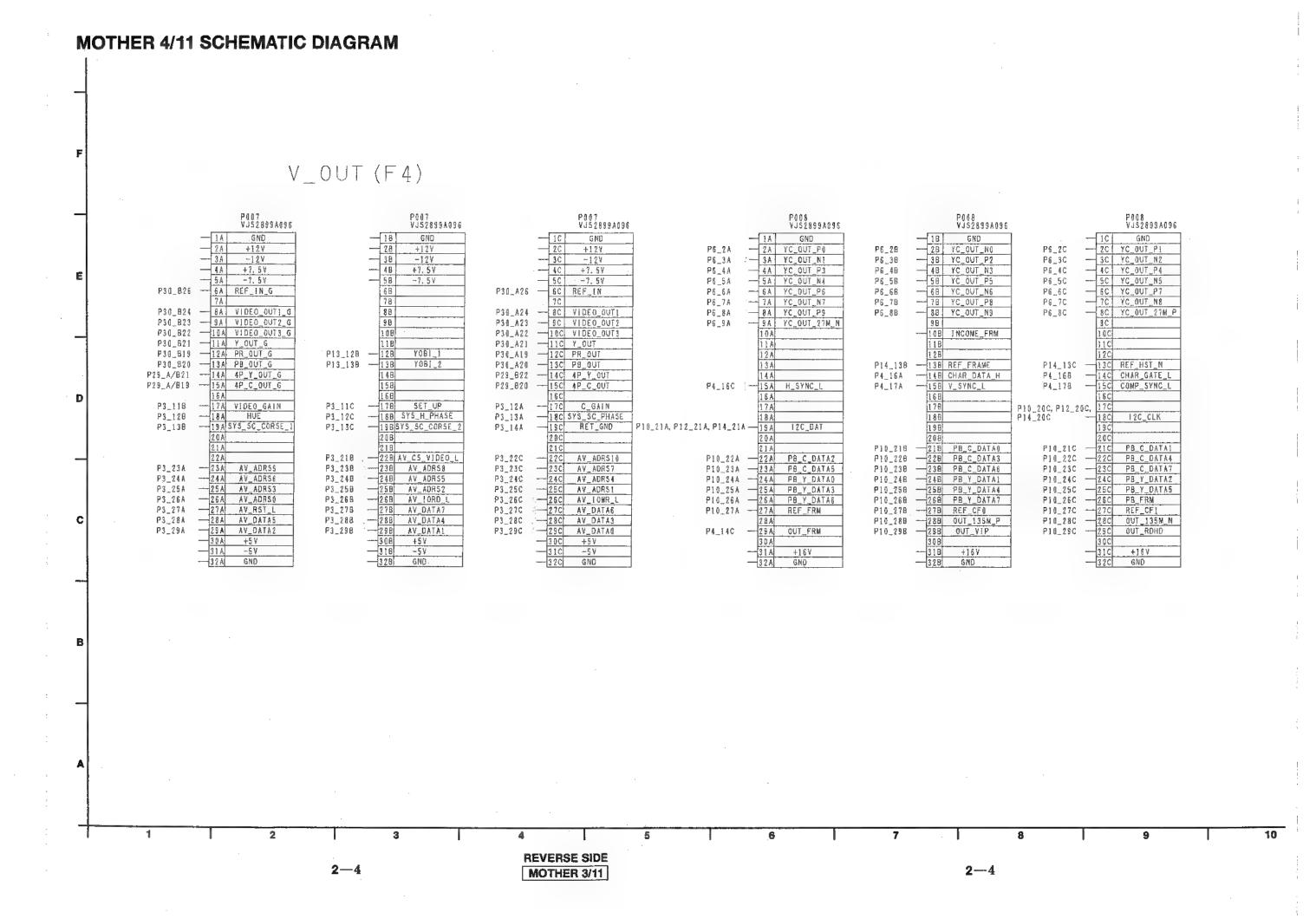
DO NOT USE THE PART NUMBER SHOWN ON THIS DRAWING FOR ORDERING.
THE CORRECT PART NUMBER IS SHOWN IN THE PARTS LISTS.
AND MAY BE SLIGHTLY DIFFERENT OR AMENDED SINCE THIS DRAWING WAS PREPARED.

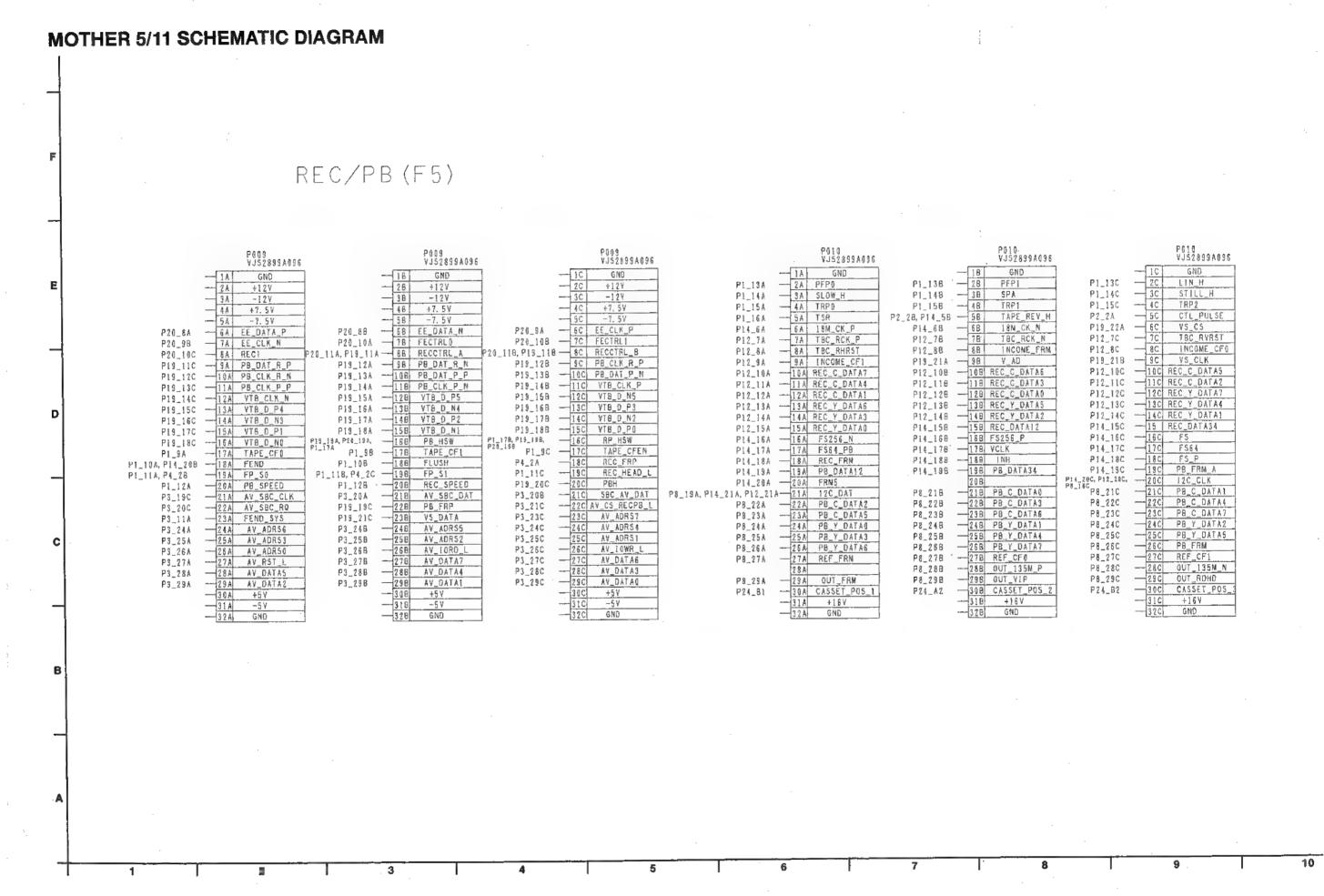
MOTHER 1/11 SCHEMATIC DIAGRAM SERVO (F1)

			*.			P002
	9001. VJS2898096	P001 VJ\$2899A096	P001 VJ52899A096	P002 VJS2899A096	P082 VJS2889A096	A7258334036.
		-	- IC GND	— IA GNO	— IBI GND	- IC GND
E	1A GND	18 GND - 28 +12V	- 2C +12V	P4_11C, P10_5C - 2A CTL_PULSE	P4_12A, P10_5B- 2B TAPE_REV_H	P4_12B - 2C FR_PULSE
	- 3A -12V	3B -12V	- 3C -12V	P4_12C - 3A T_RL_REV_L	P4_13A - 3B CAP_FG1_SYS	P4_13B — 3C S_RL_FG_SYS
	- 4A +7.5Y	4B +7.5V	- 4C +7.5V	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	P4_14A, P24_B17 4B CYLH1-	924_A17 - 4C CYLH1+
	— 5A -7, 5Y	──5B -7,5V	5C -7. 5V	P24_818 - 5A CYLH2-	924_A18 — 58 CYLH2+	924_B19 — 5C CYLH3-
	- 6A GND	P19_7A GB RTV_SPD_PB	P19_7B · GC RTV_SPD_RP	P24_A19 — 6A CYLH3+	923_30	P24_A1
_	- 7A GND	P19_8A - 7B REF_CLK	- 7C GND	P24_A15 — 7A TENSION	P24_815 — 78 TENS_GNO	P24_B39 — 8C CTL_GND
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	PS_4A :- 15A TRPO	P9_48 . — 158 TRP1	P9_4C 15C TRP2	P24_828 - 15A CAH_V-	P24_A29 - 15B CAH1+	P24_829 - 15C CAH1- P24_A31 - 16C CAH3+
	P8_5A — 16A TSR	P20_208 -16B PB_ENV	P20_20A 16C REC_ENV	P24_A30 16A CAH2+	P24_B30 — 168 CAH2- P24_A32 — 178 CAP FG VCC	P24_A31 — 16C CAH3+ P24_B32 — 17C CAP_FG_GND
	P9_168, P20_19B 17A PB_HSW	P20_19A, P8_16C, 17B RP_HSW	P20_19C 17C RE_HSW	P24_B31 — 17A CAH3- P24_A33 — 18A CAP_FG1_M	P24_A32 — 178 CAP_FG_VCC P24_B33 — 188 CAP_FG2_M	PZ4_A34 — 18C CYL_PG+
	P19_19B	P19_19A 18B	P13_14C — 19C AUDIO_JOG1	P24_B34 — 19A CYL_FG+	P24_A35 - 198 T_FG_V	P24_835 - 19C T_FG_GND
_	P23_29 1 19A	P13_148 — 19B AUDIO_JOGO	200	P24_A36 - Z0A T_FG1_M	P24_B36 - 20B T_FG2_M	P24_A37 - 20C 5_FG_V
	[20 A	P4_3A — 218 SYS_ABO	P4_3B / - 21C 5YS_AB1	P24_B37 - 21A S_FG_GND	P24_A38 -21B S_FG1_M	P24_B38 - 21C S_FG2_M
	P4_3C 3—22A SYS_A82	P4_4A — 22B SYS_AB3	P4_4B 22C SYS_AB4	P4_A14 — 22A LOAD_ERR_SYS	P24_A38 — 22B CYL_PF_GND	22C
	P4_4C 23A SYS_AB5	P4_5A - 23B SYS_AB6	P4_5B - 23C SYS_AB7	23A	P23_19 - 23B TRM3	P23_17 - 23C TRM1 P23_18 - 24C TRM2
	P4_5CZ4A _SYS_A88	P4_6A 248 SYS_A89	P4_6B / 24C 5YS_AB10	[24 A]	P23_22 — 25B SRM3	P23_18 - 24C TRM2 P23_20 - 25C SRM1
·C	P4_6C - 25A SYS_080	P4_7A - 258 SYS_D81	P4_7B - 25C SYS_DB2	ZDA	25B 3Km3	P23_21 — 26C SRM2
	P4_7C - 26A SYS_DB3	P4_8A 26B SYS_DB4	P4_8B - 26C SYS_DB5 P4_9B - 27C SERVO_CS_L_	77 A	P23_25 -278 CAM3	P23_23 - 27C GAM1
	P4_8C	P4_9A - 27B SYS_DB7 P4_10A - 28B SYS_WR_L	P4_10B 28C SYS_BUSY_L	Z 8 A	288	P23_24 — Z8C CAM2
	P4_9C 4-28A SYS_RD_L P4_10C 29A SERVO_INT_L	P4_11A - 298 SERVO_RESET_L	P4_11B - 29C 5YS_SERVO_INT_L	29 A	P23_28 - 298 CYL_M3	P23_26 - 29C CYL_M1
	30A +5V	30B +5V	-30C +5V	J1 30A	30B	P23_27 -30C CYL_M2
_	31A -5V	318 -5V		LÂND -31A +16V	- 31B +16V	- 31C +16V
	- 32A GND	- 32B GND	32C GND	32A GND_M	3 Z B GND_M	- 32C GND_M

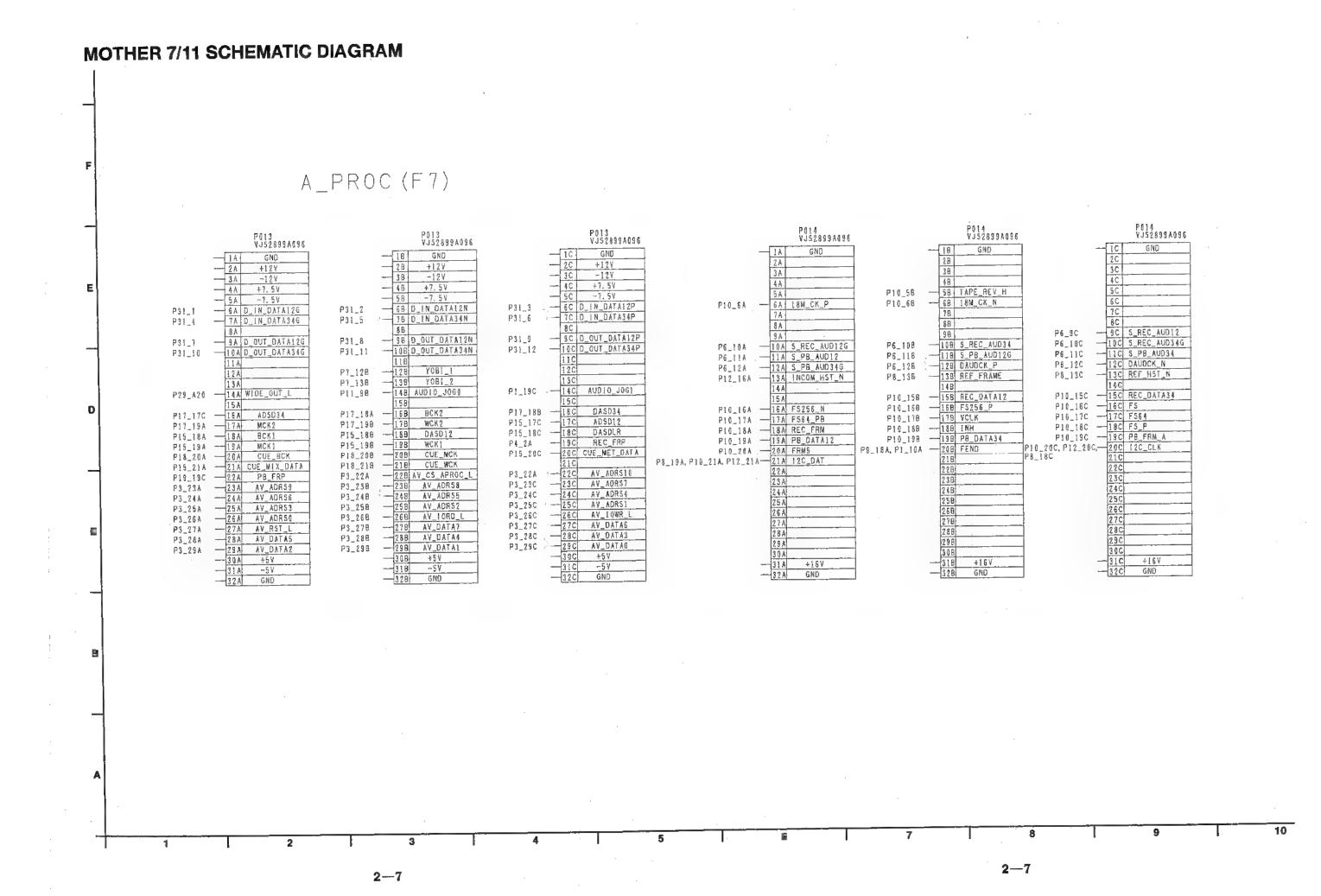
MOTHER 2/11 SCHEMATIC DIAGRAM SYSCON (F2) P004 VJ\$2899A096 P004 VJS2899A096 P004 VJ52899A096 P003 VJ52899A096 P003 VJS2899A096 P003 VJSZ899A096 GND GND GND GND GND P9_190, P1_11B P9_18C, P13_19C REC_FRP P9_19A, P1_11A -+127 +12Y +12Y - 3A SYS_ABO SY5_AB2 P1_21C - 3B SYS_AB1 P1_22A --12Y +7.5Y P1_21B 4C SYS_AB5 5C SYS_AB8 4B SYS_AB4 5B SYS_AB7 - 4A SYS_AB3 P1_23A P1_22C 47.5V +7. 5V P1_22B 5A SYS_AB6 6A SYS_AB9 P1_24A P1_23C P1_23B -7.5V -7.5V SYS_DB0 SYS_D83 - 6B SYS_AB10 - 78 SYS_DB2 P1_24C P1_25A TG_OUT_H P1_24B TC_1N_H TC_DUT_C - 68 TC_IN_C - 78 FAN_PULSE1 P29_B6 P29_A6 P1_25C P1_26A 8C SYS_DB6 9C SYS_RD_L 10C SERVO_INT_L 11C CTL_PULSE 12C T_RL_REV_L SYS_DB1 P29_A4 P25_1 P32_2 FAN_PULSE2 P1_25B 8A SYS_DB4 - 88 SYS_DBS P1_27A P30_B15 - 8B RECEIVE_B_10 P30_A13 - 9B RECEIVE_A_OUT P1_26C - 8A RECEIVE_A_IO P30_A14 8C TRANSMIT_A_[0 P1 26B P30 A15 -- 9B SERVO_CS_L P1_28A - 9C RECEIVE_B_OUT P1_270 P30_812 P1_27B SA TRANSMIT B 10 P30_B14 10B SYS_BUSY_L P1_29A P1_28C P1_28B P30_A12 --10A TRANSMIT_A_OU P2_2A P1_29C 118 SYS_SERVO_INT -11C SET_UP P1_29B 11A FEND_SYS 12A C_GAIN P9_23A P7_178 2B FR_PULSE P2_3A P2_2C -12C SYS_H_PHASE P7_18B P7_17C 13C T_RL_FG_SYS 13B S_RL_FG_SYS P2_4A 13C SYS_SC_CORSE_ 14C P_SERIAL_OUT P2_3C 13A SYS_SC_PHASE P7_198 P7_180 14B CASSET_MID_H 14C GUT_FRM 15C BACKUP P8_29A P24_3 P7_19C P3@_A5 14A RET GND D 15C 232C_SPARE 16C RXD 15B BACKUP P30_84 P10_85, P22_2 - 15A SERIAL_CLK - 16C H_SYNC_L - 17C CASSET_INR_F P8 15A P8..14B - 16A CHAR_DATA_H P8_14C - 16B CHAR_GATE_L P30_A2 P30_A3 P8_15C 17B COMP_SYNC_L P24_A4 17A TXD P30_B1 17C RTS P30_B2 P24_A5 -18B CASSETT H P24_B5 -18C DEW_ON_H P19_23A, P20_23A — 18A RF_CLK P20_24A — 19A RF_HALD_H P19_23C 18C RF_EQLD_H 19C DEW_GND ZDC PINCH_ON 19B CASSET_DOWN_H 20B MECHA_POS2_L P24_B6 P24_A7 PZ4_A6 -19A CASSET2_H P9_21A - 19C AV_SBC_CLK P20_24A -P23_9 20A CASSET_UP_L 21A MECHA_POS1_L P24_B7 P24_A8 20A AV_SEC_DAT P9_22A 20C AV_SBC_RQ P24_A9 - Z18 SAFETY_TAB PINCH_OFF P23_10 21C AV_CS_RECPB_L P24_B8 P9_22C 22C S_BRAKE_ON -Z3C S_BRAKE_OFF -Z4C T_BRAKE_ON P23_11 22A AV_CS_APROC_L P7_228 P24_A10 --228 REEL_POSZ_H 228 AV_CS_AUDIO_L 238 AV_ADRS8 - 22C AV_ADRS10 P24_A11 - 23B TAPE_SENSOR_OF P23_12 23A AV_ADRS9 24A AV_ADRS6 P17_22B, P18_22B, P15_22B AV_ADRS7 P24_A12 — Z4B F_LOAD_IN P24_B12 — Z5B F_LOAD_OUT P23_13 - 248 AV_ADRS5 -- 24C AV_ADRS4 Z5C T_BRAKE_OFF 26C M_STOPPER_OFF 27C M_STOPPER_ON P23_14 P24_B12 AV_ADRS1 25A AV_ADR53 26A AV_ADR50 AV_ADRS2 P23_15 AV_IOWR_L AV_LORD_L COMMON P23_16 COMMON 27A R_STAND_L2S AV DATAR P23_7 27A AV_RST_L AV_DATA7 28C FR_SERIOB 29C FR_SERIOB 30C COMMON P23 8 -- 28A R_STAND_S2L P21_2 AV DATAS 28A AV_DATAS AV_DATA4 29B FR_SERIIA P21_4 29A AV_DATA2 AV_DATAI AV_DATA0 +5 V -5 V +5 V 30A +5V +16V +167 +16 V -5¥ GND GND GND GND GND 2 **REVERSE SIDE** 2 - 22-2 MOTHER 1/11



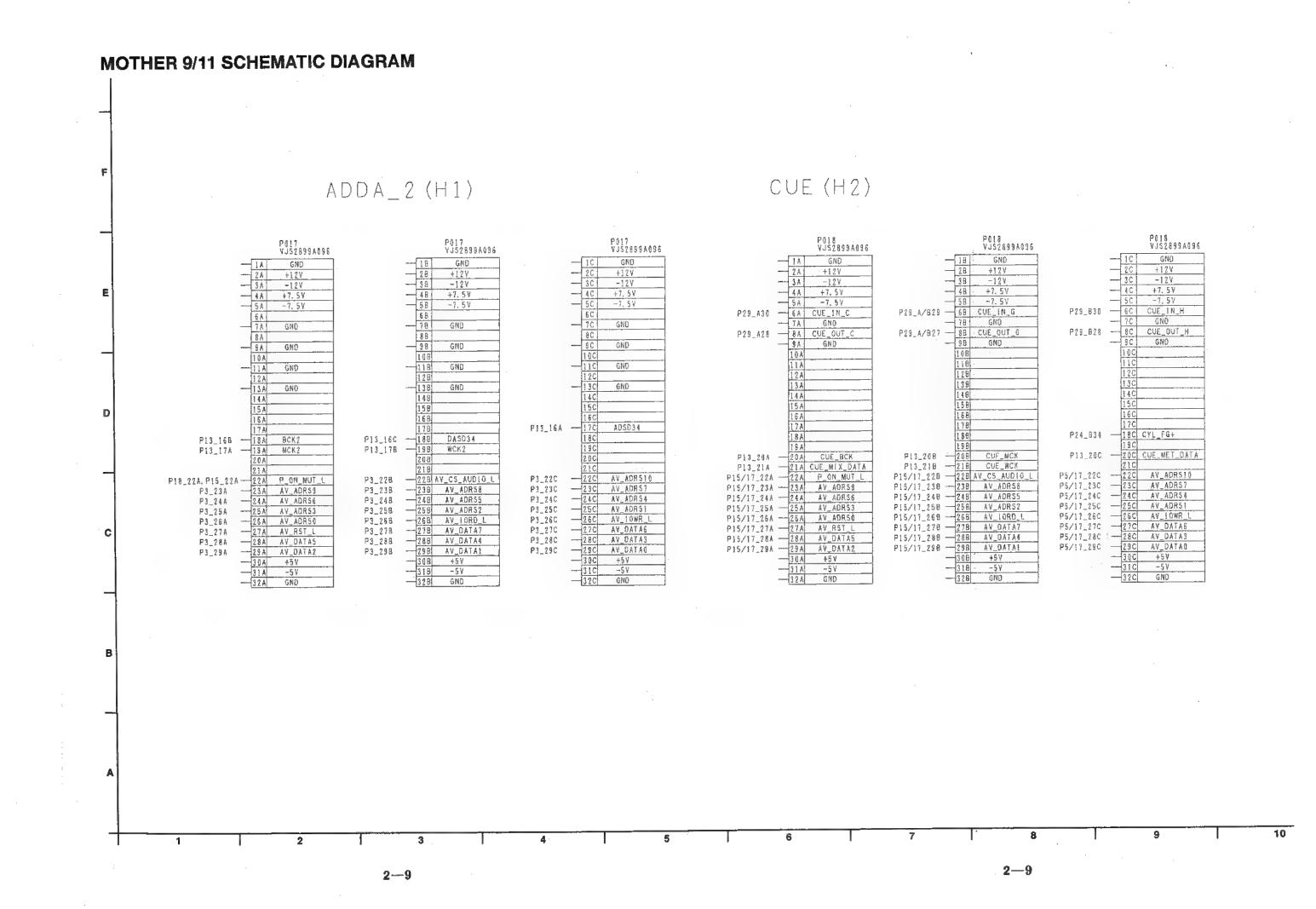




	MOTHER 6/11 SCHEMATIC	DIAGRAM				· · · · · · · · · · · · · · · · · · ·	
·	F						
	VIN (F6>					
	P011	POIL VJS2839A096	P011 VJS2899A096	P012		P012 VJS289SA096	
	A					. 9	10
:	1 1 2	26	REVERSE SIDE MOTHER 5/11	1 * 1	2—6	-	

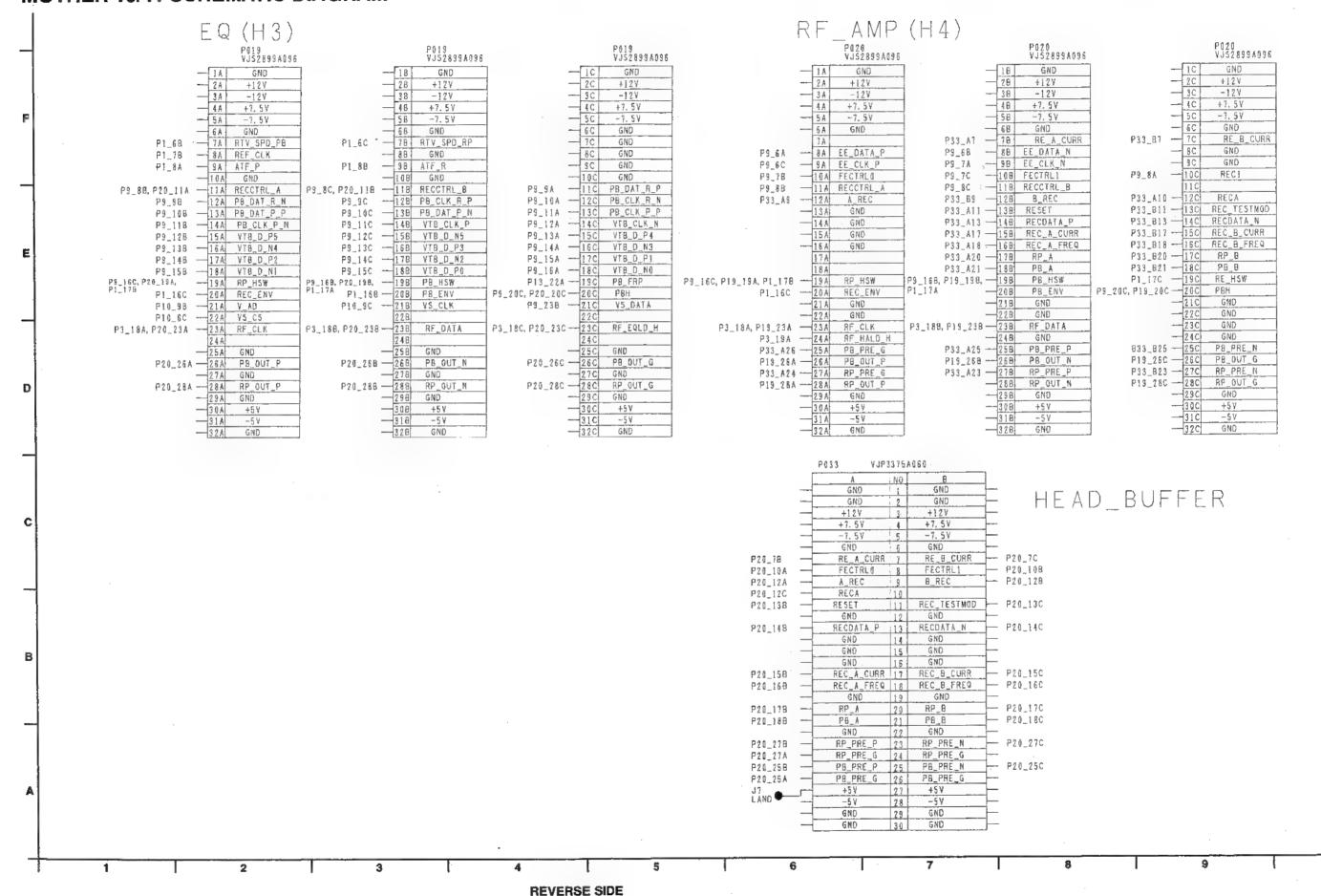


MOTHER 8/11 SCHEMATIC DIAGRAM ADDA_1 (F8) P016 VJ52899A096 P016 VJ52899A096 P015 VJS2889A096 P016 VJ52899A096 P015 VJS2899A096 P015 VJ\$2899A096 GND GND GND GND GND GND 28 38 48 58 68 78 +124 +127 -12V -12V -12Y +7. 5 V +7.57 +7.54 -7.5V -7.5Y -7, 51 PZ9_A/B17 - 6B CHI_IN_6 829_818 - 6C CH1_1N_H P29_A18 -- 6A CH1_IN_C GND GND - 8A CH2_IN_C P29_A/B15 - 8B CH2_IN_G P29_A16 GND - 9B GND GND P29_A/B13 - 108 CH1_OUT_G - 10C CH1_OUT_H 10A CH1_OUT_C P29_A14 -11C GND 12C CH2_OUT_H 118 GND P29_A/B11 - 12B CH2_OUT_G - 13B GND - 12A CH2_GUT_C P29_A12 13C GND -14C L_MONI_OUT_H -15C R_MONI_OUT_H 13A GND P29_A/B9 — 14B L_MONI_OUT_G P29_A/B7 — 15B R_MONI_GUT_G B29_810 P29_A10 -14A L_MONI_OUT_C B29_B8 P29_A8 15A R_MONI_OUT_C 16C GND 17C ADSD12 18C DASDLR 19C CYL_FG+ 20C CUE_MET_DATA GND 168 GND GND - 17C CTL_EH_H 17B CTL_EH_G CTL_EH_C GND P36_9 P13_17C P36_7 18B DASD12 P13...18C P13_18A - 18A BCX1 P13_18B CUE_EH_G 19C CUE_EH_H P36_6 P24_834 CUE_EH_C P13_19B P13_19A -- 19A MCK1 - 19B WCK1 OB GND 1 B CUE_R/P_HEAD_G 21C CUE_R/P_HEAD_F P36_1 ZIACUE_R/P_HEAD_C GND GND GND P3_228 - 22B AV_CS_AUDIO_L P3_23B - 21B AY_ADRS8 GND GND GND 24B CH2_VAR (G) P22_10, P34_10, P35_10 24C CH2_VAR (-) 24A CH2_VAR (+) P22_9, P34_9, P35_9 GND 25A GND GND P22_8, P34_8, P35_8 - 26B CH1_VAR (G) P22_7, P34_7, P35_7 26C CH1_VAR (-) 26A CH1_VAR (+) P3_26A P22_2, P34_2, P35_2 — 28A P22_1, P34_1, P35_1 — 29A 278 GND P22_5, P34_5, P35_5 — 288 HP_MUTE_H P22_3, P34_3, P34_3 — 298 HP_R GND C P3_27A HP_L_G HP_L P3_28A P22_4, P34_4, P35_4 ___29C HP_R_G 29A AV_DATA2 30A +5Y P3_29B — 29B AV DATA1 — 30B +5V P3_29A +5 Y -5 V **REVERSE SIDE** 2-8 2-8 MOTHER 7/11



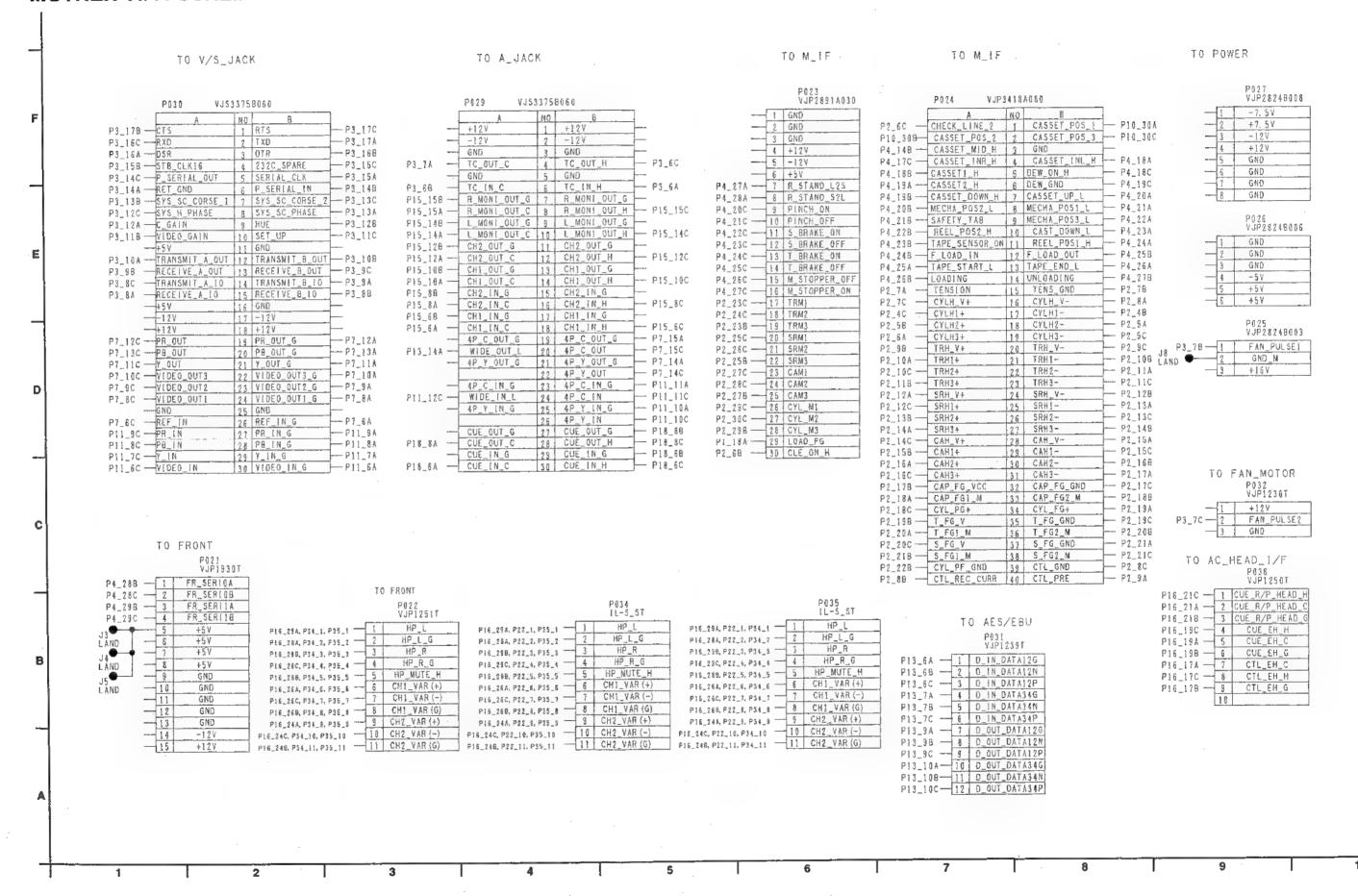
MOTHER 10/11 SCHEMATIC DIAGRAM

2 - 10

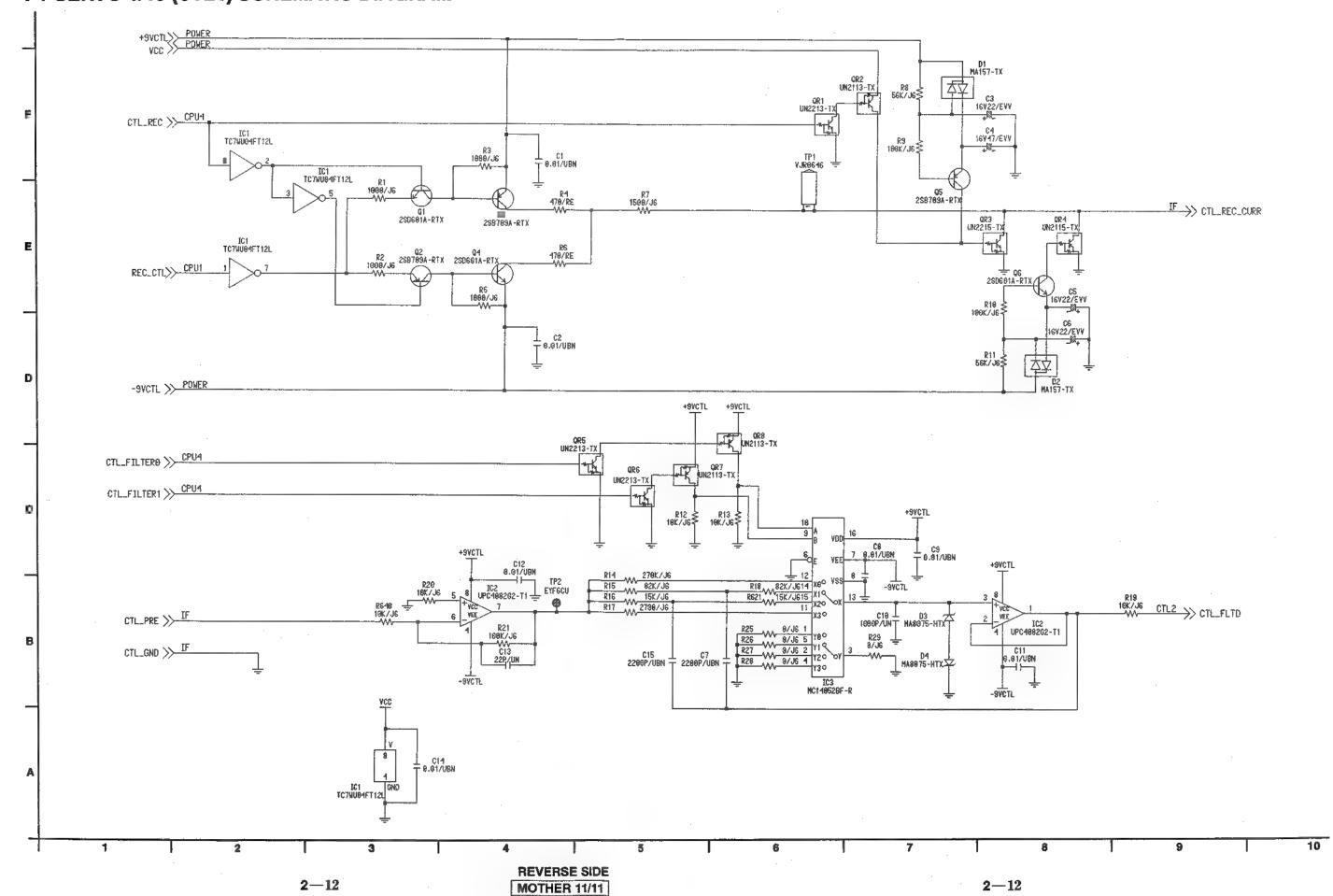


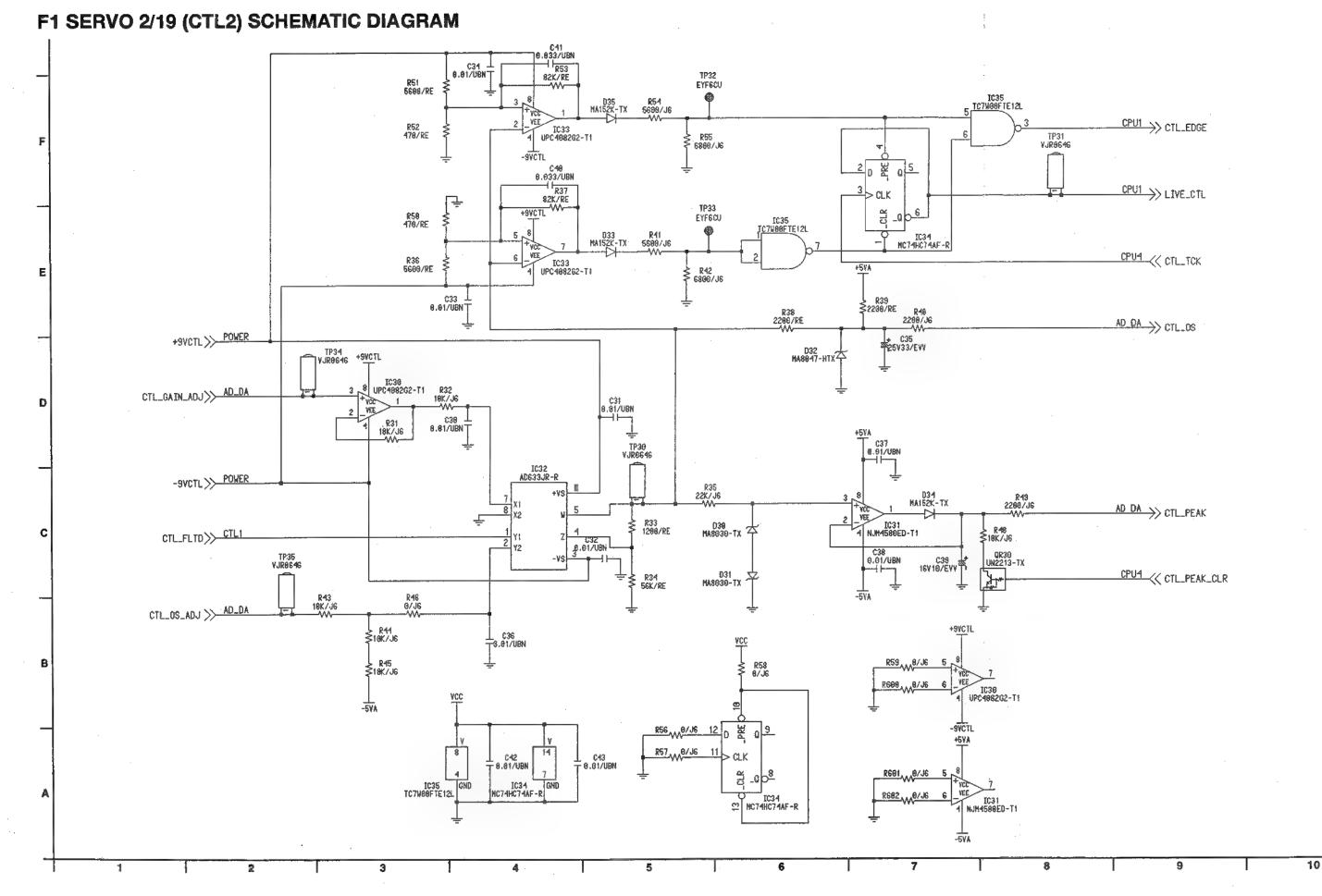
MOTHER 9/11

MOTHER 11/11 SCHEMATIC DIAGRAM

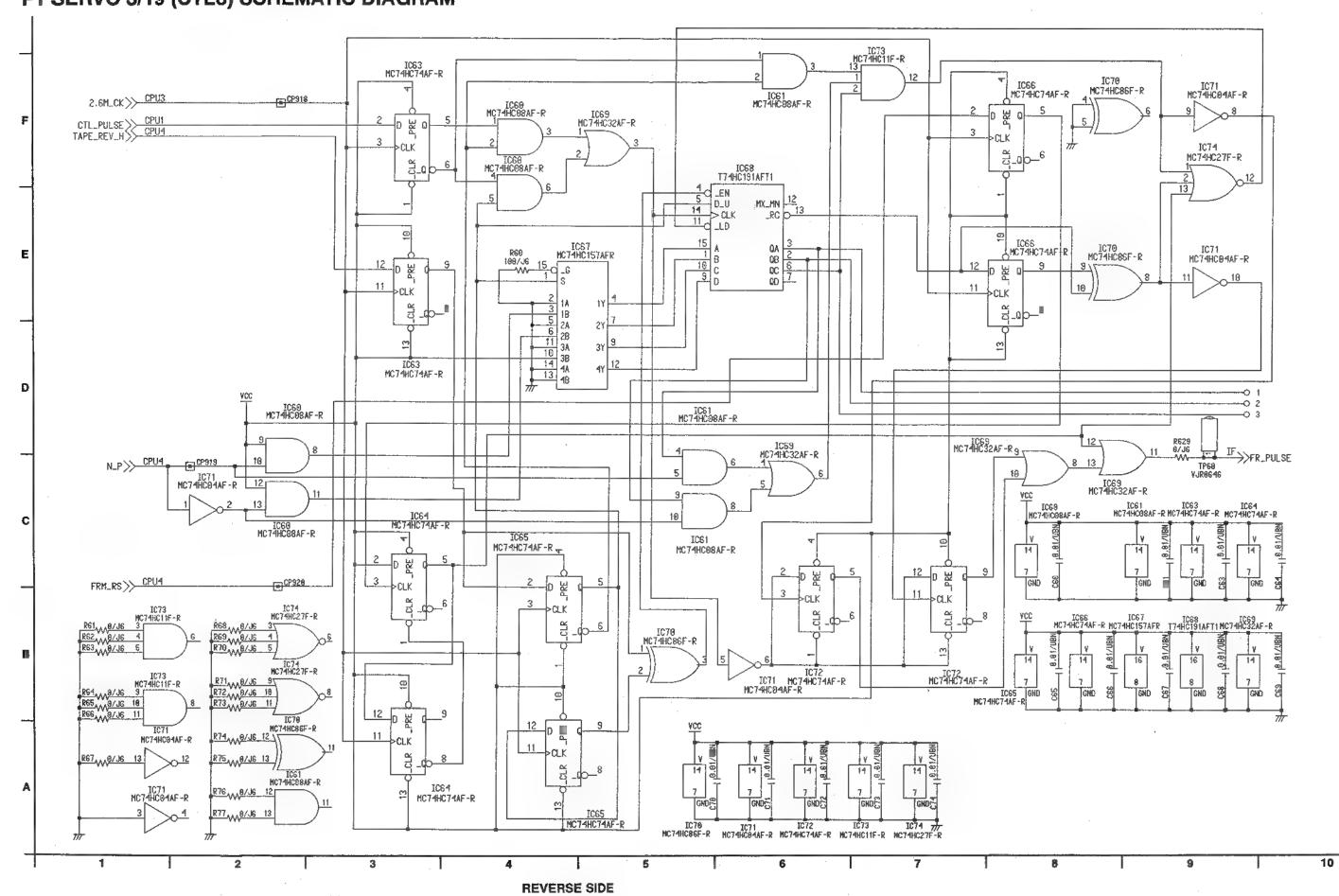


F1 SERVO 1/19 (CTL1) SCHEMATIC DIAGRAM

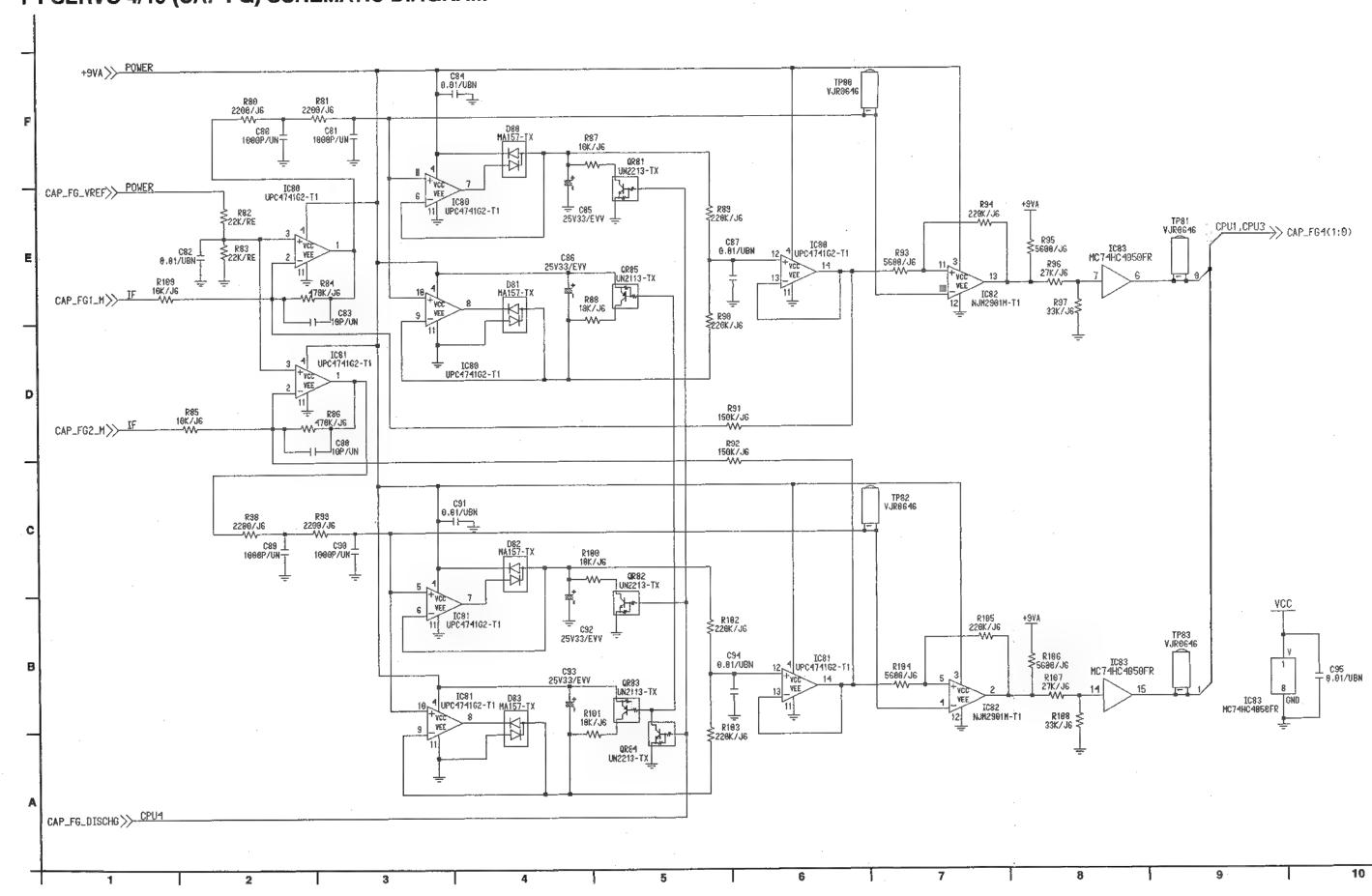




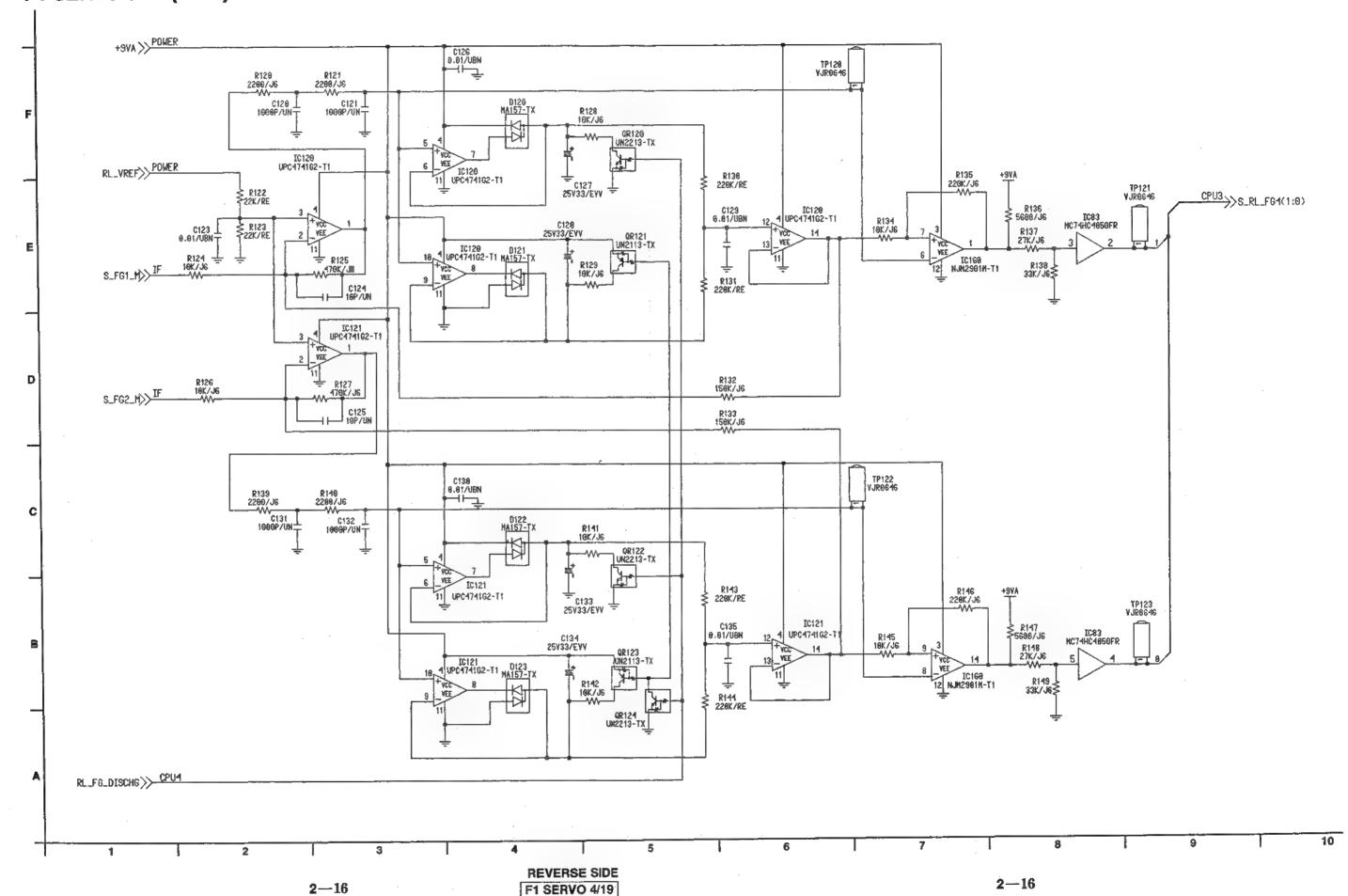
F1 SERVO 3/19 (CTL3) SCHEMATIC DIAGRAM



F1 SERVO 4/19 (CAP FG) SCHEMATIC DIAGRAM



F1 SERVO 5/19 (S FG) SCHEMATIC DIAGRAM



F1 SERVO 6/19 (T FG) SCHEMATIC DIAGRAM +9VA>>> POWER C169 6,91/UBN C165 9.91/UBN TP160 VJR0646 R161 2200/J6 IC168 12 NJM2981M-T1 12 NJM2991M-T1 C160 1 12R168 UN2213-TX RL_VREF>> POHER IC161 11 UPC474162-T1 IC161 UPC4741G2-T1 25V33/EVV R178 226K/RE TP161 VJR0646 IC161 12_4 UPC4741G2-T1 C168 0.61/UBN VCC IC83 NC74HC4058FR CPU3 >>> T_RL_FG4(1:0) R172 19K/J6 C167 25V33/EVV IC161 D161 HO 4 UPC474162-T1 MA157-TX WEE 8 QR161 UN2113-TX T_FG1_M>> IF R164 + 19K/J6 W R169 18K/J6 R176 \$ 33K/J6\$ R171 228K/RE IC162 UPC474162-T VEE. T_FG2_M>> IF RIGG 16K/JG R178 159K/J6 0.81/UBM 7P162 YJR9646 C171 1 C178 1990P/UNT QR162 UN2213-TX IC152 11 UPC4741G2-T1 R186 228K/J6 ± C173 25V33/EVV = R183 228K/RE 12 4 UPC4741G2-T1 R187 \$5699/J6 IC83 MC74HC4050FR ₹185 18K/J6 --VV--C174 25V33/EVV QR163 UN2113-TX IC162 18 4 UPC474162-T1 HA157-TX IC82 NJM2901M-T1 R184 228K/RE QR164 UN2213-TX RL_FG_DISCHG>> CPU4

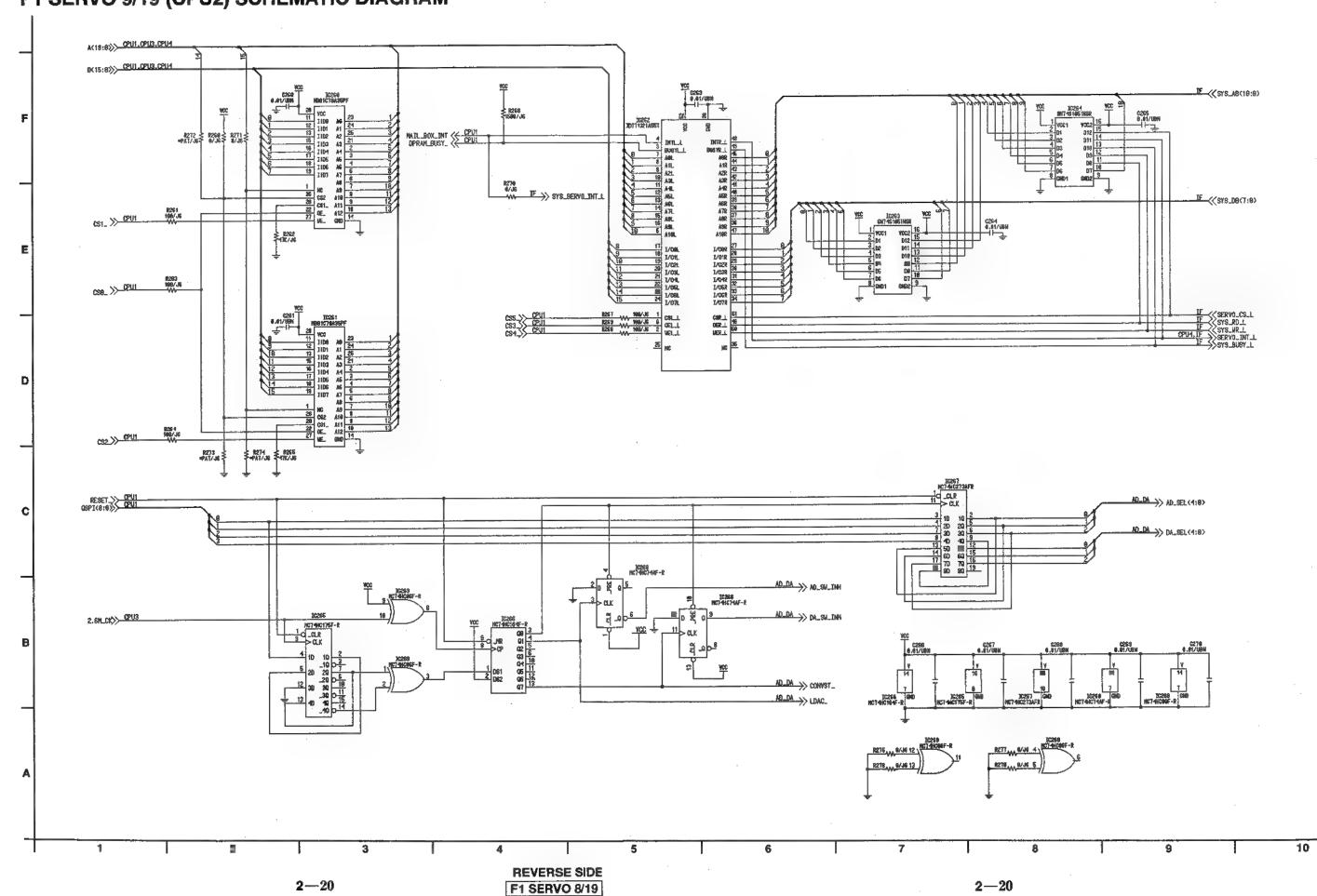
2-17

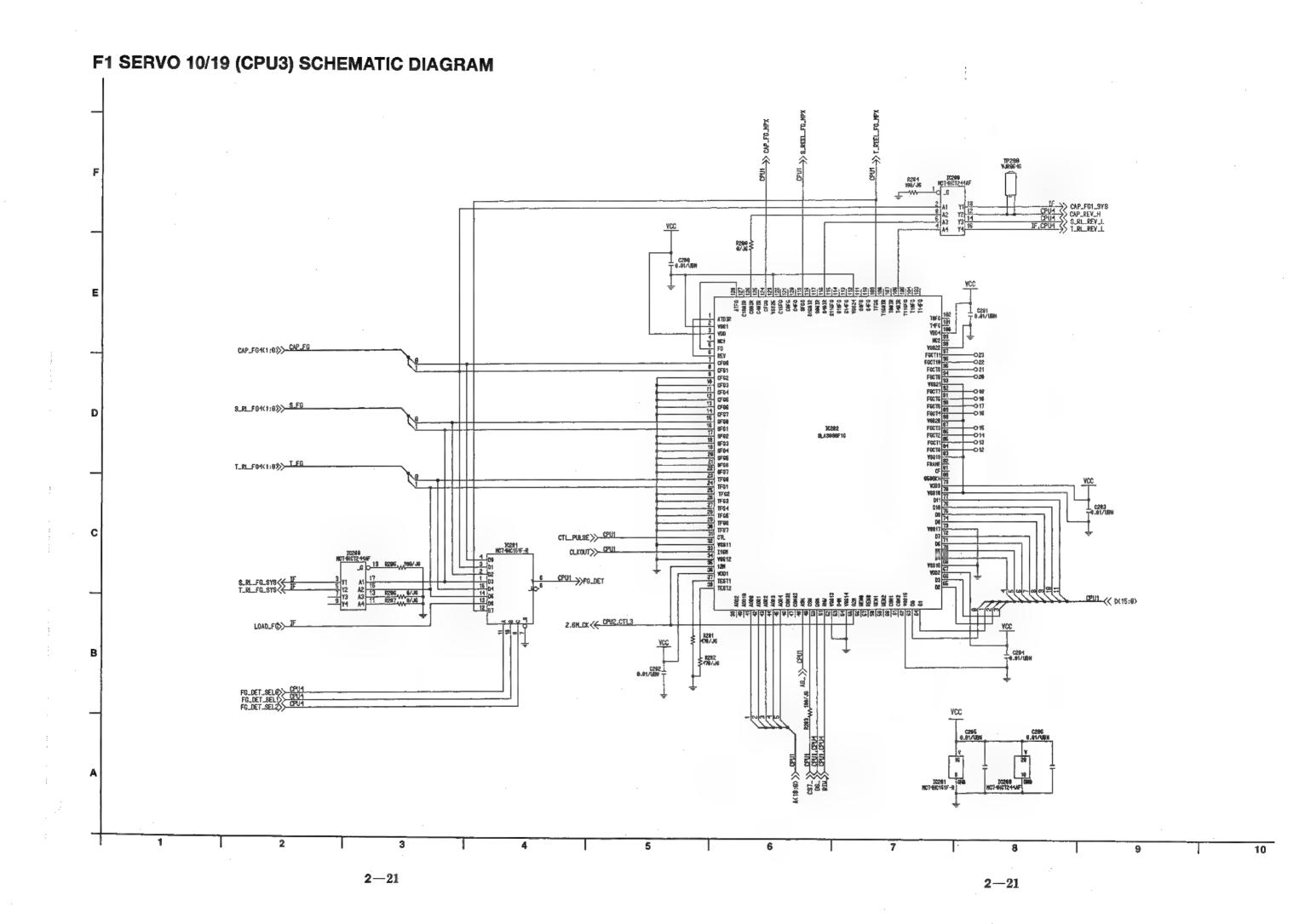
2 - 17

F1 SERVO 7/19 (AD DA) SCHEMATIC DIAGRAM IC281 AD7896AR-R TP200 EYF6CU POWER +5VA + C284 16V47/EVV __CA_CY_DRV >> SERVO_REF R299 22K/J6 REC_ENV> ₩A86 47-HTX 0.01/UBN CPU1 >>QSPI(8:0) DGND C208 4760P/UBN R205 = 3300 / RE D201 NA152K-TX sck 5 SDATA IC204 HC74HC244AFR TP201 VJR0646 IC205 | UPC408262-T! R202 | 2280/J6 R219_W190/J6 1 CONVST. BUSY 8 TENSION >> IF 0200 MA152K-TX D209 MA152K-TX TENS_GND>>> IF CPU2 CONVST_ CPU2 AD_SW_INH AD_SEL(2:0) CTL_PEAK>>> CTL2 CTL_0\$>>\frac{cTL2}{} D CPU2 ->> LDAC_ R208 2200/J6 REC_SPEED>> IF CA_CY_DRV >> CYL_ERR C208 4700P/UBN POWER >>+5VA 10282 AD7943BR-R D204 MA8047-HTX VDD 10 10 0213 0.81/U8N) POWER >>+5VA -5VA < PONER CA_CY_DRV >> CAP_ERR C214 16V47/EVV CA_CY_DRV >>> CAP_ER_DA D205 HA8047-HTX 7/7 7/7 C211 C212 16V47/EYV 0.01/UBN STB1 R209 22K/J6 R210 2200/J6 RL_DRV >T_RL_ERR RL_DRV >S_RL_ERR LM_DRV,CTL2 >S_RL_ERR CTL2 >CTL_GAIN_ADJ IF >RTV_SPD_PB SRI PB_SPEED>> IF SRO D208 NA152K-TX снз 🗄 8 STB2 C269 4766P/UBN +9YA 5 8 UPC-168262-T1 R214 7 476/J6 AAA IOUT 1 R217 (600/J6 IOUT2 R218 1000/J6 AGNE DGND 0207 MA152K-TX POWER +5VA D203 R212 2299/J6 D296 HA152K-TX PB_ENV >>> IF CPU2 << DA_SW_ENH CPUZ << DA_SEL(2:0) +9VA < POWER C218 4766P/UBN ATF_ERR> C218 0.01/UBN R224 4/J6 6 IC264 GND MC74HC244AFR -9VA REVERSE SIDE 2-18 2-18 F1 SERVO 6/19

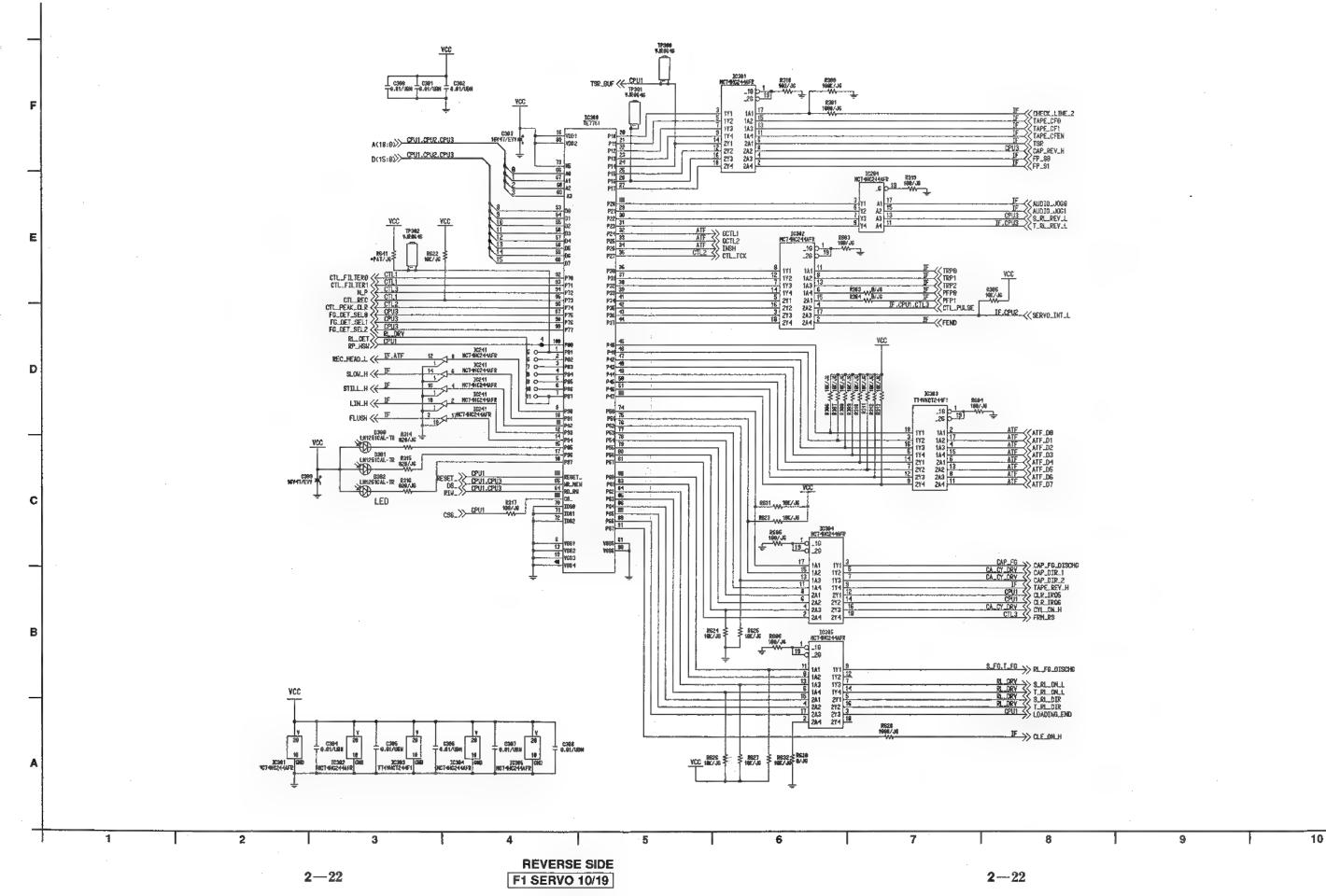
F1 SERVO 8/19 (CPU1) SCHEMATIC DIAGRAM A(18:0) D(15:0) CPU2,CPU3,CPU4>> D(15:0> CPU4 (CLR_IR05 TPU(15:0) CPV4 (CLR_IROS QSPI(8:0) MATI_BOX_INT>> CPUS YCC 1 0230 1 0230 TSR_BUF >> __CPU4 CYL_FG_PRE >> CA_CY_DRY CAP_FG4(1:8) >> CAP_FG_CPU3 8_REEL_FG_MPX >> CPU3 REC_CTL CTL1 T_REEL_FG_MPX >> CPU3 R232 0/J6 CS_(0:11) 💆 08_(0:40) 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 97/3698 TP(B) TP232 TP[C] DSACKO TP[D] CTLEDGE >> CTL2 10219 107100-4FT12L CPU4.CTL3 ->> CTL_PULSE LOADING_END>> CPU4 CPU4 >>> RESET_ | PCSL_SL | PCSL_| | C6900T_ BR_1C89_ BC_1C51_ BGACK_IC52_ FC81C83_ FC11C84_ FC21C65_ CLKOUT R247€ 07J6≩ SERVO_RESET_L>> IF

F1 SERVO 9/19 (CPU2) SCHEMATIC DIAGRAM

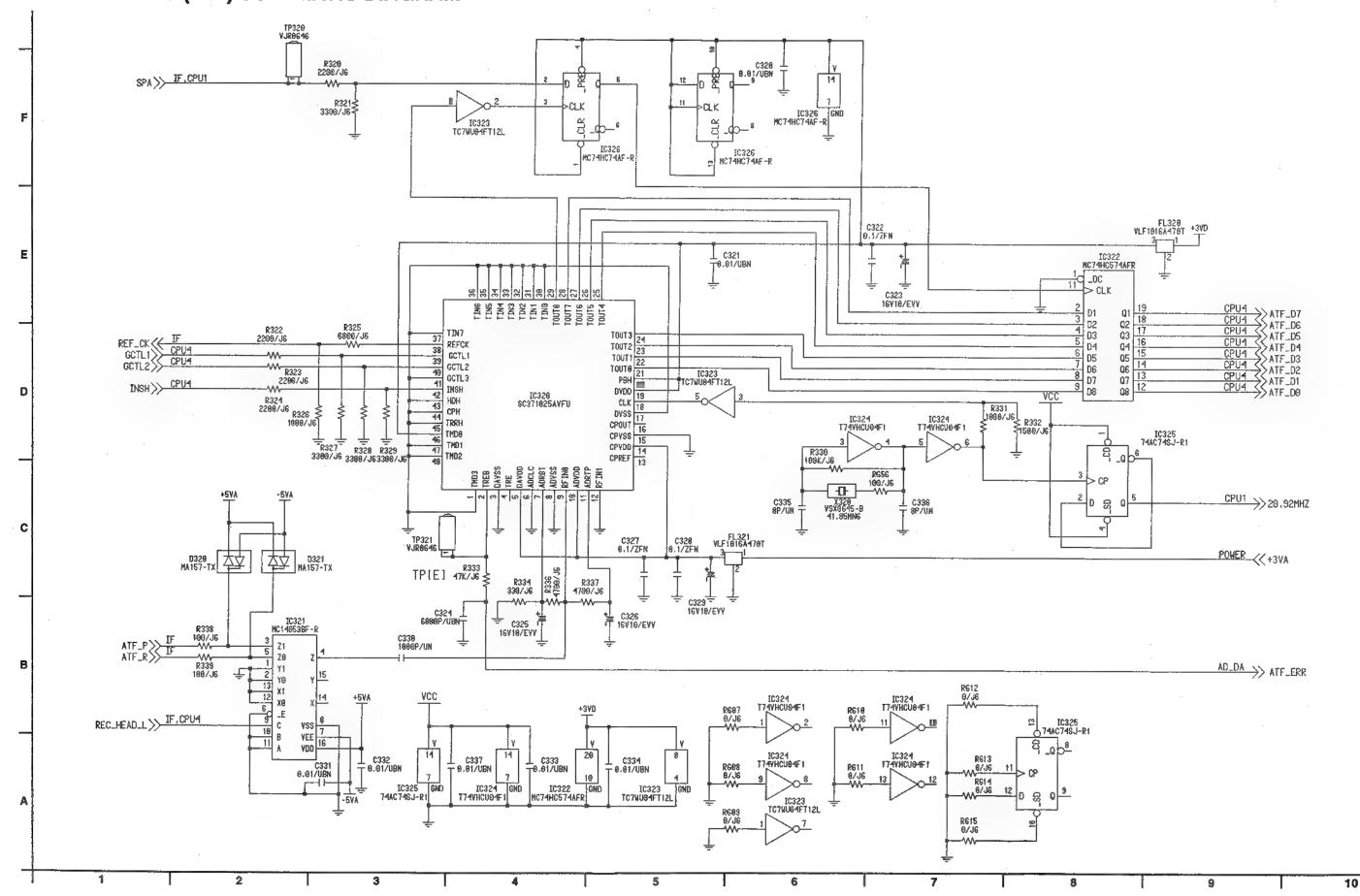




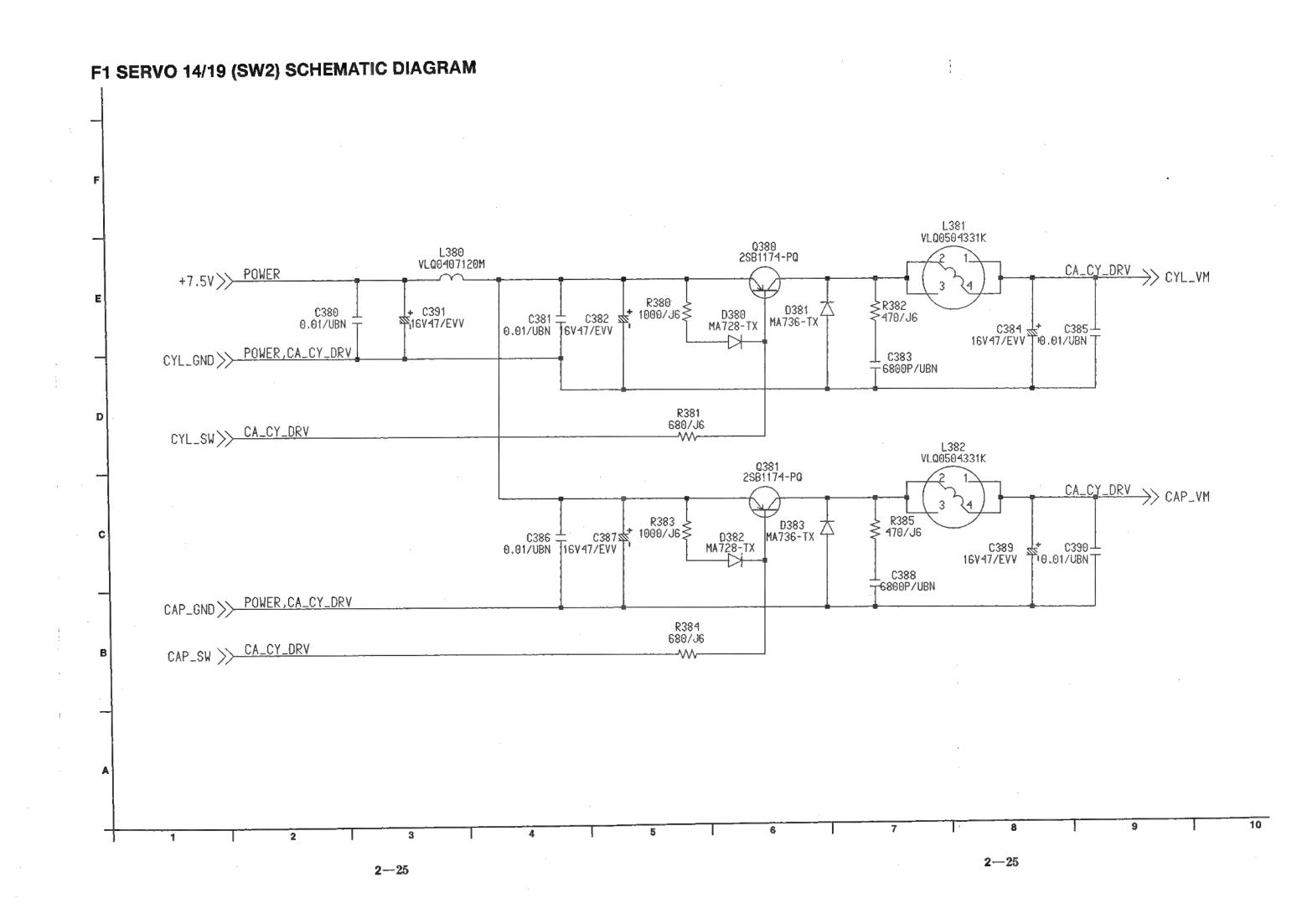
F1 SERVO 11/19 (CPU4) SCHEMATIC DIAGRAM



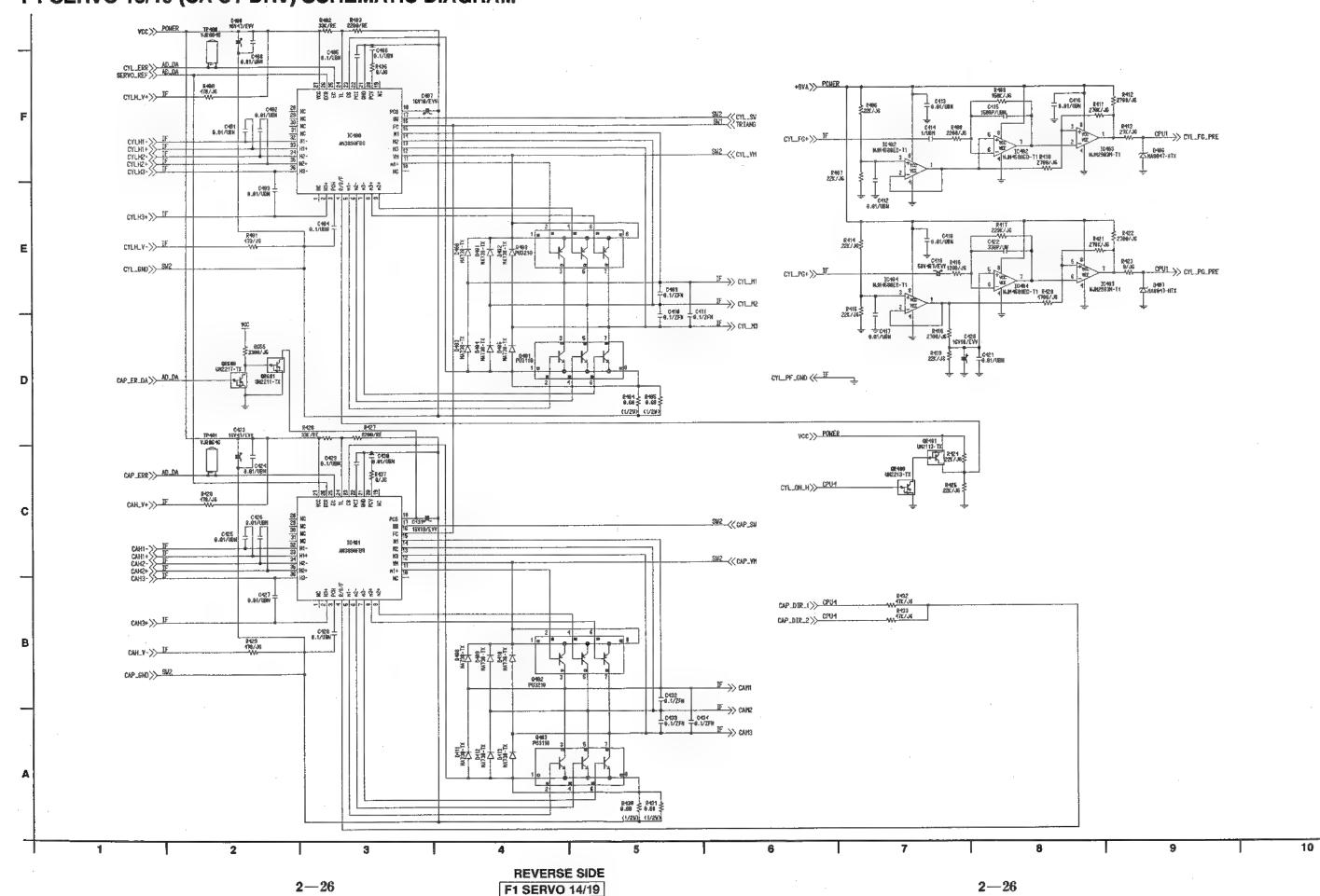
F1 SERVO 12/19 (ATF) SCHEMATIC DIAGRAM



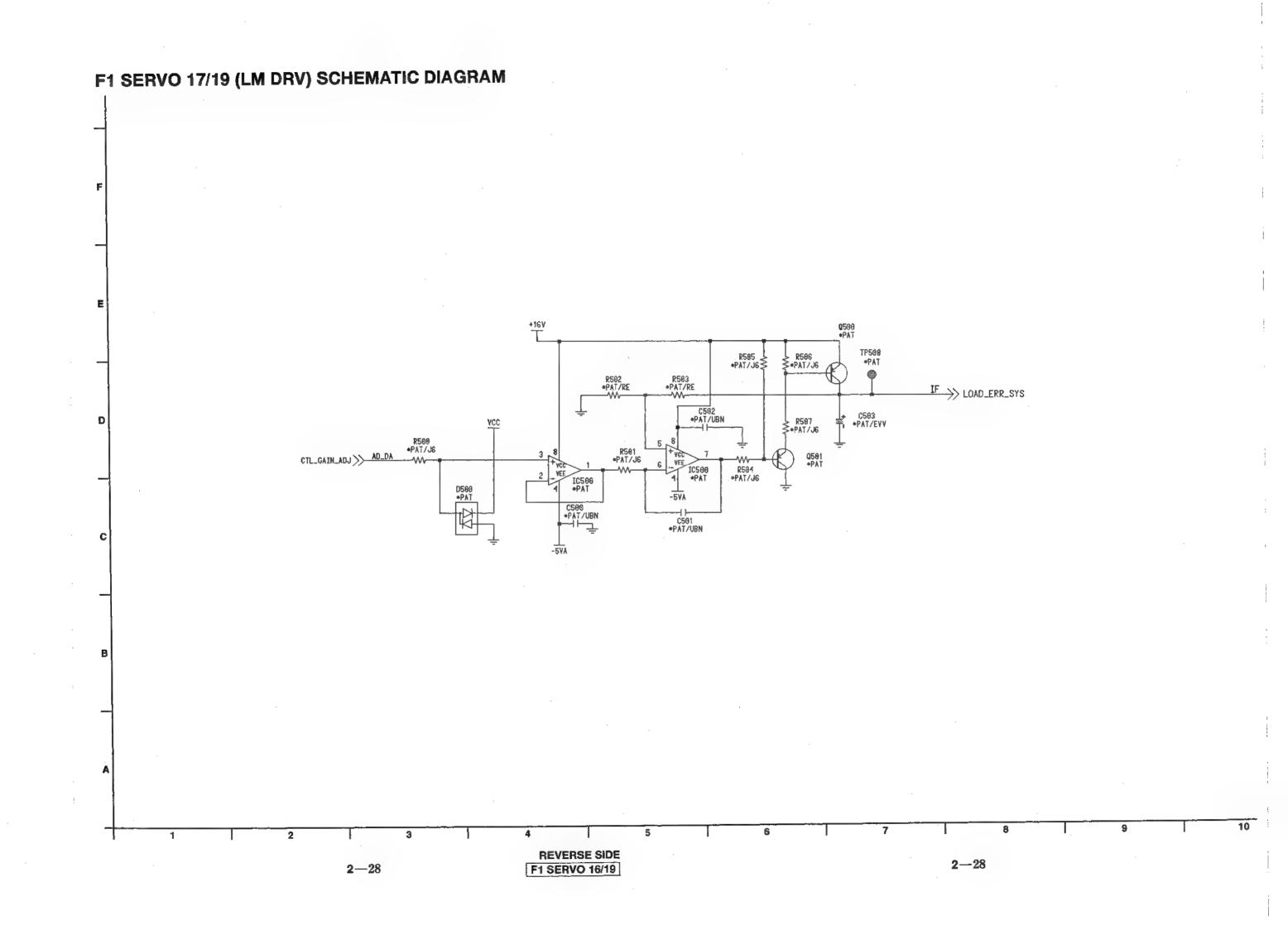
F1 SERVO 13/19 (SW1) SCHEMATIC DIAGRAM L340 VLQ0504331K Q340 2SB1174-PQ RL_DRV >> T_VM C346 0.1/ZFN R341 \$470/J6 R340 D340 330/J6 MA728-TX C345 = 1 25V100 ECA1EXLV101Z R342 C348 C340 0.1/ZFN C341 V 25V100 ECA1EXLV101Z 15K/J6 0.1/ZFN [∆] D3∮1 MA736-TX R343 -₩-470K/J6 C349 R344 ₹1200/J6 , 0.01/UBN T_FB>> RL_DRV IC342 NJM4580ED-T1 R374 W 0/J6 --VVV--R350 R356 100K/J6 R345 ____390K/J6 R349 47K/J6 R375 C350 0.01/UBN _A0/J6 ~W~ VCC C342 T 0.01/UBN \$ R347 \$56K/J6 R346 1000/J6 -5VA ₹ R353 10K/J6 R351 ≸ 10K/J61 R357 \$10K/J65VA C351 0,01/UBN VCC + REF SCP SCP INVINZ BZ SCP TFBZ NIVINZ SCP TFBZ OUTZ 0.1/ZFN T -₩ R348 \$10K/J6 ם IC342 8 NJM4580ED-T1 R368 1000/J6 CA_CY_DRV UN2111-TX C343 0.01/UBN — R354 100K/J6 CT NINYIN1 INVIN1 FB1 DTC1 OUT1 ₹ R355 10K/J6 R352 ≸ 10K/J6↑ S_FB>> RL_DRV VCC VEE →> TRIANG R358 R359 \$ ___ D344 ^Z MA8039-LTX C352 M 0.01/UBN R360 18K/J6 VCC ₹ R362 100K/J6 ┦┝╌<u>┧</u> C355 L 0.01/UBN R361 ≶ 47K/J6∫ -5VA R365 QR341 VÉE R367 \$10K/J6 470K/J6 UN2111-TX IC341 UPC393G2-T1 R366 15K/J6 ____ C353 0.01/UBN ______100K/J6 R371 56K/J6 C356 0.1/ZFN R363 R370 | |-RL_DRV >> S_VM 10K/J6 L341 VLQ0407120M R372 D343 470/J6 MA736-TX Q341 2SB1174-PQ +16V >> POWER R369 330/J6 D342 MA728-TX \$\$\frac{1}{25\footnote{100}} = 125\footnote{100} C359 L342 VLQ0504331K ⊥ C361 ⊤0.1/ZFN C360 25V100 C362 VV-25V100 ECA1EXLV101Z ECA1EXLV101Z R373 RL_GND>> RL_DRV, POWER 1200/J6 2 7 **REVERSE SIDE** 2 - 242-24 F1 SERVO 12/19

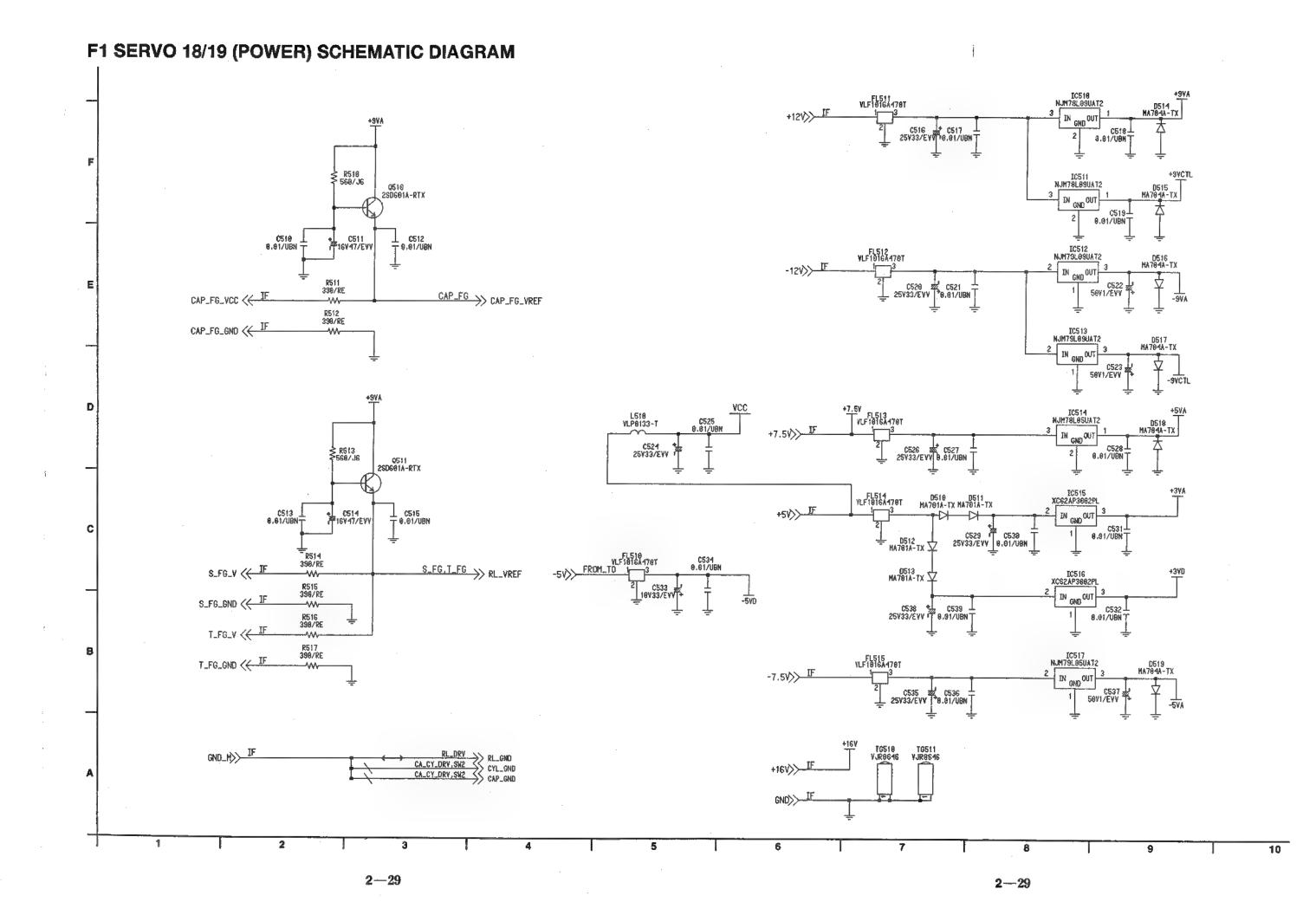


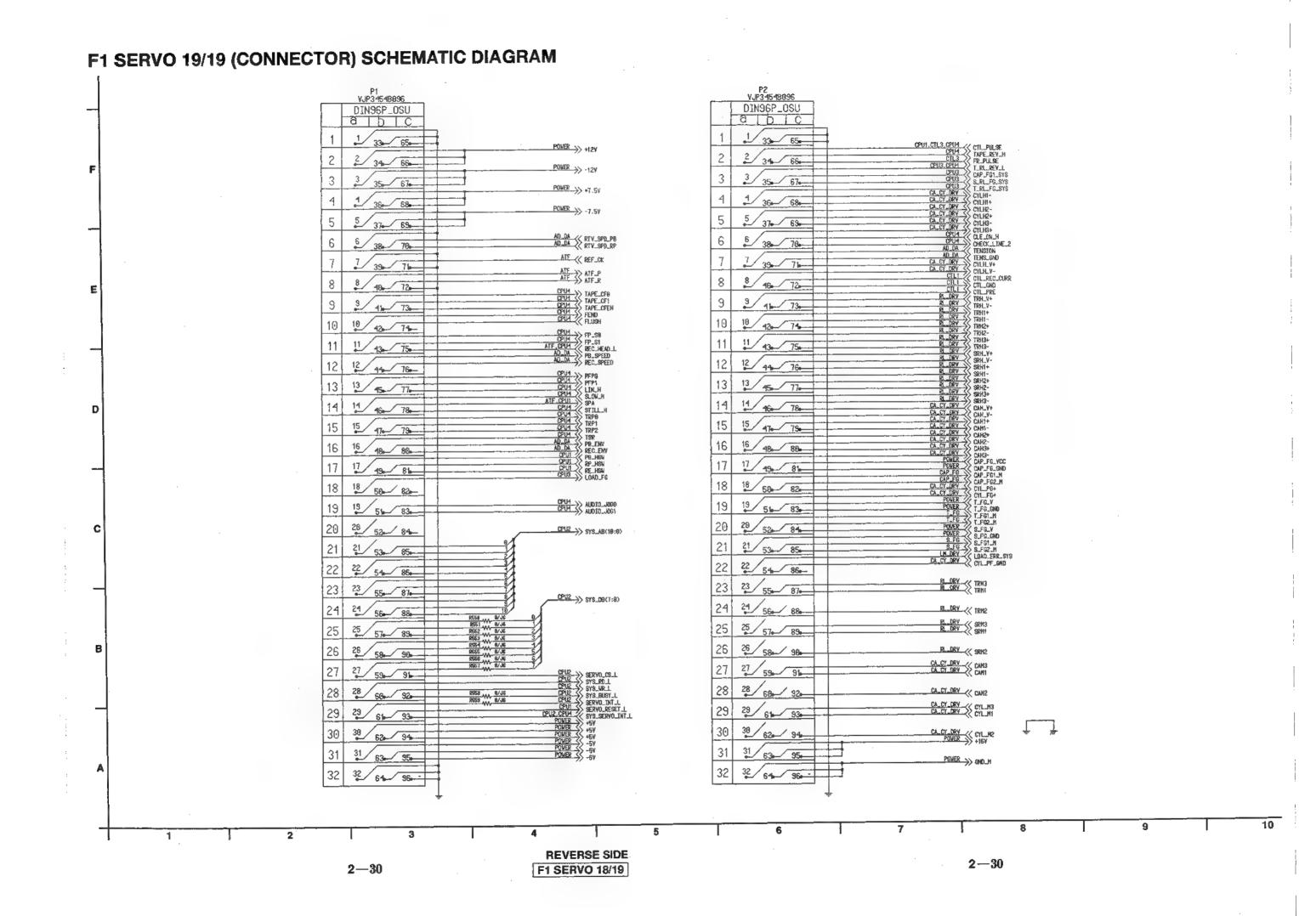
F1 SERVO 15/19 (CA CY DRV) SCHEMATIC DIAGRAM

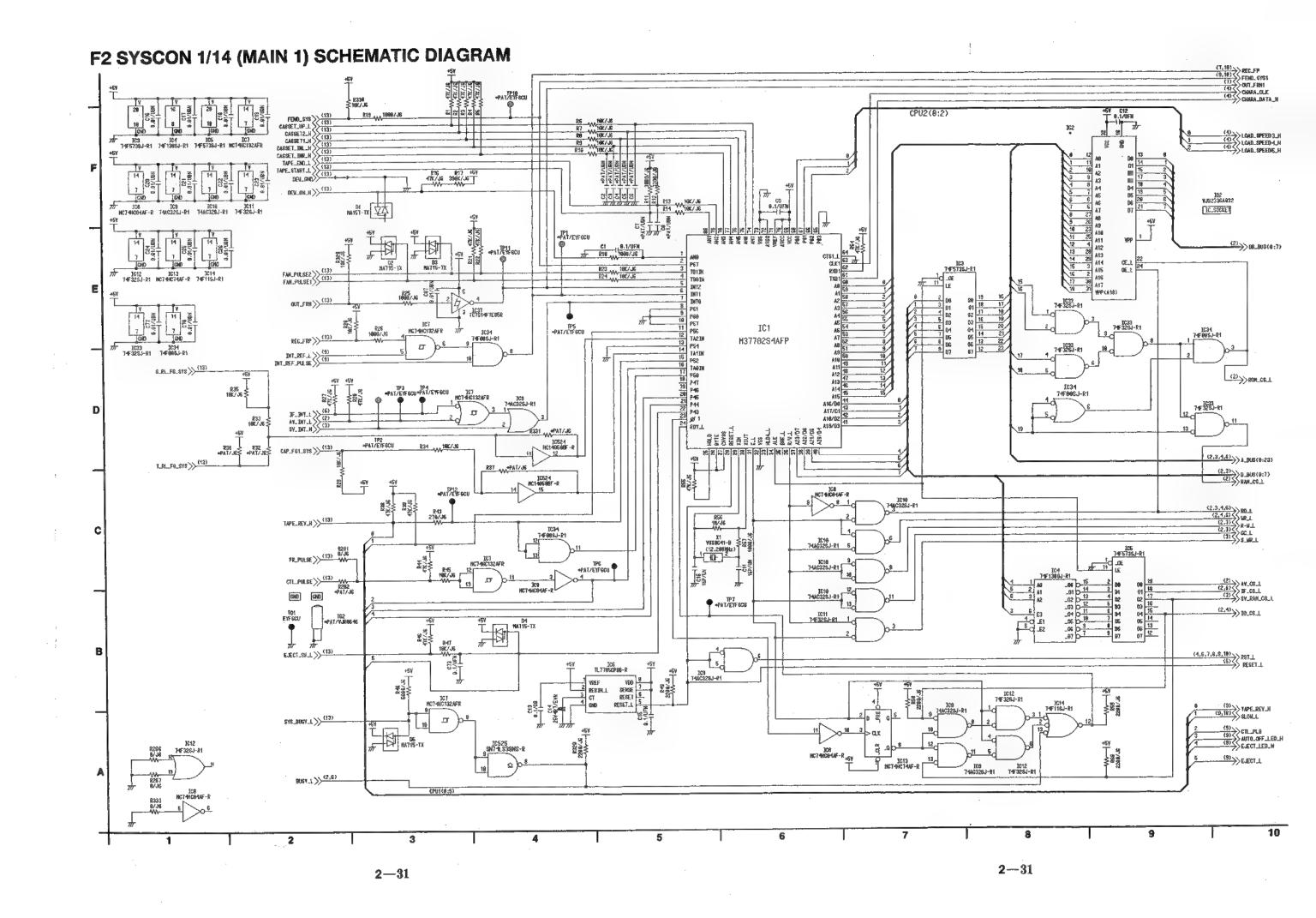


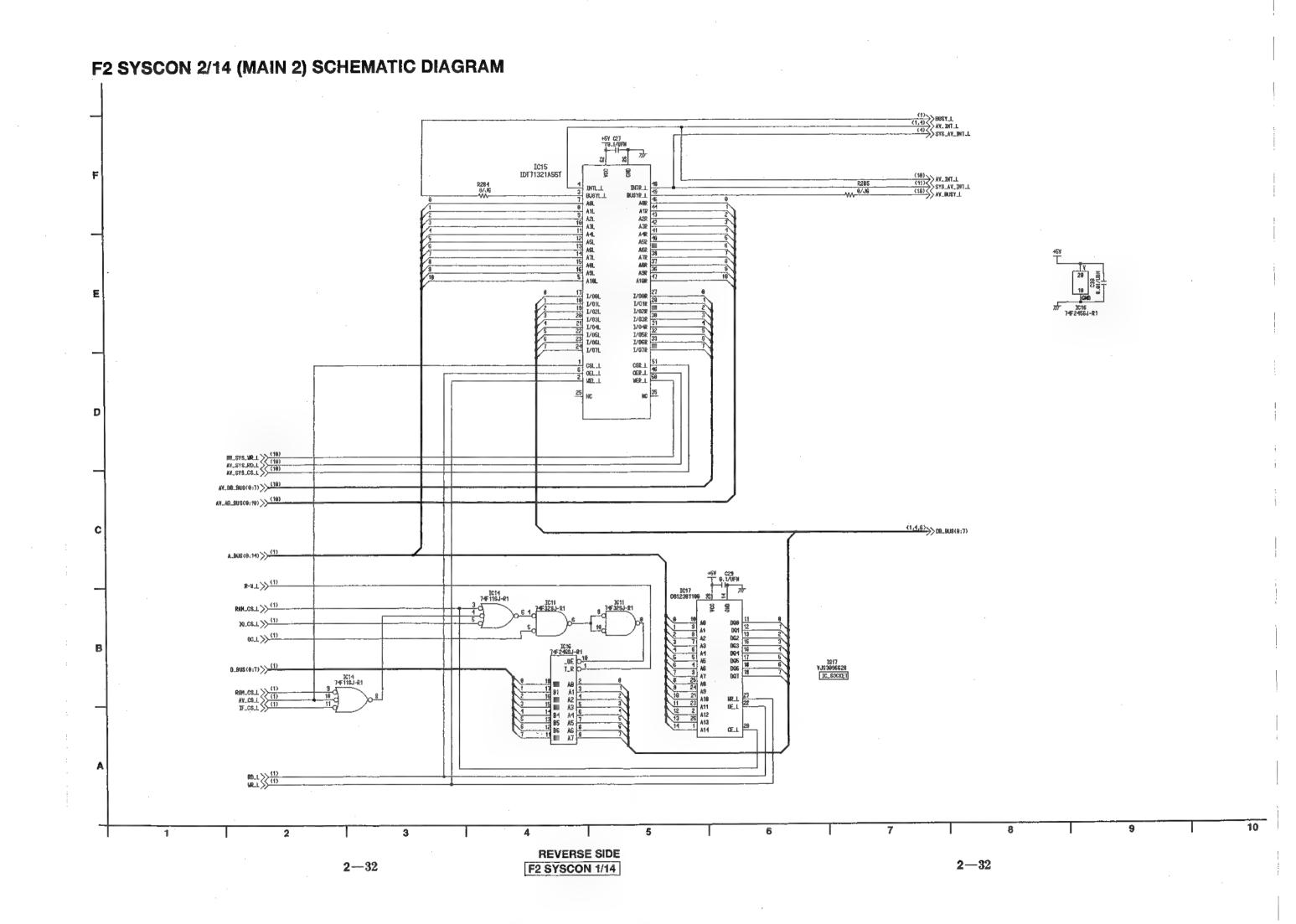
F1 SERVO 16/19 (RL DRV) SCHEMATIC DIAGRAM IC452 UPC4558G2-T1 T_RL_ERR >>AD_DA 4789/RE R452 1886/J6 TR(0:11) D450 HA152K-TX ☆ C450 -0.022/UBN C459 50V2R2/EV<u>V</u> R455 33/J6 P451 C461 0.047/UBN C456 50V2R2/EVV C458 50V2R2/EVV 1200/RE -5VA R652 R653 6869/RE 1009/RE C691 0.81/UBN C453 0.047/UBN C460 0.1/ZFN D455 MA738-TX R453 33/J6 ¥ R454 ≸ 33/J6 0.455 0.1/ZFN:₇ IF TRM2 IF TRH1+ IF TRH1IF TRM3 IF TRM2+ IF TRM2IF TRM2 IF TRM2 IF TRM3TRM3TRM3- $\dashv \Box$ 0.1/ZFN T D454 MA738-TX T_VM >> SNT D451 HA738-TX IC450 AN3834K D456 Ma738-tx C454 0.047/UBN VH A1 -|| D453 HA738-1) ATC CS A2 RL_DET < CPU4 R651 5688/J6 0452 MA738-TX PCV C600 0.01/UBN H3+ H2-8 H2+ VCC 10 H1+ 11 PFG 12 PCH C463 C465 C467 C462 9.933/UBN 0.933/UBN 0.933/UBN VCC 13 R650 5600/J6 TRH_V+ 1C688 NJM2983H-T1 C468 0.1/UBN TRH_V-T_FB >> SW1 R458 39K/J6 ≸ __C472 ____C47/KBM R456 R457 2.2 2.2 (1/2W) (1/2W) C464 C466 4760P/UBN 4760P/UBN T_RL_ON_L >> CPU4 R459 12K/J6≨ T_RL_DIR >> CPU4 T_REEL(0:1) 16V10/EVV T_REEL(0:1) SR(0:11) IF >>> SRH_V+ 0457 NA152K-TX C489 0V2R2/EVY C482 6.947/UBN C478 50V2R2/EVV IF >>> SRH_V-+5YA R463 1886/J6 ≸ 50V2R2/EVV C481 0.1/ZFN S_RL_ERR >> AD_DA 4700/RE WV T C474 0.047/UBN D462 MA738-TX ₹ R470 \$ 270/J6 R464 \$33/J6 C479 0.1/ZFN ₹ 8465 \$ 33/J6 VEE C476 L P.1/ZFN T IC452 UPC455862-T1 C473 0.022/UBN V 0461 IC451 NA738-TX AN3834K C475 0.947/UBN D458 MA738-TX D463 MA738-TX VH 24 23 A1 22 CS 21 PCV 29 -14 D468 MA738-TX ATC S_VH >> SW1 || ys. CND 20 H3-OS 19 18 17 16 15 ED 14 13 ₽ H3+ H2-RL_GNO >> POWER,SWI H2+ 10 H1+ 11 PFG 12 PCH VCC 13 C486 C488 C484 S_FB >> SN1 ± 0489 18.1/UBN +5VA 0.033/UBN 0.033/UBN 0.033/UBN C483 4700P/UBN R468 39K/J≣≶ C496 L C491 L 0.1/UBN 0.01/UBN R466 R467 2.2 2.2 (1/2W) (1/2W) S_RL_ON_L >> CPU4 4700P/UBN 4700P/UBN S_RL_DIR >> CPU4 C492 C493 16V19/EVV 0.47/KBM S_REEL(0:1) S_REEL(0:1)

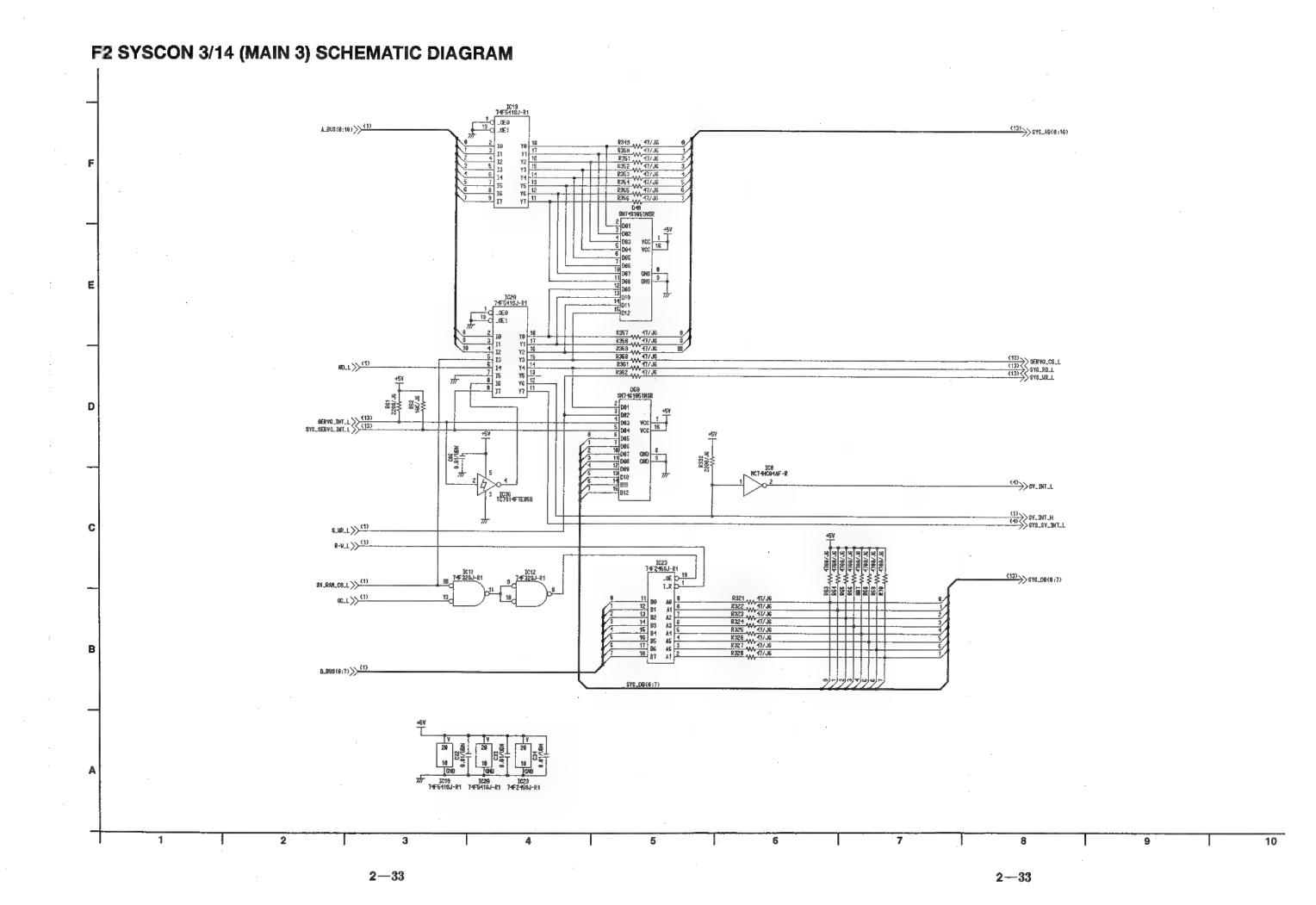




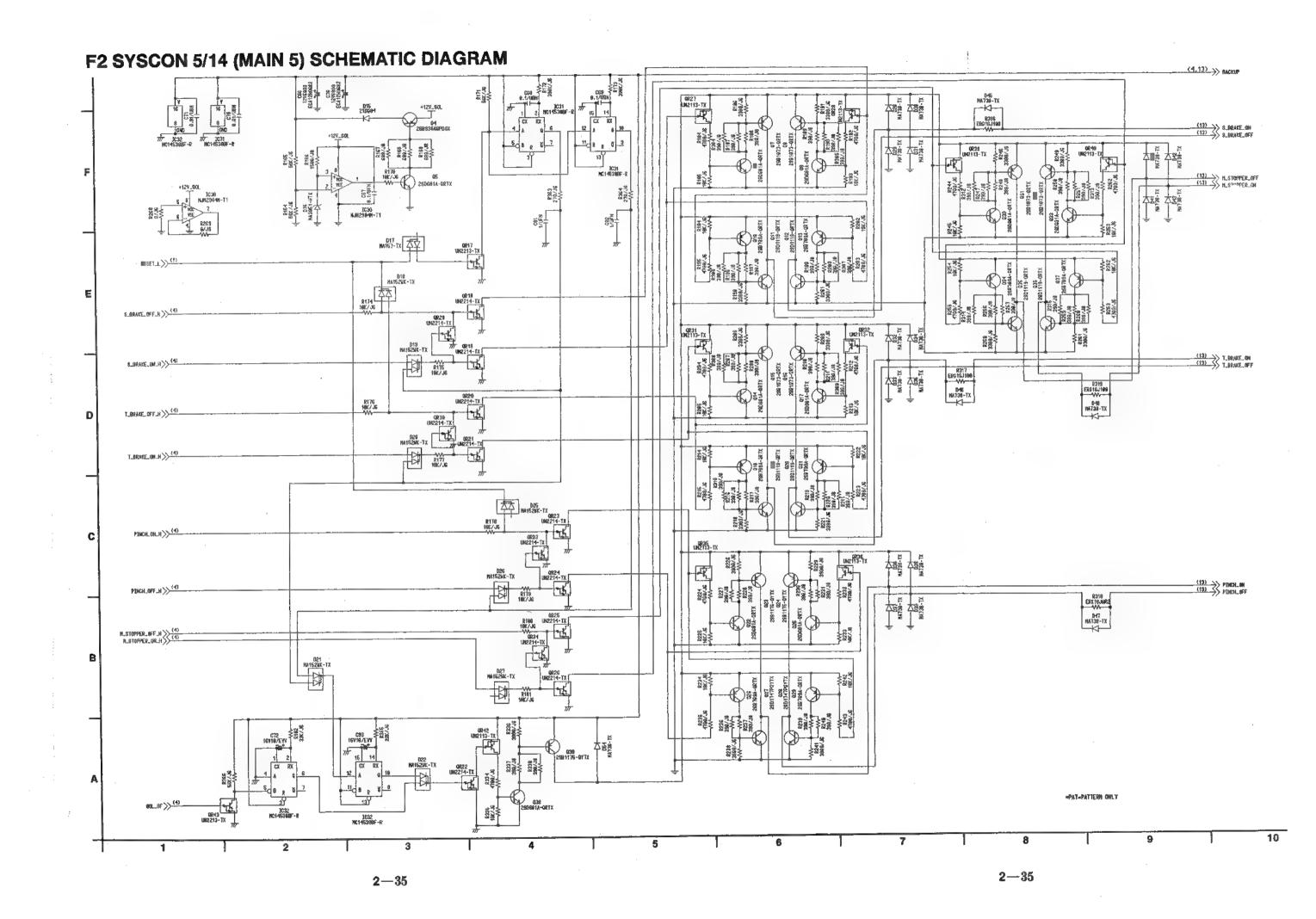




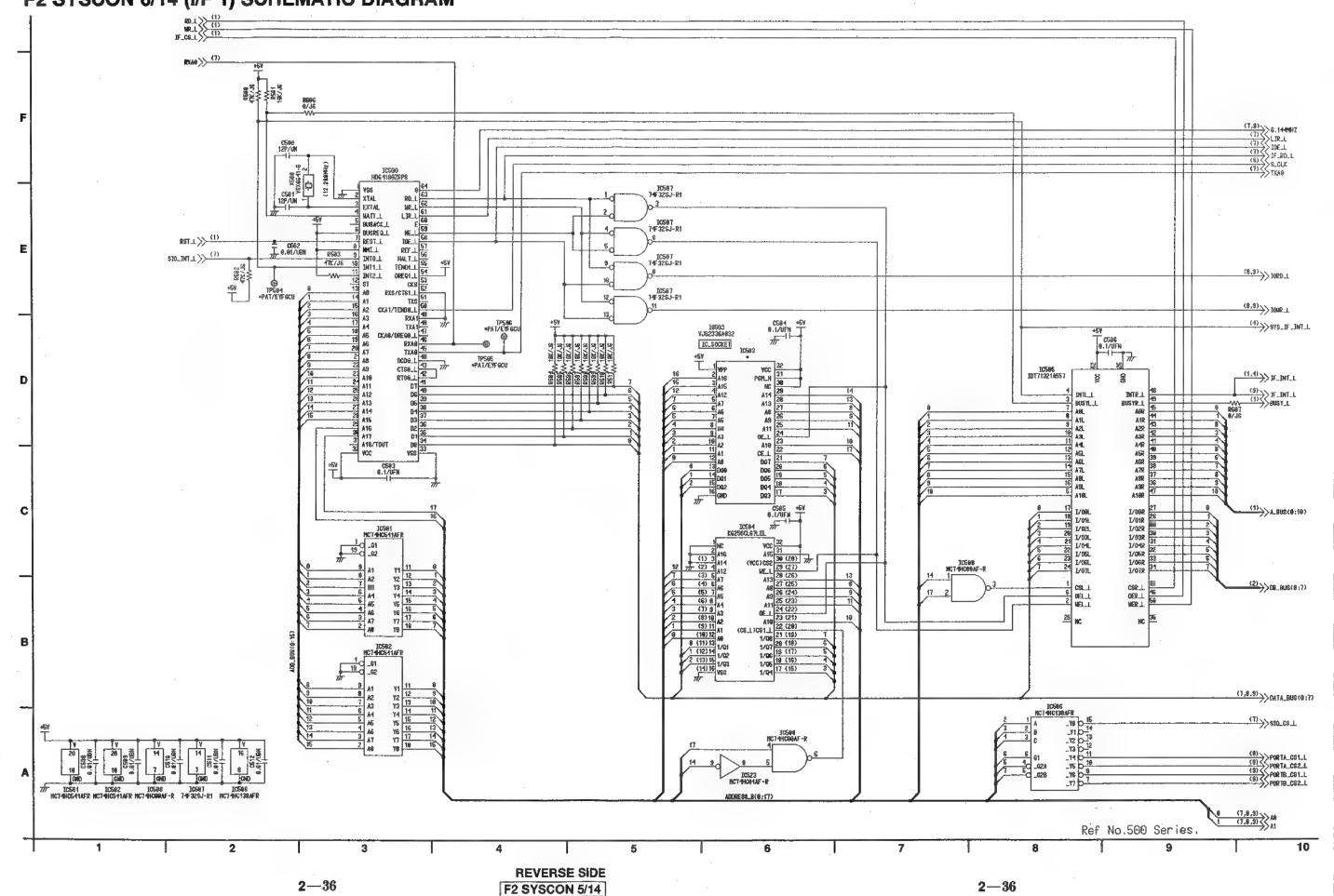




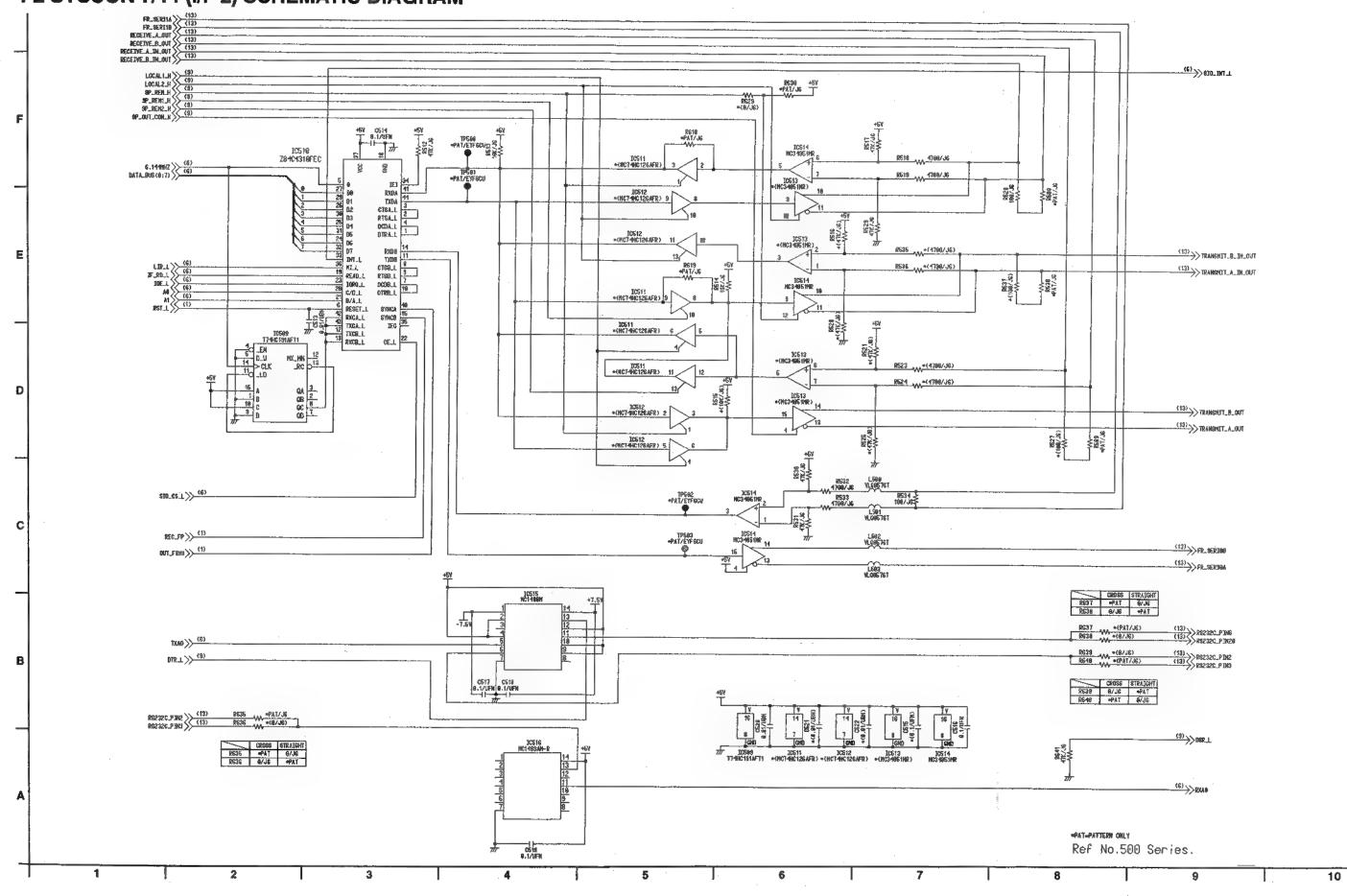
F2 SYSCON 4/14 (MAIN 4) SCHEMATIC DIAGRAM SERVICE ENFOUR JII O-1 2 3 4 5 6 7 8 9 186 C88 C81 155 CASSET_DONH_H>> (13) IC26 IC26 123456118396 ALBEN R09 +PAT/J6 PAL (5) 8.BRAKE OFF.H (6) S.BRAKE OM.H (5) T.BRAKE ON.H (5) P.DIRCH OR.H (5) P.DIRCH OFF.H (6) P.DIRCH OFF.H (6) H.STOPPER_OFF.H H.STOPPER_OR.H LINIZETCAL-TR LIN251CAL-TR LIM25TCAL-TR LIN2SHCAL-TR В (13) (5) 80L_8F R159 ER61SJ229P R161 2206/J6 960 **REVERSE SIDE** 2-34 2 - 34F2 SYSCON 3/14



F2 SYSCON 6/14 (I/F 1) SCHEMATIC DIAGRAM



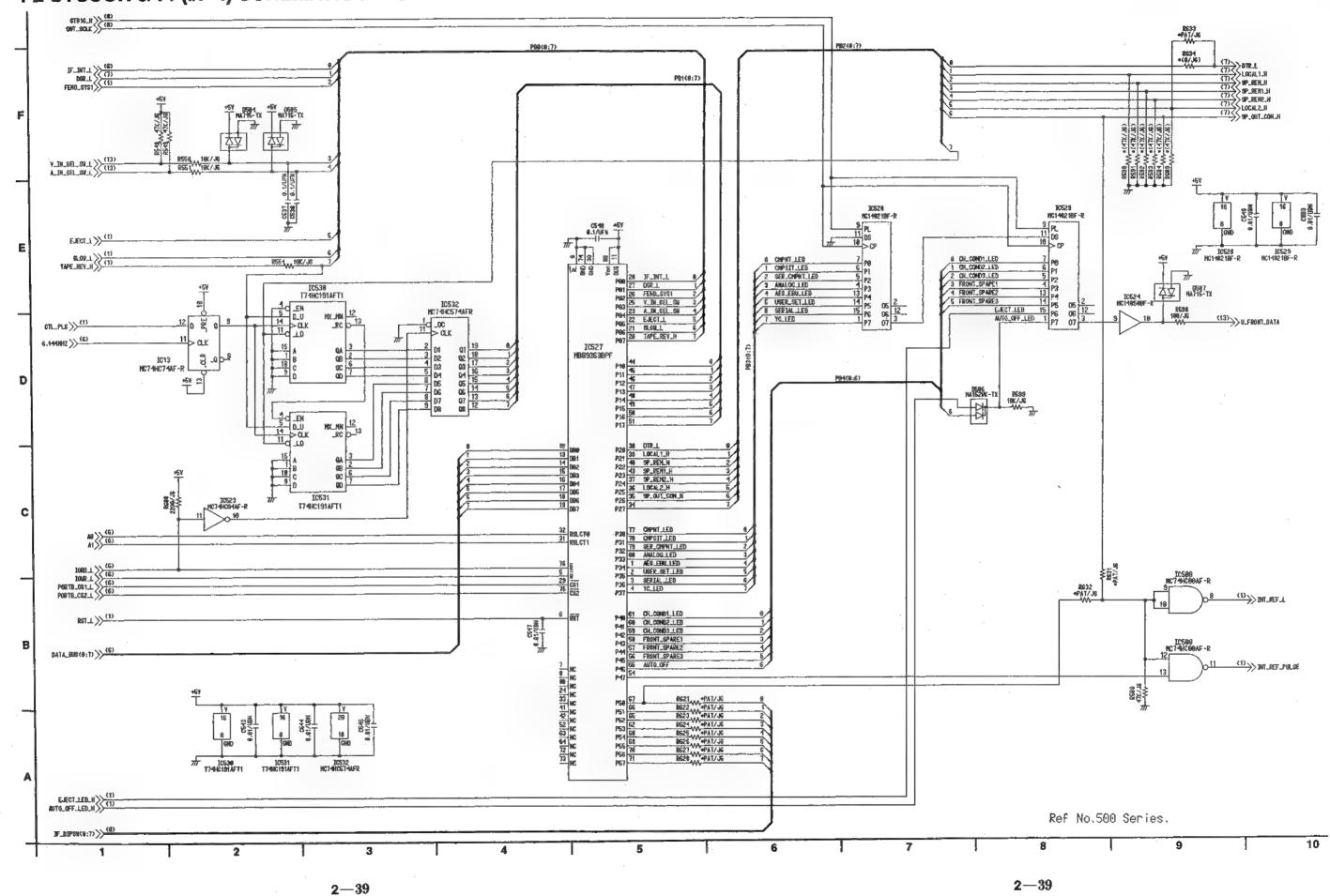
F2 SYSCON 7/14 (I/F 2) SCHEMATIC DIAGRAM



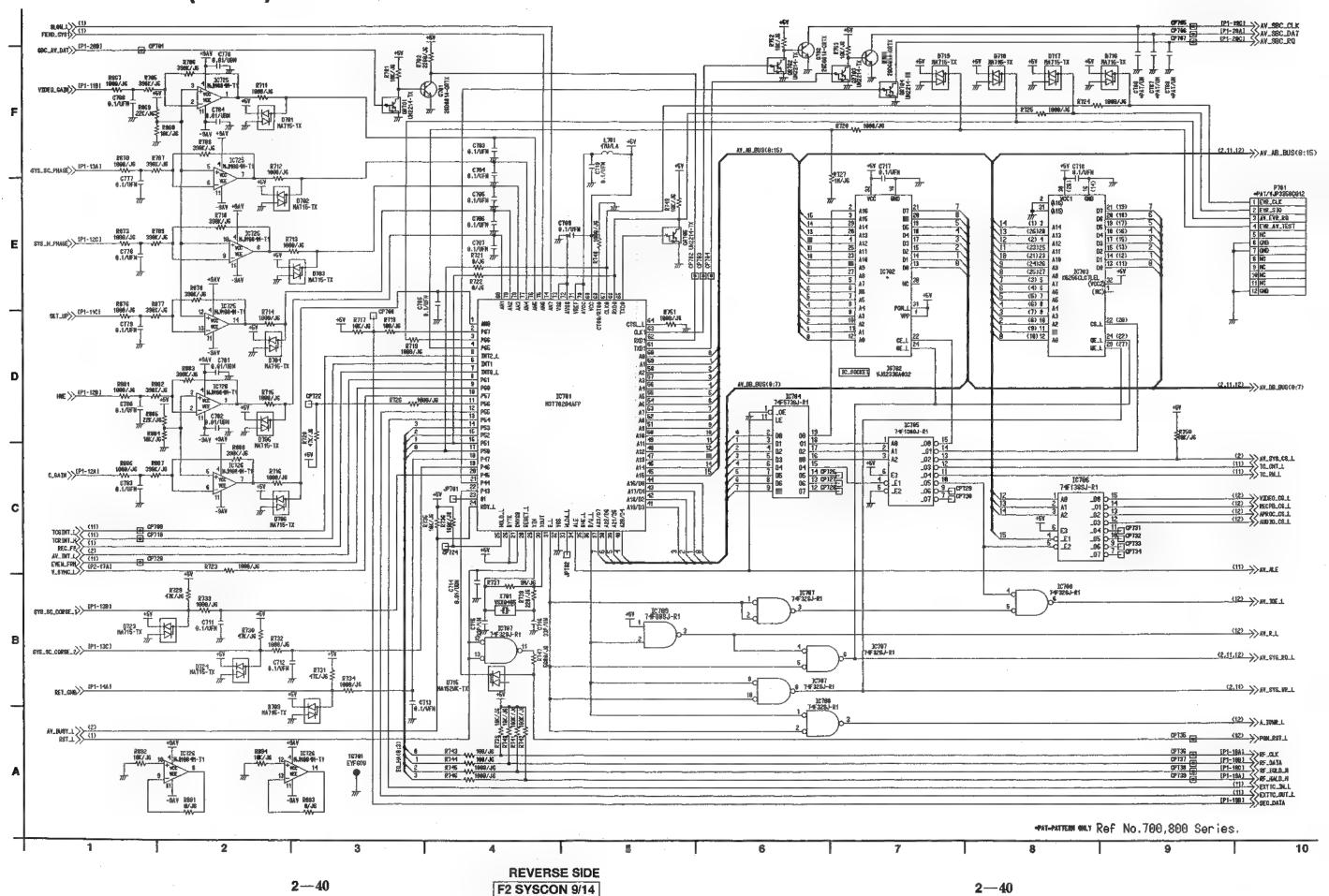
F2 SYSCON 8/14 (I/F 3) SCHEMATIC DIAGRAM (13) SERIAL_OLK \$18_CLK16 (9) STB16_H s_clt >> (6) P_SERIAL_OUT TORD_L (6) TORD_L (6) TORD_L (6) TORD_L (6) TORD_L (6) #ST_L>> (1) P_SERIAL_IN >> (13) VSS375 | - 80 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 | - 10 |

Ref No.500 Series.

F2 SYSCON 9/14 (I/F 4) SCHEMATIC DIAGRAM



F2 SYSCON 10/14 (AV I/F 1) SCHEMATIC DIAGRAM

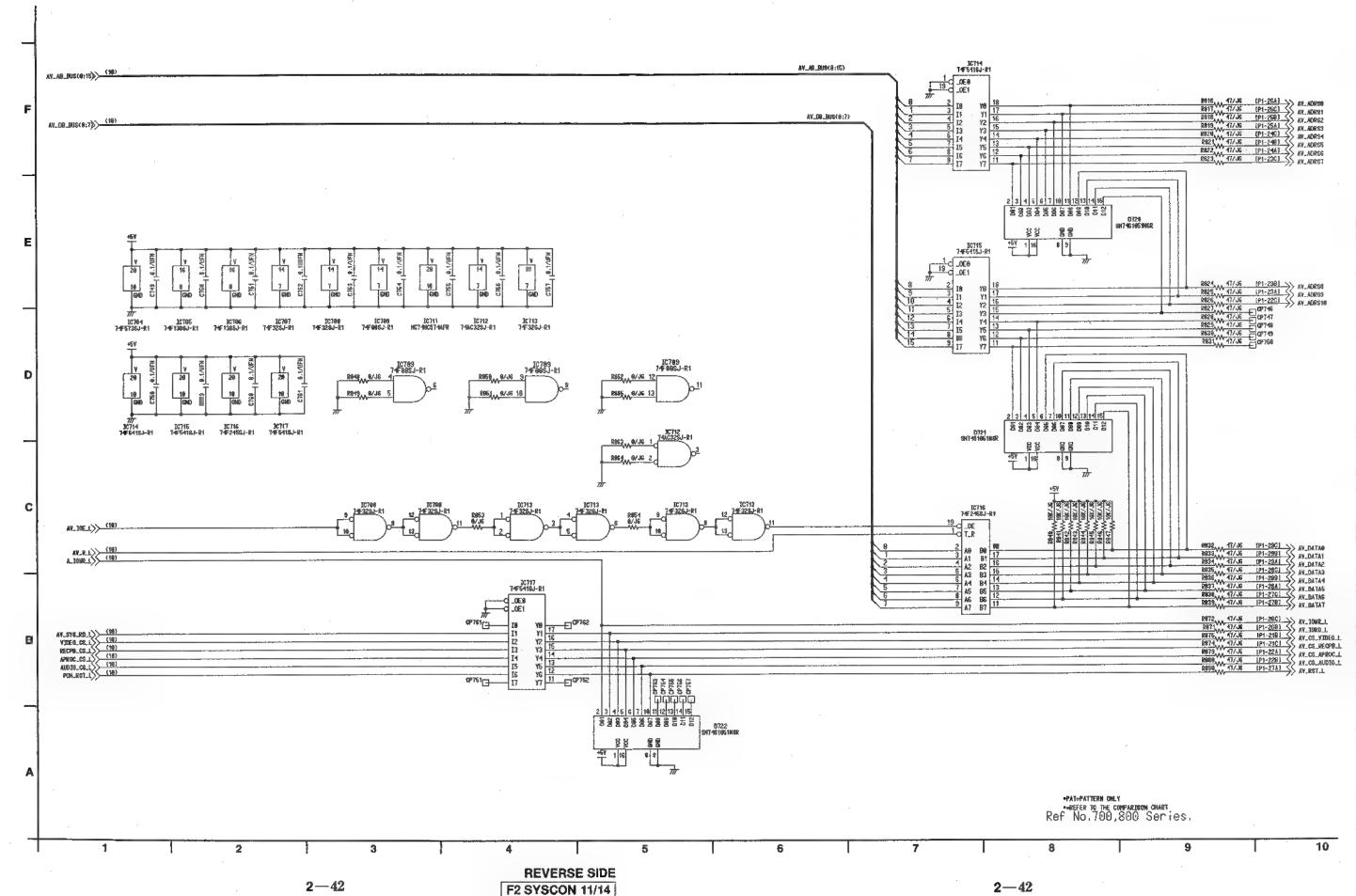


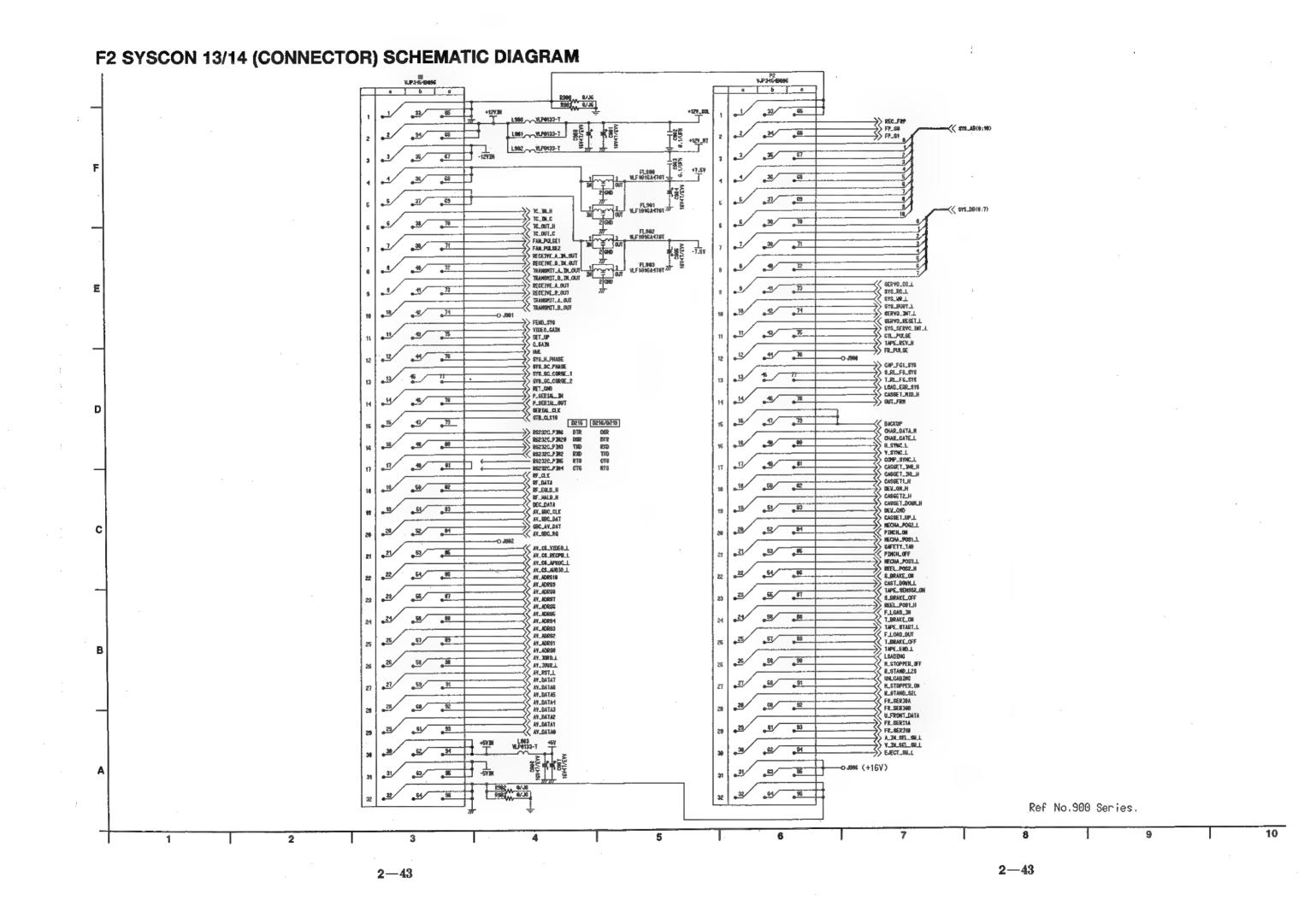
F2 SYSCON 11/14 (AV I/F 2) SCHEMATIC DIAGRAM IP701 •PAT/EYFGCU 25V4R7/EVN 9.61/USM TC, CHT_L>> (16) TC_BL(\$) [P1-GA] IC722 UPC474162-T1 Ref No.700,800Series.

2-41

2-41

F2 SYSCON 12/14 (AV I/F 3) SCHEMATIC DIAGRAM





F2 SYSCON 14/14 (COMPARISON CHART) SCHEMATIC DIAGRAM

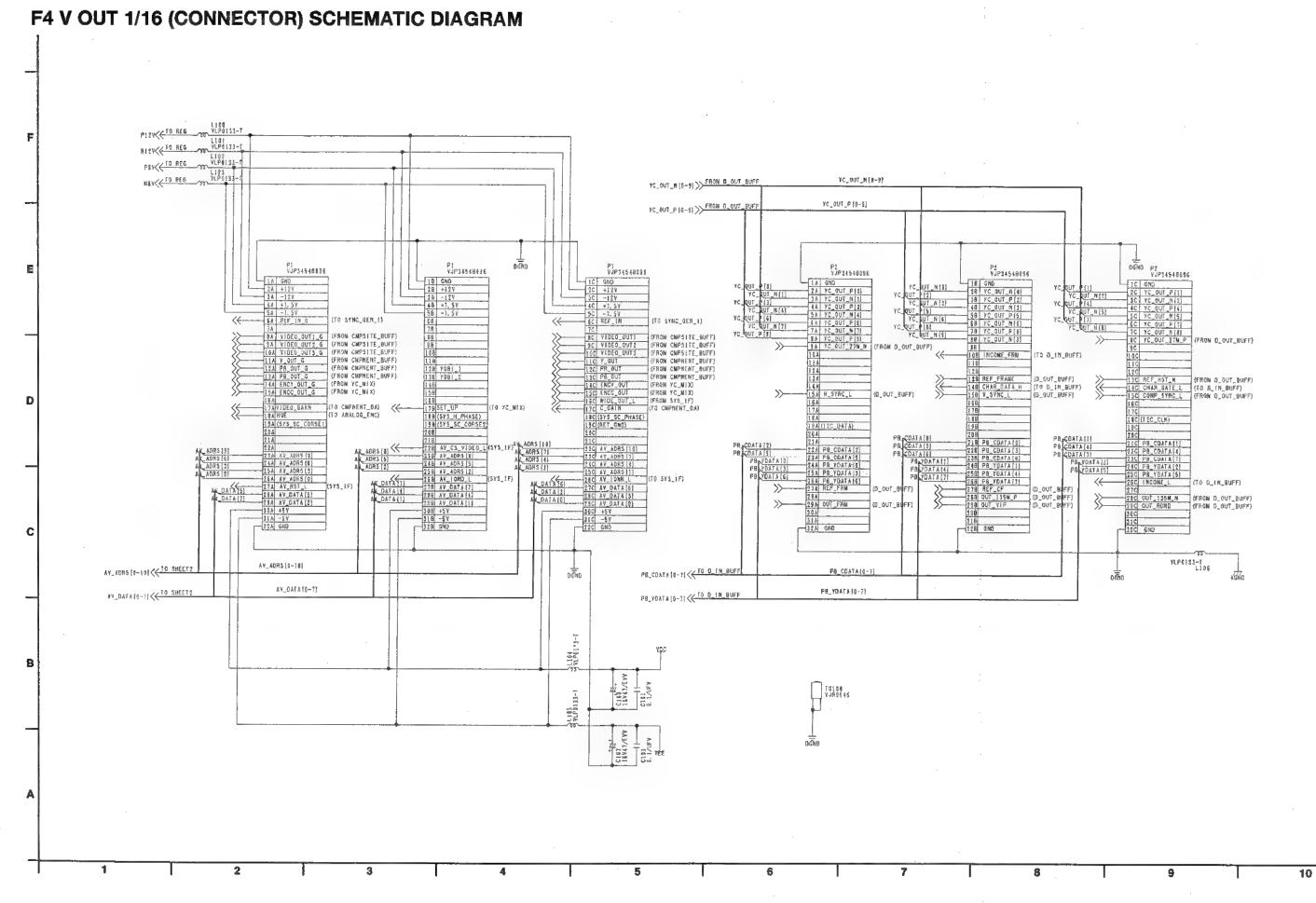
\$REF\$	NTSC	PAL
C2	*PAT/UBN	*PAT/UBN
C3	*PAT/UBN	*PAT/UBN
C4	*PAT/UBN	*PAT/UBN
C40	*PAT/UBN	*PAT/UBN
C41	*PAT/UBN	*PAT/UBN
C42	*PAT/UBN	*PAT/UBN
C43	*PAT/UBN	*PAT/UBN
C44	*PAT/UBN	*PAT/UBN
C45	*PAT/UBN	*PAT/UBN
C48	*PAT/UBN	*PAT/UBN
C5	*PAT/UBN	*PAT/UBN
C50	*PAT/UBN	*PAT/UBN
C515	*PAT/UFN	*PAT/UFN
C52	*PAT/UBN	*PAT/UBN
C521	*PAT/UBN	*PAT/UBN
C522	*PAT/UBN	*PAT/UBN
C524	*PAT/UFN	*PAT/UFN
C525	*PAT/UBN	*PAT/UBN
C528	*PAT/UBN	*PAT/UBN
C529	*PAT/UBN	*PAT/UBN
C530	*PAT/UBN	*PAT/UBN
C531	*PAT/UBN	*PAT/UBN
C536	*PAT/EVV	*PAT/EVV
C54	*PAT/UBN	*PAT/UBN
C6	*PAT/UBN	*PAT/UBN
C61	*PAT/EVV	*PAT/EVV .
C62	*PAT/EVN	*PAT/EVN
C63	*PAT/EVN	*PAT/EVN
C64	*PAT/EVN	*PAT/EVN
C7	*PAT/UBN	*PAT/UBN
C727	18P/UN	22P/UN
C75	*PAT/EVV	*PAT/EW
C771	120P/UN	2200P/UBN
C786	*PAT/UN	*PAT/UN
C787	*PAT/UN	*PAT/UN
C788	*PAT/UN	*PAT/UN
C8	*PAT/UBN	*PAT/UBN
C82	*PAT/UBN	*PAT/UBN
C84	*PAT/UN	*PAT/UN
C85	*PAT/UN	*PAT/UN
0501	*PAT	*PAT
0502	*PAT	*PAT
0503	*PAT	*PAT

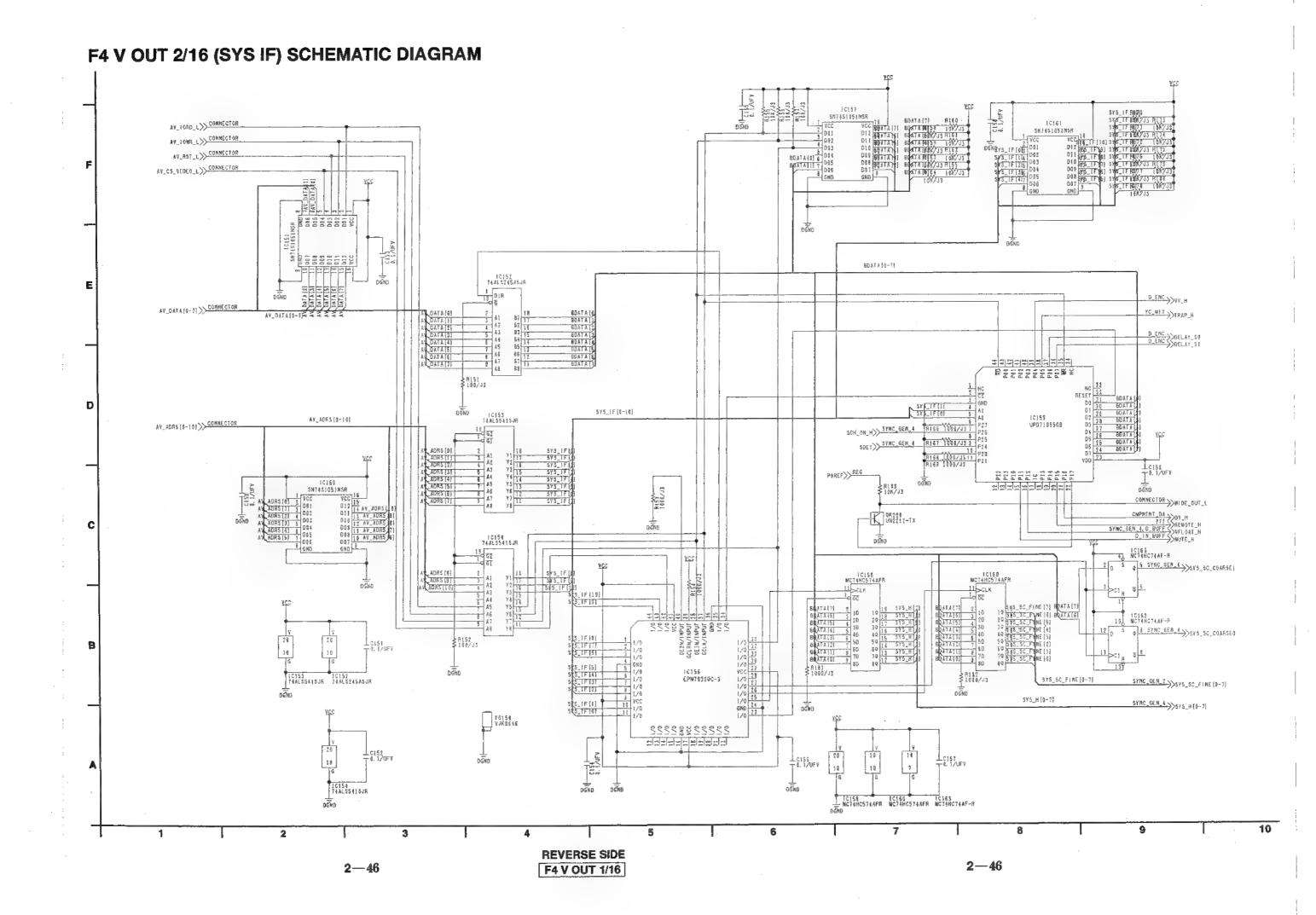
\$REF\$	NTSC	PAL
IC2	VS12384	VS12397
10503	V\$12385	VS12398
IC511	*PAT	*PAT
IC512	*PAT	*PAT
10513	*PAT	*PAT
IC518	*PAT	*PAT
10519	*PAT	*PAT
10520	*PAT	*PAT
10521	*PAT	*PAT
IC522	*PAT	*PAT
10702	V\$12387	VS12399
P701	*PAT	*PAT
R143	*PAT/J6	*PAT/J6
R149	*PAT/J6	*PAT/J6
R279	*PAT/J6	*PAT/J6
R282	*PAT/J6	*PAT/J6
R32	*PAT/J6	*PAT/J6
R331	*PAT/J6	*PAT/J6
R36	*PAT/J6	*PAT/J6
R37	*PAT/J6	*PAT/J6
R515	*PAT/J6	*PAT/J6
R516	*PAT/J6	*PAT/J6
R521	*PAT/J6	*PAT/J6
R523	*PAT/J6	*PAT/J6
R524	*PAT/J6	*PAT/J6
R525	*PAT/J6	*PAT/J6
R527	*PAT/J6	*PAT/J6
R528	*PAT/J6	*PAT/J6
R535	*PAT/J6	*PAT/J6
R536	*PAT/J6	*PAT/J6
R537	*PAT/J6	*PAT/J6
R538	*PAT/J6	*PAT/J6
R543	*PAT/J6	*PAT/J6
R544	*PAT/J6	*PAT/J6
R547	*PAT/J6	*PAT/J6
R562	*PAT/J6	*PAT/J6
R563	*PAT/J6	*PAT/J6
R564	*PAT/J6	*PAT/J6
R565	*PAT/J6	*PAT/J6
R588	*PAT/J6	*PAT/J6
R589	*PAT/J6	*PAT/J6
R590	*PAT/J6	*PAT/J6
R591	*PAT/J6	*PAT/J6

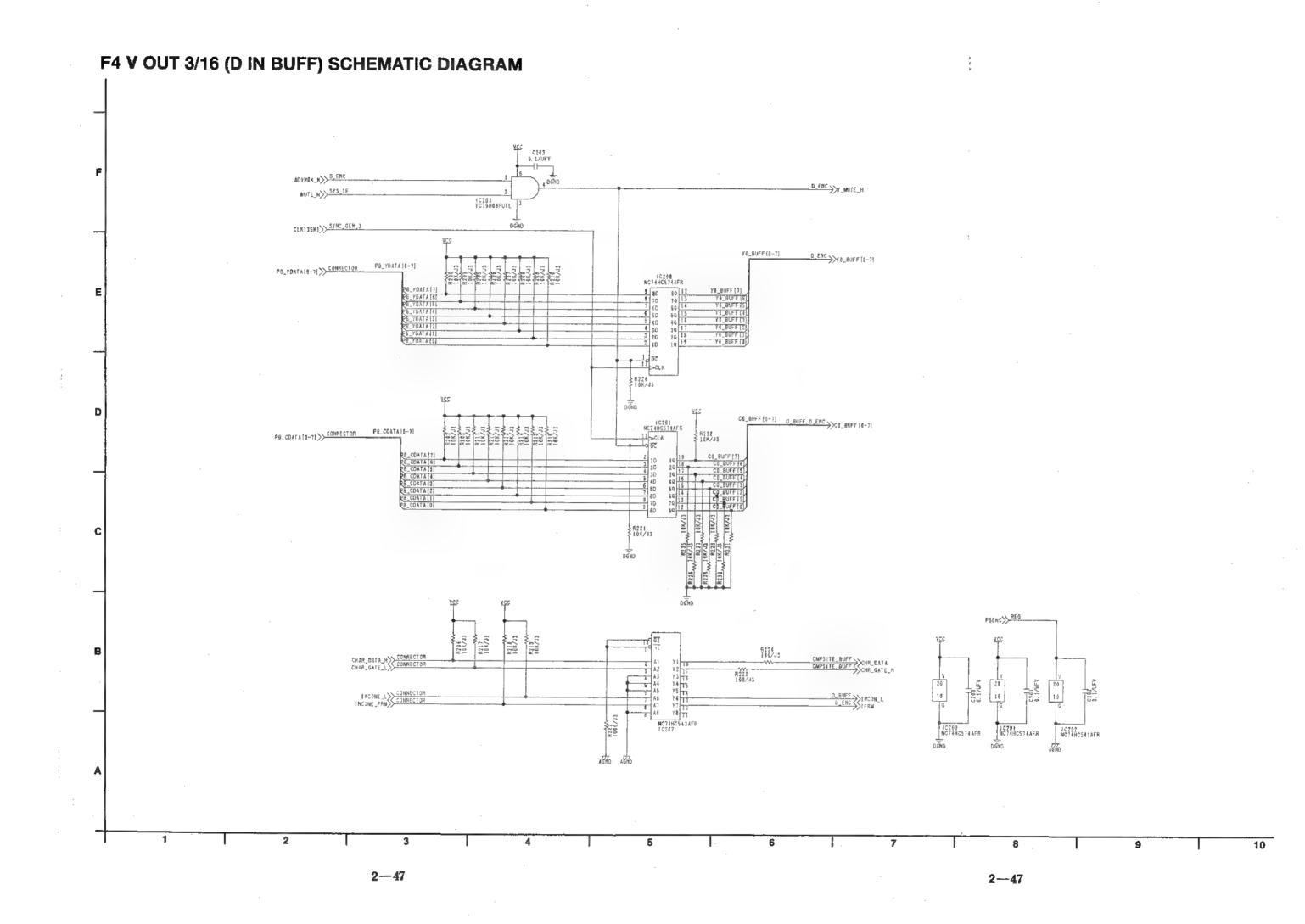
\$REF\$	NTSC	PAL
R592	*PAT/J6	*PAT/J6
R593	*PAT/J6	*PAT/J6
R594	*PAT/J6	*PAT/J6
R609	*PAT/J6	*PAT/J6
R614	*PAT/J6	*PAT/J6
R615	*PAT/J6	*PAT/J6
R616	*PAT/J6	*PAT/J6
R617	*PAT/J6	*PAT/J6
R618	0/J6	0/J6
R619	0/J6	0/J6
R620	0/J6	0/J6
R621	10K/J6	10K/J6
R622	10K/J6	10K/J6
R623	10K/J6	10K/J6
R624	10K/J6	10K/J6
R625	10K/J6	10K/J6
R626	10K/J6	10K/J6
R627	10K/J6	10K/J6
R628	10K/J6	10K/J6
R629	*PAT/J6	*PAT/J6
R630	0/J6	0/J6
R631	*PAT/J8	*PAT/J6
R632	*PAT/J6	*PAT/J6
R633	0/J6	0/J6
R634	*PAT/J6	*PAT/J6
R635	0/J6	0/J6
R636	*PAT/J6	*PAT/J6
R637	0/J6	0/J6
R638	≉PAT/J6	*PAT/J6
R639	*PAT/J6	≉PAT/J6
R640	0/J6	0/J6
R760	0/J6	*PAT/J6
R761	*PAT/J6	*PAT/J6
R763	*PAT/J6	*PAT/J6
R88	100K/J6	*PAT/J6
R89	*PAT/J6	100K/J6
TG2	*PAT	*PAT
TP1	*PAT	*PAT
TP10	*PAT	*PAT
TP11	*PAT	*PAT
TP12	*PAT	*PAT
TP2	*PAT	*PAT
TP3	*PAT	*PAT

\$REF\$	NTSC	PAL
TP4	*PAT	*PAT
TP5	*PAT	*PAT
TP500	*PAT	*PAT
TP501	*PAT	*PAT
TP502	*PAT	*PAT
TP503	*PAT	*PAT
TP504	*PAT	*PAT
TP505	*PAT	*PAT
TP506	*PAT	*PAT
TP6	*PAT	*PAT
TP7	*PAT	*PAT
TP701	*PAT	*PAT
TP702	*PAT	*PAT
TP9	*PAT	*PAT
VR701	*PAT/VR3	*PAT/VR3
X703	VSX0614-T	VSX0615-T

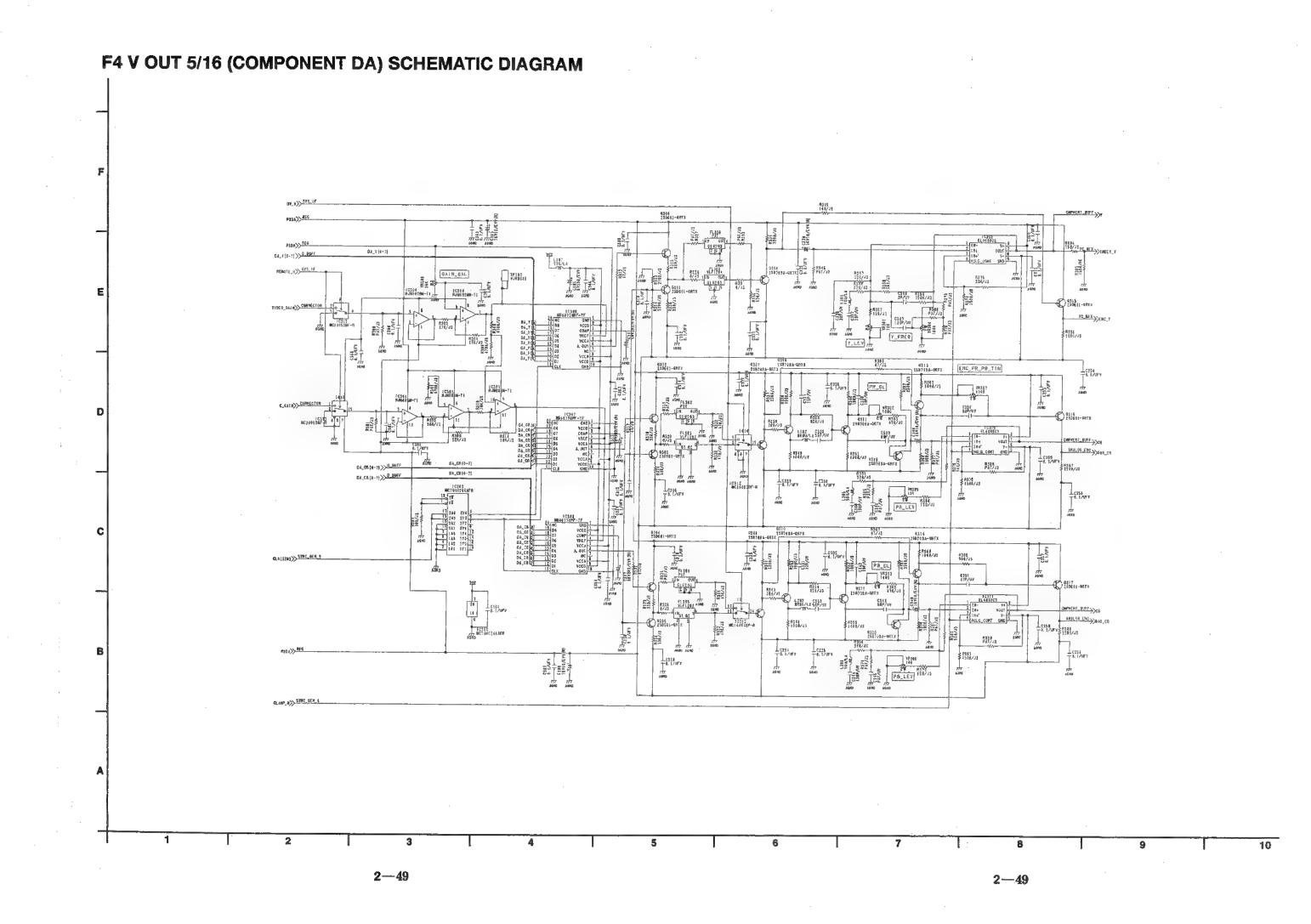
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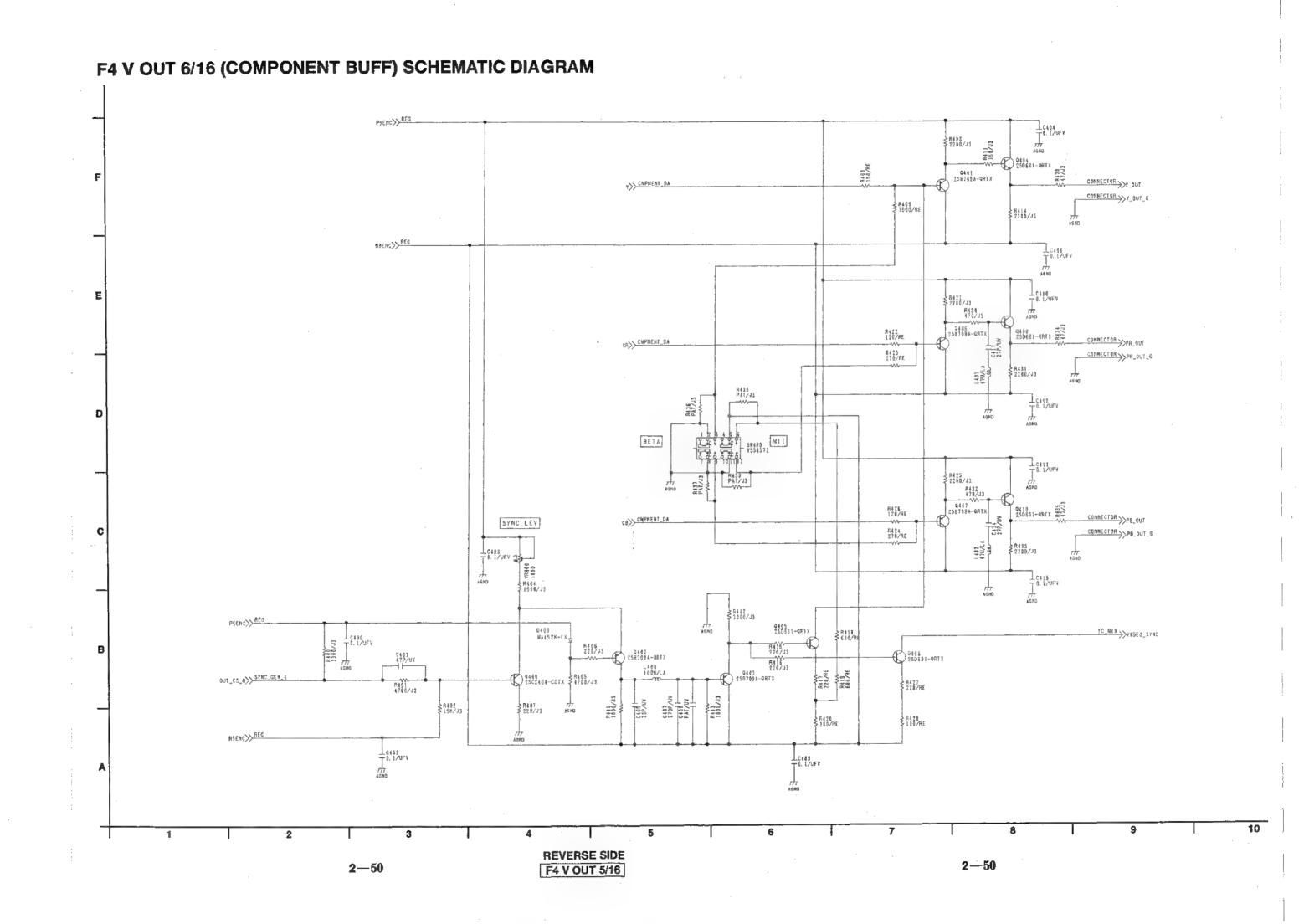




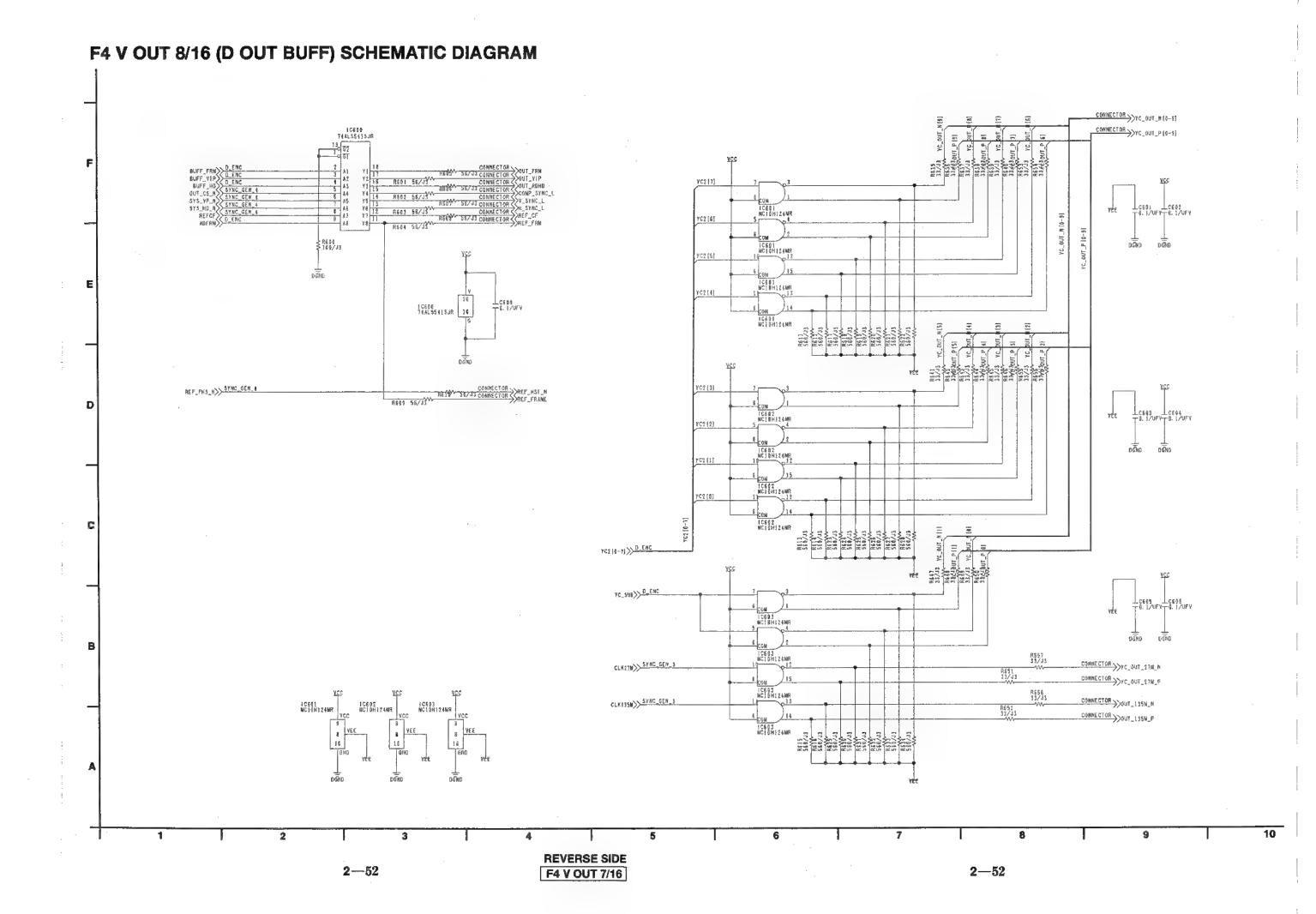


F4 V OUT 4/16 (D BUFF) SCHEMATIC DIAGRAM 10250 X062AP3002PL R250 R259 \$R261 1BK/J3 \$ FAT/J3 \$ PAT/J3 0252 16910/E99 (R)4 YFLOAT_M RIPRM SYNC_GERO_BUF_516*2] HD SYNC_GERO_BUF_516*2] HD SYNC_GERO_BUF_516*2] SYNC_GERO_BUF_516*0] CLM135%2 \$R264 \$PAT/J3 DQND D QND R263 10K/J3 10K/J3 10K/J3 0_80F_\$t\$[6-4] ₹ R270 ₹ 10K/J1 8271 FAT/J3 ¥ RZ73 ≸ 18K/J3 \$8275 \$817/J3 \$R271 \$PAT/J3 \$ 10K/J3 10251 UPD65844-026 DEC AY_Y [0-7] DEL AY_Y [0-7] >> 0_ENC 16254 VJR0648 00_8UFF (0-7) >> D_18_8UFF G0_BUFF [0-7] SA CRIC DA_CR(0-7] DA_CB[0-7] DA_Y[0-7] THOOM_L>> D_TN_BUFF #285 PAT/41 ROHO>> SYMC_GEN_4 RZ55 ₹RZ51 ₹PAI/J3 \$ #25 B \$ PA7/J3 R258 \$10K/J3 OND **REVERSE SIDE** 2-48 2 - 48F4 V OUT 3/16





F4 V OUT 7/16 (ANALOG ENC) SCHEMATIC DIAGRAM LS10 GRSU/LA CS48 27P/UV PSENC>> REG DAO_CR>> CMPNENT_DA C510 16 V 10/EVV (F) D_DM3 ENC_PR_LEY 35 LC513 T0.1/UFY 11/11 12/11 12/11 12/11 12/11 12/11 13/11 155 TON ±c567 ₹8571 17K/J3 DAO_CO>> CMPMENT_DA ENC_PB_LEY %5 8591 13 TES RS14 4700/J3 PR_BAL BURST_LEV AOBF SYNC_GEN_4 R\$44 2208/J3 LCSER TO. L/UFV 8545 2200/J3 C503 0508 V 25D661-QRI C504 R500 Q.1/VFY 828/J3 RSC>> SYNC.GEN_6 9500 9502 9503 258783A-9RTX 2582235-BCTX 258783A-9RTX | | R\$35 | 1580/J3 A511 1500/43 R549 [000/J3 PSENC>> REG LC533 R551 1000/J3 SY5_C5_P>> SYNC_GEN_A C524 R536 D. 1/UFY \$ 1500/J3 REMOTE_H>> SYS_IF R509 39M/J3 9513 25D101-QRTX HUE COMMECTOR 13 COK_H R583 1000/J3 0560 L541 0.1/UFY 47U/LA 10568 NJM9028M-T} TP500 4J90648 R508 PAT/J3 MC110538F-A CBK_H>> D_ENC MMENC>> REG PALP>> SYNC_GEN_4 _G517 __0.1/UFV Q512 F Z3C2ZJ5-BCTX C534 2505 82P/UY 2502235-BCTX 19543 VJR4646 NC74HC4455FR 9511 MA33-RT 11 2502295-BCTX TH500 R\$21 ERTOZEMLIB23 1500/J3 R\$22 1000/J3 83/J3 R\$ M1/13 54 25 € C536 0. 1/UFY 10576 10.1/UFY R524 \$80/J3 \$8503 \$1000/J3 77 MSENC>> REG

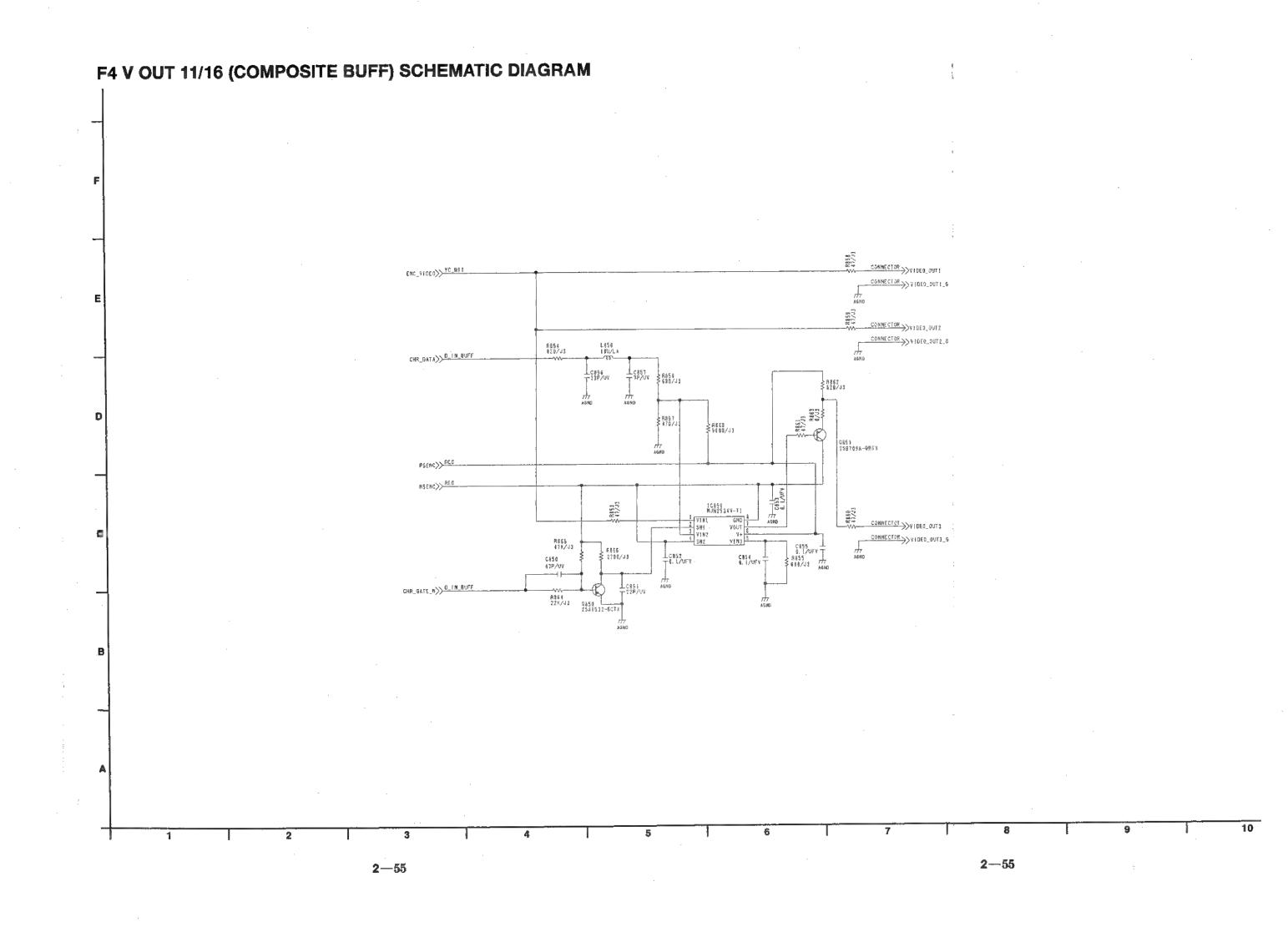


F4 V OUT 9/16 (D ENC) SCHEMATIC DIAGRAM CLK135M4 REFOR SYNC SEN 4 NCAK SYNC GEN 4 DELAY_Y [0-7] 0_8UFF >> OEL AY_Y (0-7) ROFRM>> SYNC_GEN_4 C6_8UFF (4-7] >> 0_1 M_8UFF CQ_BUFF[[-7] R700 \$1000/J3 \$1000/J3 P3 YJF1233T |FRM>> D_IN_BUFF RDHD>> SYNC_GER_4 YNBK SYNC_GEN_4 80H**0** TP700 VJR0646 E5#37788261**89** YC2 [8-7] GUT_BF>> SYNC_GEN_4 D_IN_BUFF >>ADYNSK_N Y_MUTE_H>> D_IM_SUFF Y0_8UFF (0-2) 0_EN_8UFF Y0_8UFF (0-6_HD SYNC_GER_1 >> FLOAT_RST 18742 V3R45(\$ B_MEP D_QUT_BUFF_HD TP763 YJR0646 O_OUT_BUFF_YIP SDET>> SYNC_GEN_S AA-H>> 242-1E ANALOG_ENC >> COK_H O_GUT_BUFF

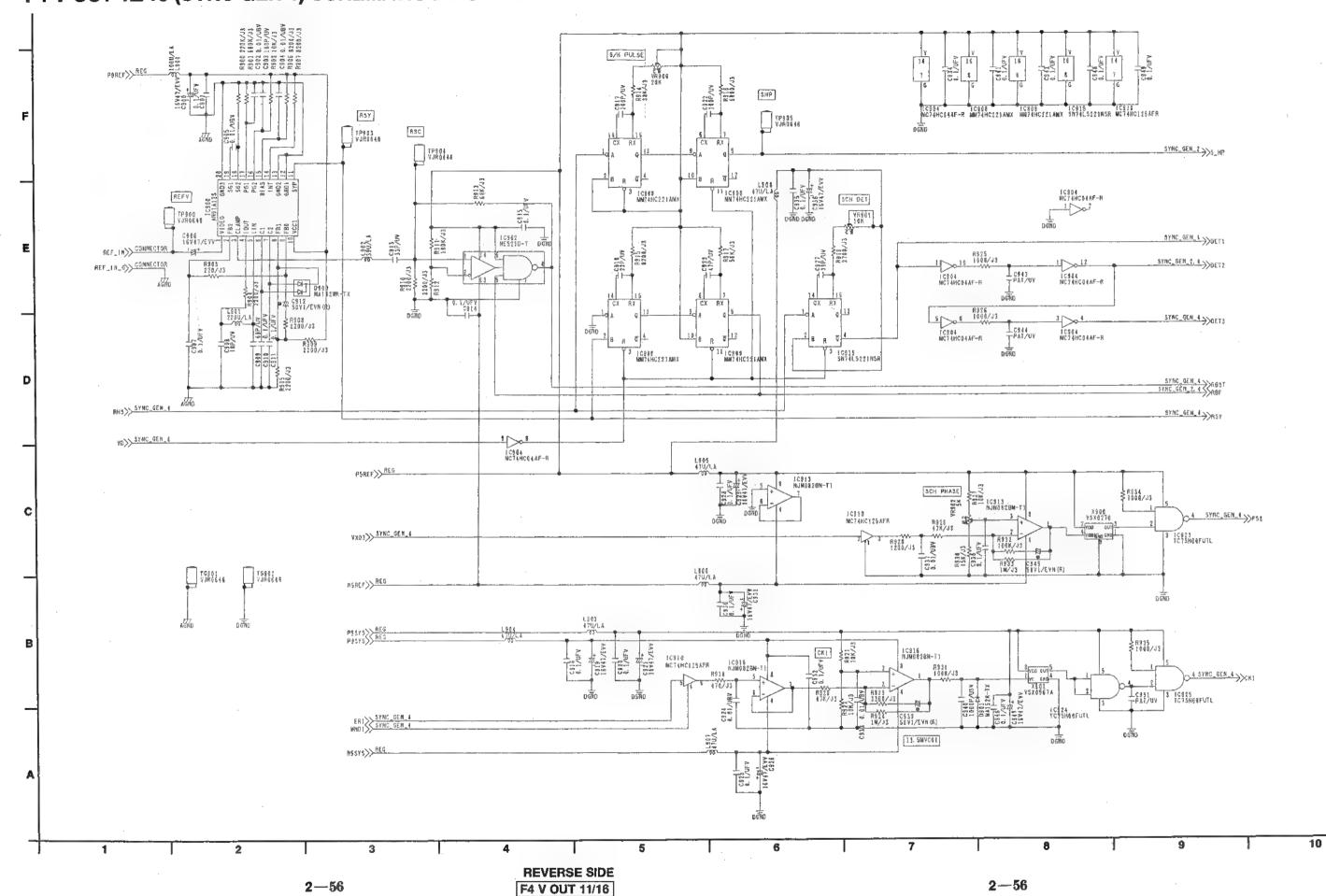
2-53

2 - 53

F4 V OUT 10/16 (YC MIX) SCHEMATIC DIAGRAM Q888 258709A-QRTX TRAP_H>> SYS_[F R847 \$1500/J3 ¥ R852 ≸ 2200/J3 ENC_C>> AMALDG_ENC 180/J3 9107 258709A-QRTX \$ 8846 \$ 477/513 \$ RB49 PSENC>> REG ENC_Y>> CHPMENT_DA T C835 1 M+ VOUT 4 HOLD CONT GND 10882 NJM2534V-F1 11 6ND 1 40UF LC899 T0.1/UFY C825 0.17UFV ENC_Y_LEV RB72 PAT/J3 PAT/UN2211-TX R803 1000/J3 A841 > 3500/JS GIII C805 R804 2P/UV 180K/J3 CE27 ENC_YC_TIM IC804 NJN2534V-T1 C806 120P/UV R801 #20/J3 YOK_N 22P/JJV AMA 8862 158/43 TP80G \$JRDE46) 2803 oj - GRTX 🔏 5W2 ENC_Y_FREQ MSENC>> REG2 15P/UV ₹834 1500/J3 777 4680 R845 W 180/J3 VIDEO_SYNC CNPMENT_BUFF SYNC_GEN_4 VBK_N D_ENC ₹2700/J3 D AUND THOE X_Y CHPNENT_DA IDX_H>> D_ENC H_KOT R812 47/J3 COMMECTOR ENCY_OUT_G CONNECTOR ENCC_OUT_G R834 820/J3 P9ENC>>> REG R\$23 10K/J3 SETUP_LEV R\$35 820/J3 TCB1/UFV ICESS ADRIBAR-R 181/33 ON TO THE TO THE RB27 PAT/J3 TG802 R&13 56K/J3 CMPSITE_BUFF >>ENC_VIDEO #871 12K/J3 REMOTE_H> SYS_IF R826 10805 AD828AR-P ICSSI NUMBEZEM-T R810 56K/J3 R839 820/J3 R870 18K/J3 R#42 1248/33 TC891 NJN982BM-TI В 101005 NJN084M-T1 \$P#1/J3 824/13 R840 824/13 上C#26 丁22P/UY M9EMC>> REG 10804 +0.1/VFV 2 REVERSE SIDE 2-54 F4 V OUT 9/16 2 - 54



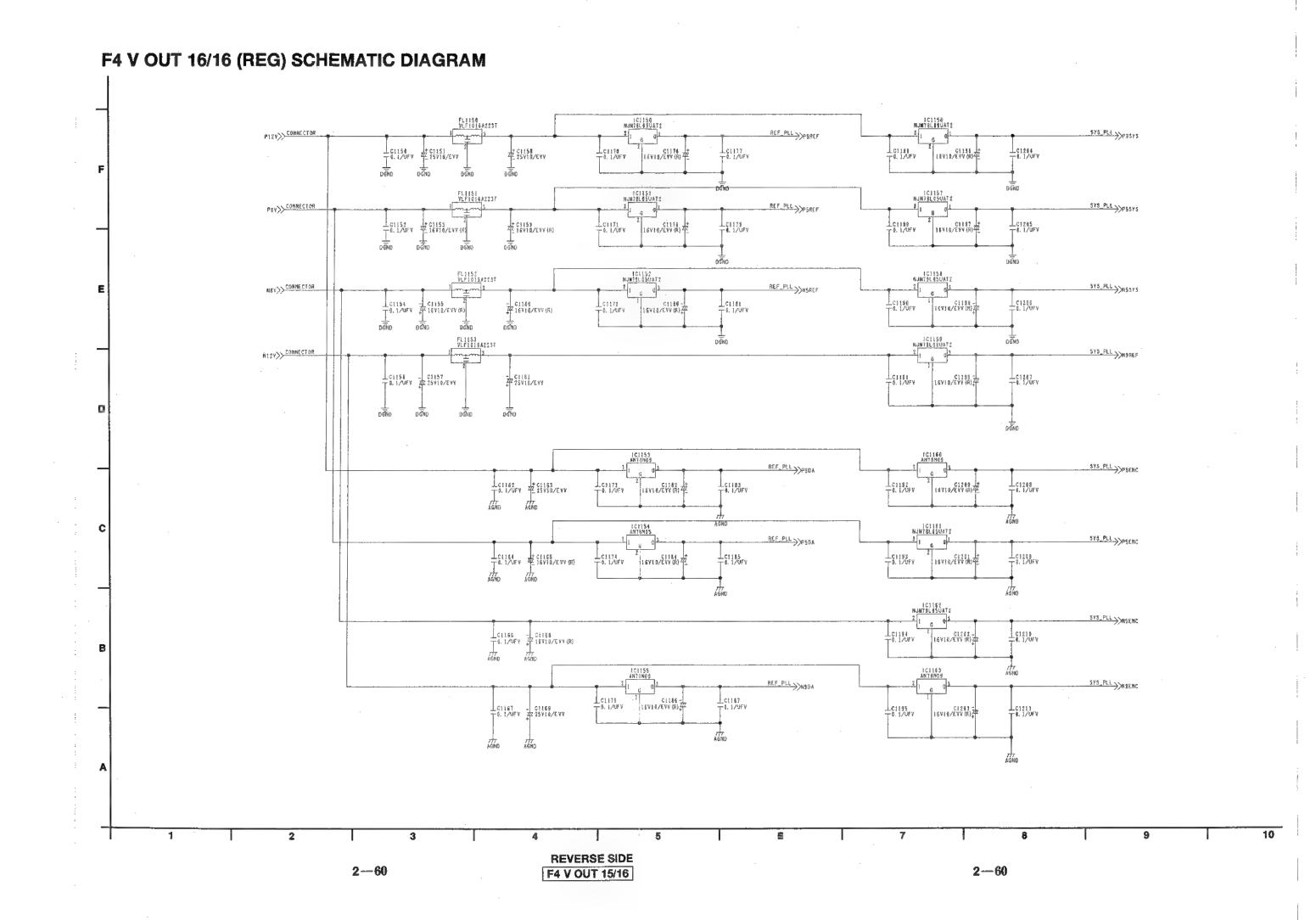
F4 V OUT 12/16 (SYNC GEN 1) SCHEMATIC DIAGRAM



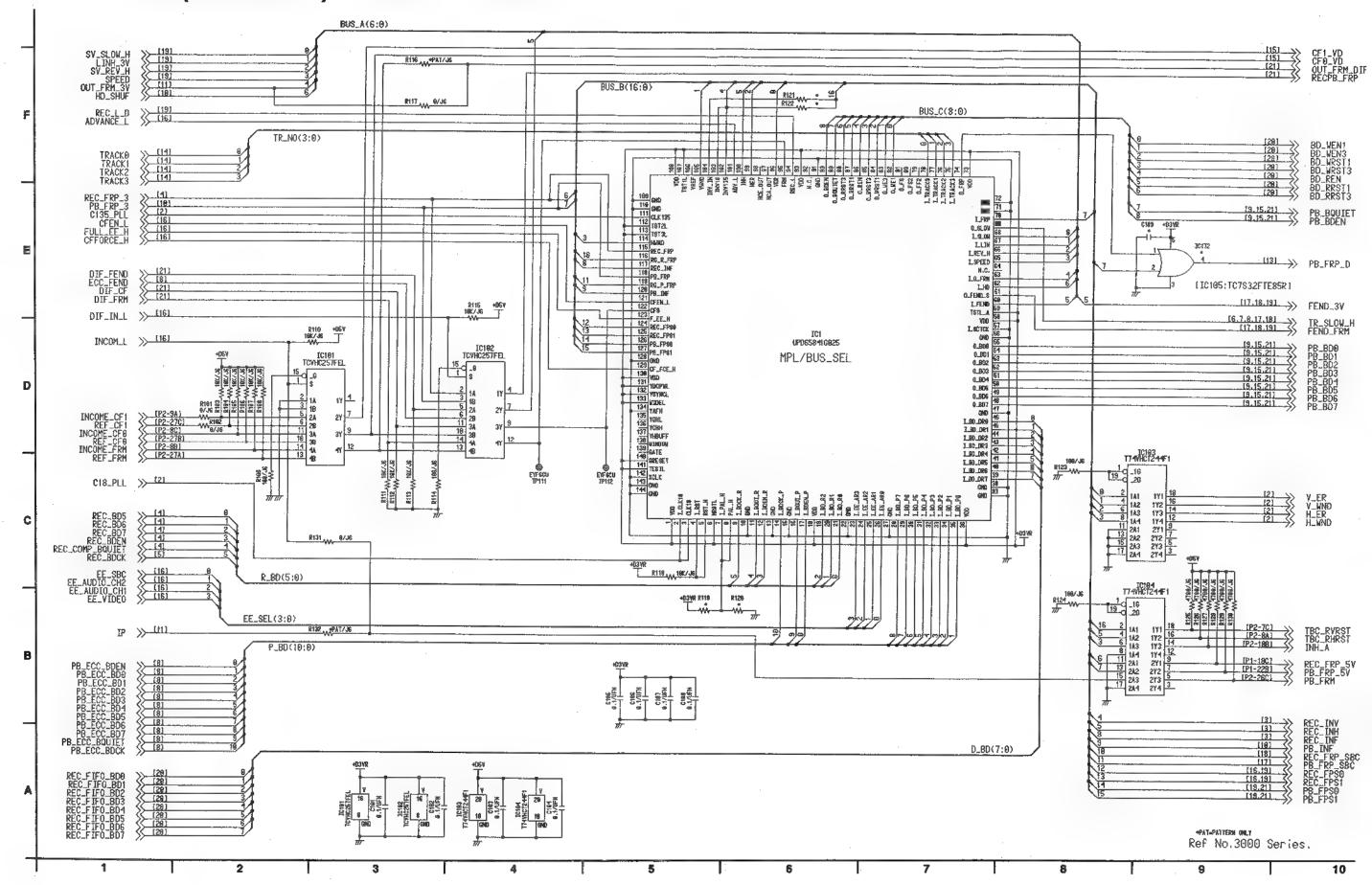
F4 V OUT 13/16 (SYNC GEN 2) SCHEMATIC DIAGRAM IC1003 NJM082BN-T1 | C1083 | MJW982BM-T1 ₹1030 1000/13 10962 NES210-T R1014 R1015 1M/J3 1000/J3 SYNC_GEM_4 VC) 400 ECTOIS TC75H01FUTL FS PHASE MSREF>> REG PSREF>> REG CC 1C1002 MC14053BF-R VEE L1004 (7U/LA \$ PAT/J3 101000 NES210-T 10915 501419221NSR LALIO>> SYNC_SEM_4 SYNC GEN 4 [C1005 NJM084M-T] POREF >> REG R1021 \$Y\$_\$C_FINE[0-7}>> \$Y5_|F 2-57 2 - 57

F4 V OUT 14/16 (SYNC GEN 3) SCHEMATIC DIAGRAM LC1954 T0. 1/UFY R1051 \$10k/J3 VIDEO_PHASE IC910 MC74HC125AFR R1050 470/J3 C1650 1000P/USV C1057 2110/13 O_ENC >> GLK27M1 DGND NSSYS>> REG IC:057 SNT4AS:4NST 81956 D_OUT_BUFF >>CLK135M 101 SN74A574NSR ₹1061 1000/J3 101058 SH7445244MSR DGND REVERSE SIDE 2-58 2-58 F4 V OUT 13/16

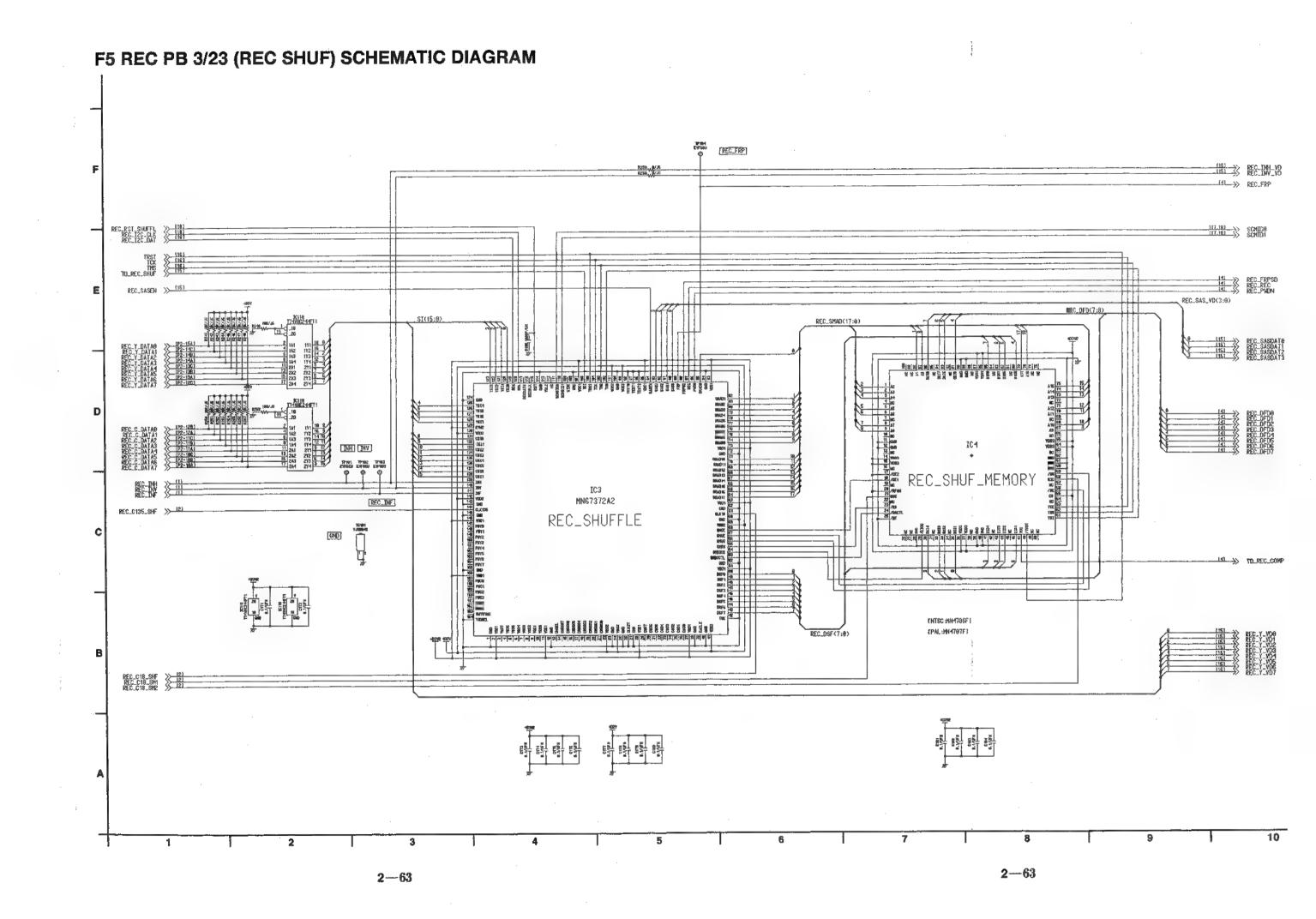
F4 V OUT 15/16 (SYNC GEN 4) SCHEMATIC DIAGRAM 16 101107 NC749C01AF-R 5YS_1F SCH_ON_F MC74HC04AF-R13 SYS_SC_COARSEO R1115 VFLOAT_H>> SY5_FF ROFAM LR1118 ≶PAT/J3 FLOAT_RST>> D_ENC D BUFF HO D BUFF YP D ENC. D QUT BUFF ARFCF D QUT BUFF ARFCF BF_PHASE PSREF>>REG RECE VURGE46 ADS2 ADS1 SSEL VSP 56_BUS (1-34) 2 ANALOG ENC > ADBF SN74LS221NSR ₹ 81120 \$ 6/33 EC1114 5874L5221M3R \$8117Ja MGD1 83 H5EL1 85 H5EL0 85 LSEL 84 EQP 83 SELD 82 SELC 81 н РИЛЗЕ HC74HC04AF-R SYNC_GEN_2 >> ALID 50 BUS [26] 50 BUS [27] 56_BU\$ [16] TGT108 VJROS46 SYNC_GEN_1 (RSY HR 2 5 0 6 1C1187 MC74HC04AF-R ANALOG_ENC >>RSC TP1101 VJR0646 \$ R1122 \$ 101K/J3 TC1103 SN74E5221NSA 1 CHPMENT_DANAHALOG_ENC, YC_MIX 22 16 V LO /E VV (8) IC1114 MC74HC00AF-R FEREF>> REG DISNO SYS_H 56_BU5[1-34] PAT SYMC_GEN_1 >>RHS 575_H(6-7]>> 575_IF VJR0545 TP1102 E_BUT_BUFF >>SYS_HG_N YJR4645 TP1103 R1184 5600/J3 R1101 18K/J3 MC74HC04AF-Rg อดักอ 100 1500/J3 1 CT 83 R1126 9. 17UFV 108733 ICIION NC74HC04AF-R DQND T 101114 MC74HCFOAF-R 11 O ED CMPNENT_BUFF, D_OUF_BUFF OUT_CS_I 13 0.12 ICIEOS ANALOG_ENC >>SYS_CS_P MC74HC04AF-R ICIIOS MC74HCD4AF-R VARIOUS 49 TPE104 CSI NSREF >> REG IC1113 MC74HC244AFR ICTIOT NCTAHCOMAF-R ICL118 SM74L3221N5R



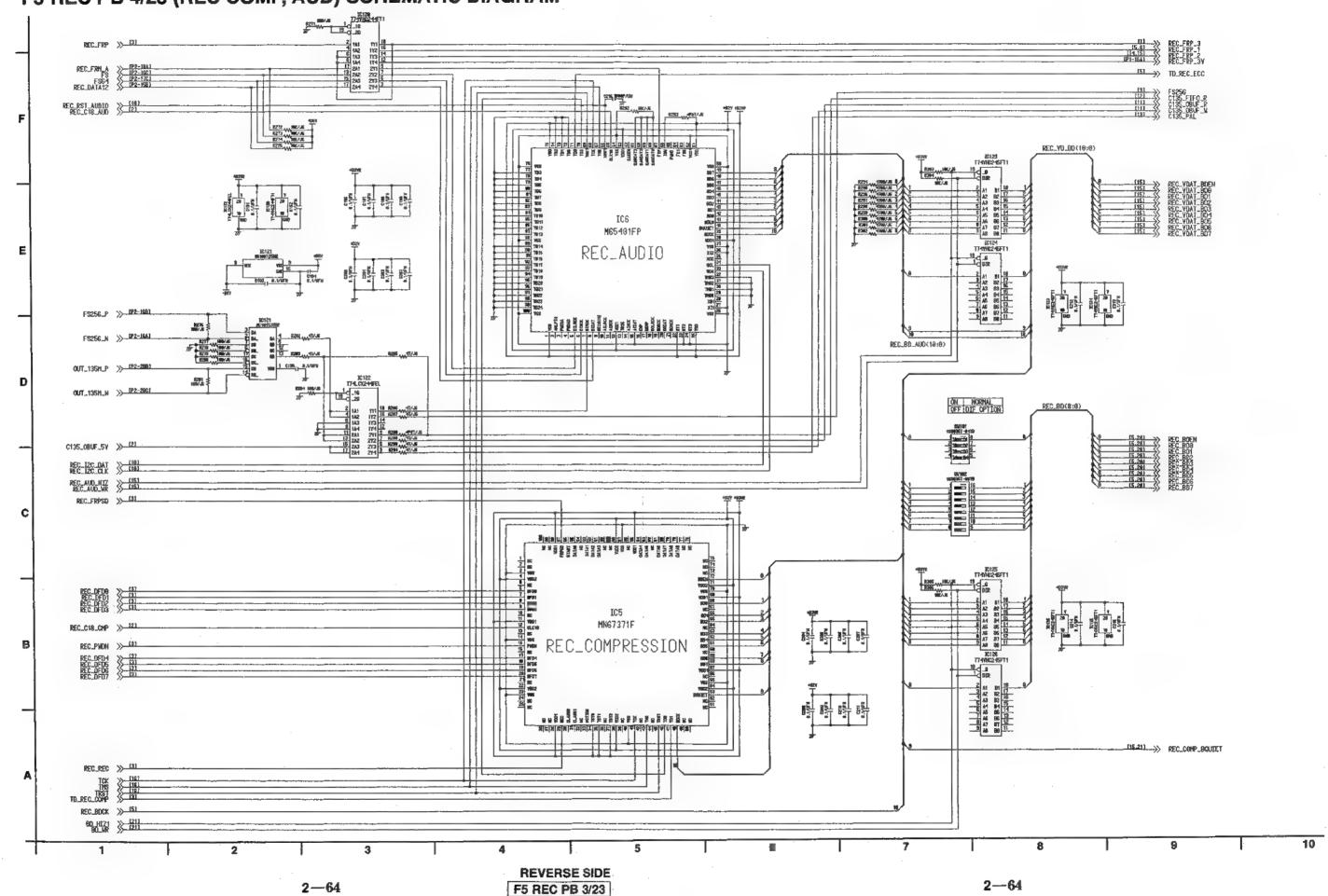
F5 REC PB 1/23 (MPL/BUS SEL) SCHEMATIC DIAGRAM

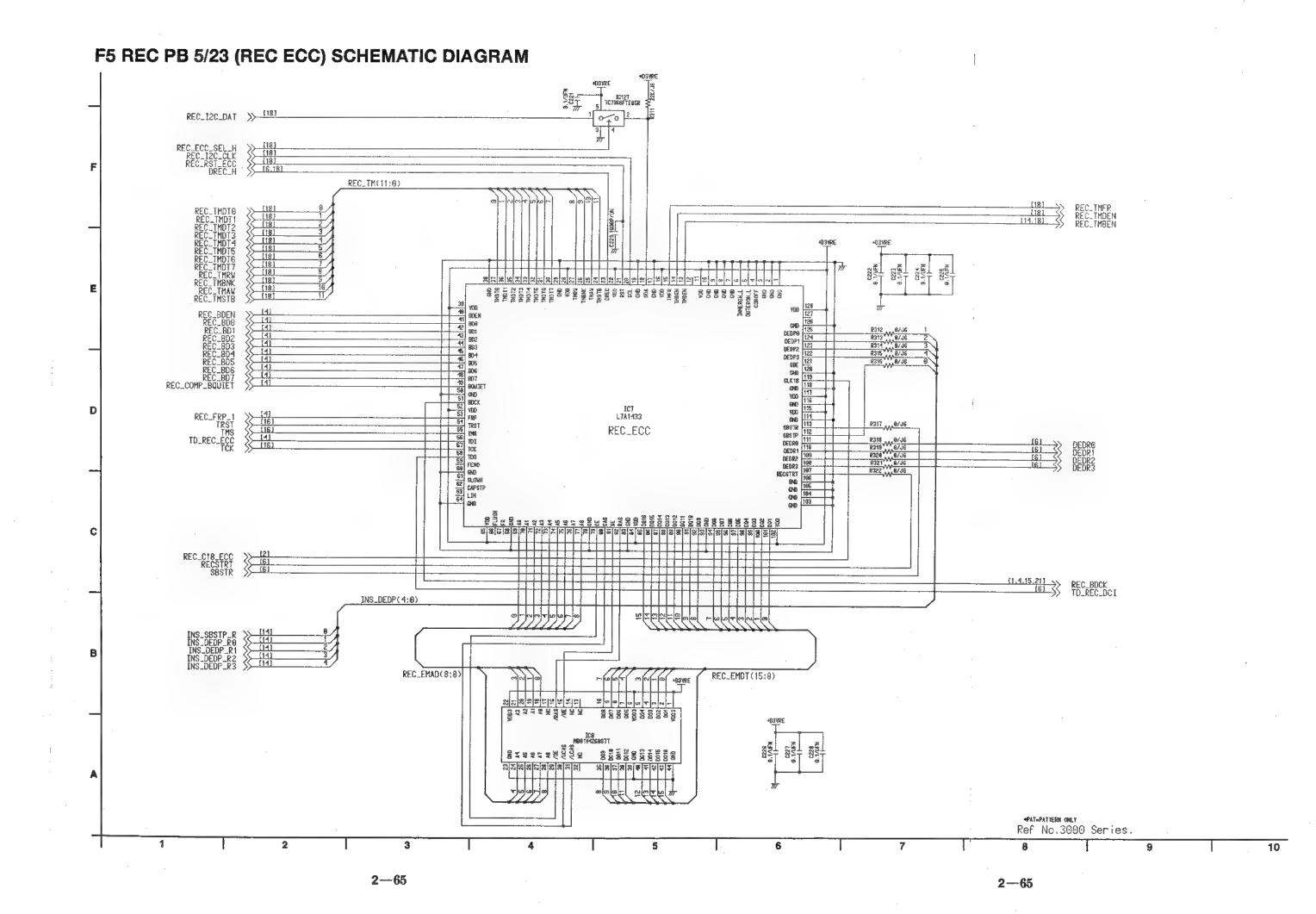


F5 REC PB 2/23 (PLL) SCHEMATIC DIAGRAM 13.5MHz IC113 T74LCX244FEL 1.181 17U/LC SHIELD CASE TBC_RCK_P TBC_RCK_N V_ER >> [1] R141 8/J6 W-V_WND >> (1) R149 WA1H/J6 IC115 174LCX244FEL L184~47U/LC 18MHz IC105 NC74HC125AFR X162 VSX0789₄ H_ER >> [1] 1668P/UN H_WND >>_______ IC116 T74LCX244FEL [IC170:TC7S66FTE85R] [8] [9] [9] [10] [10] [10] [20] *PAT*PATTERN ONLY Ref No.3000 Series. **REVERSE SIDE** 2-62 2 - 62F5 REC PB 1/23

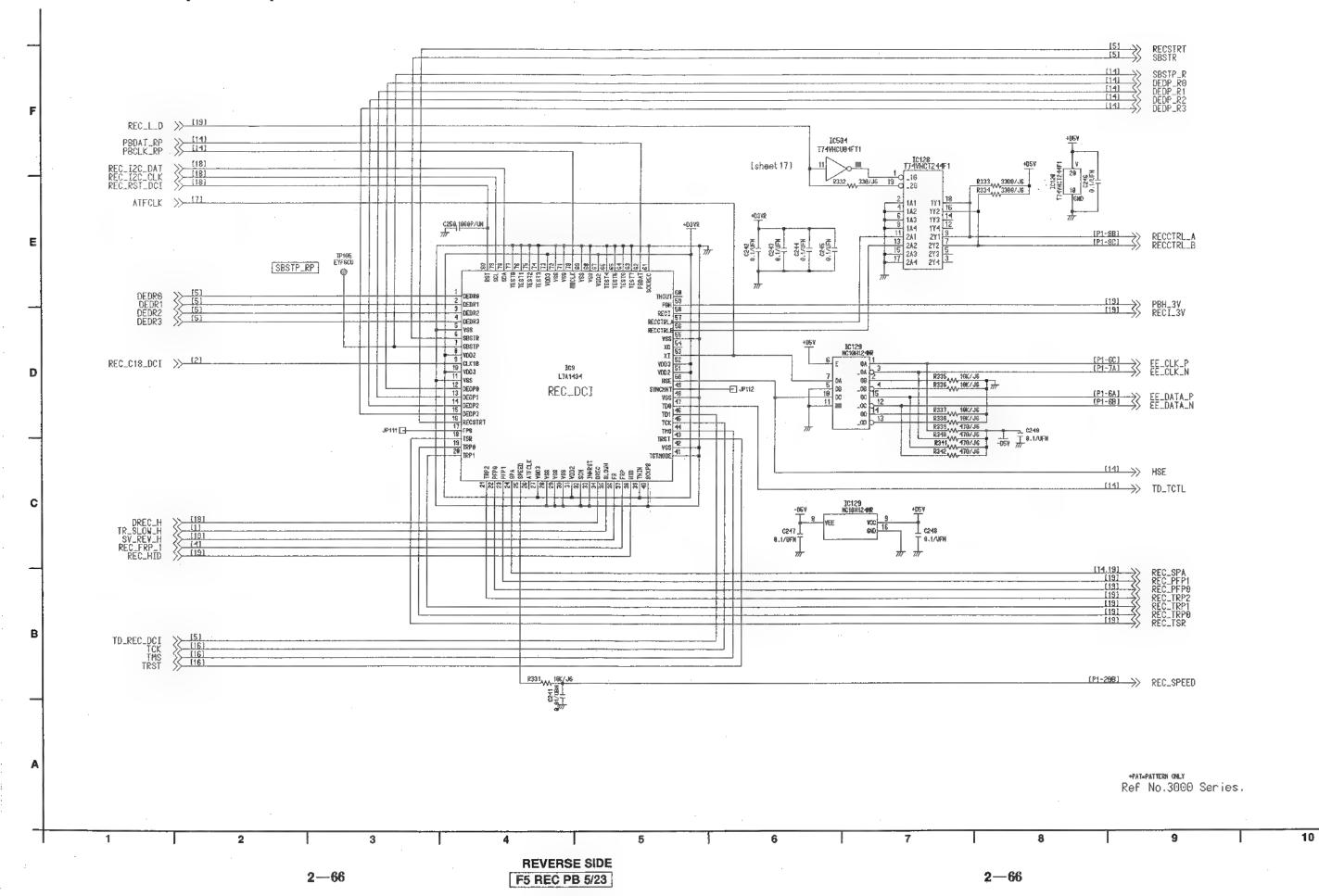


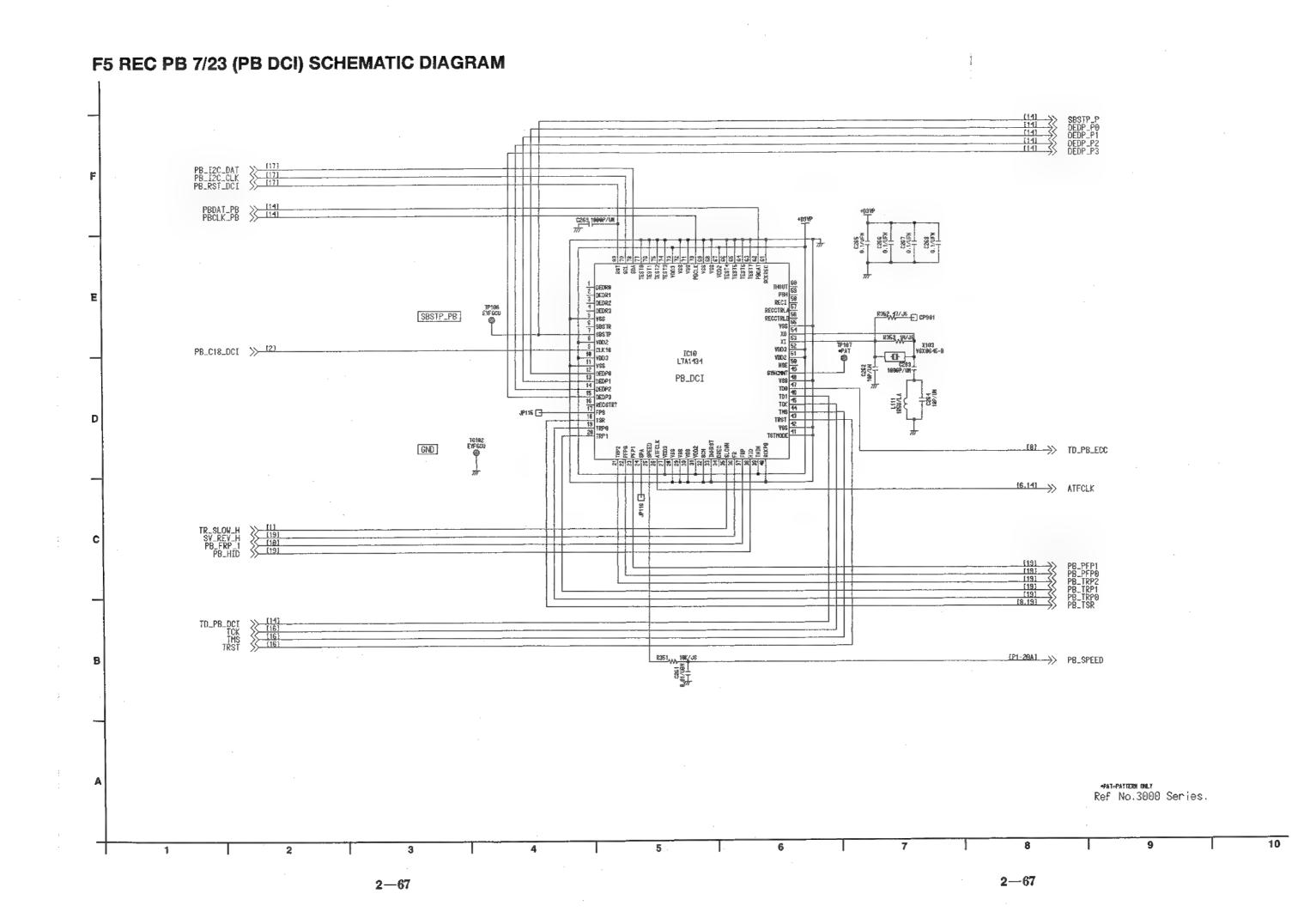
F5 REC PB 4/23 (REC COMP, AUD) SCHEMATIC DIAGRAM



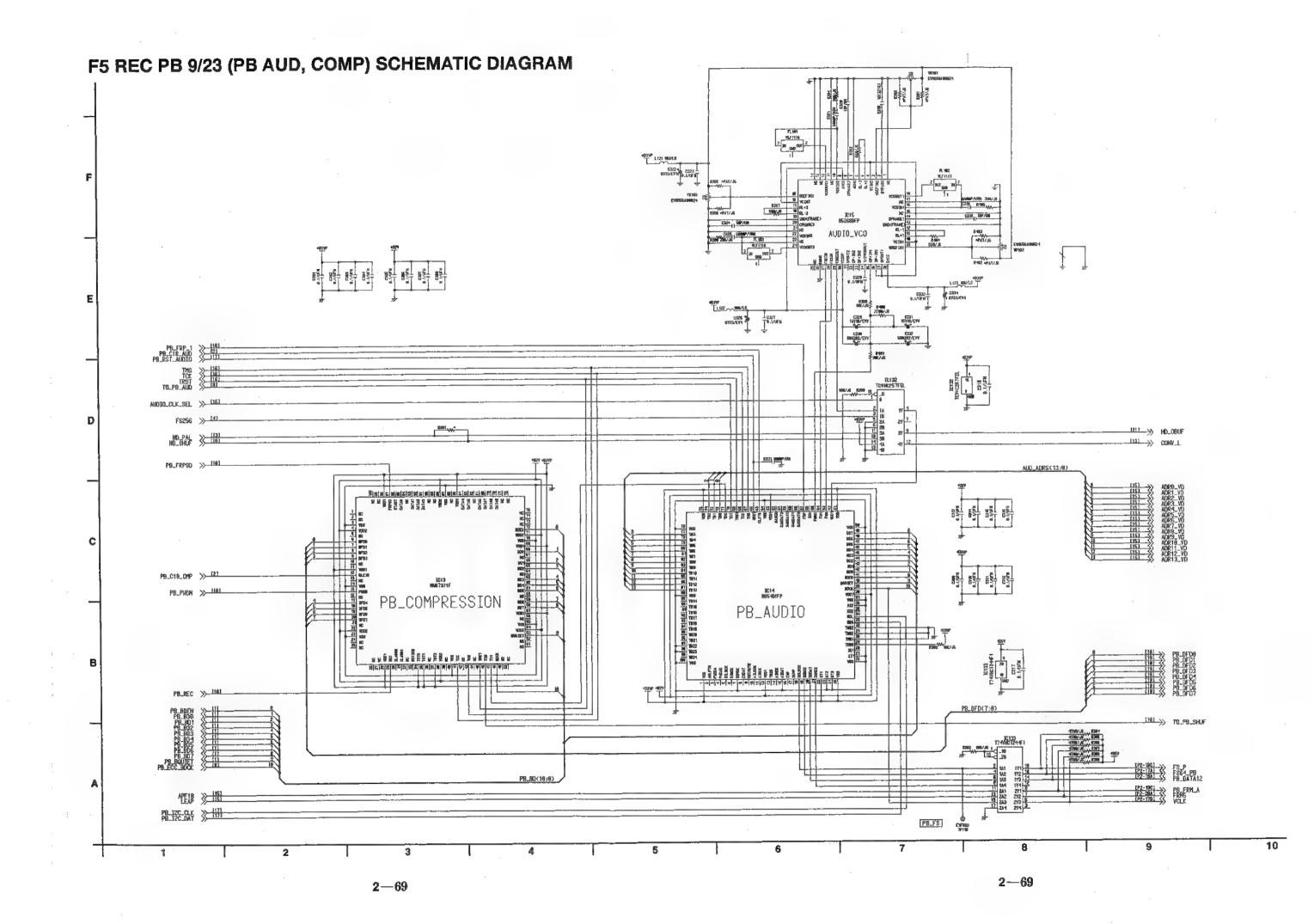


F5 REC PB 6/23 (REC DCI) SCHEMATIC DIAGRAM





F5 REC PB 8/23 (PB ECC) SCHEMATIC DIAGRAM PB_I2C_DAT >> [17] (17) (17) (17) (17) (17) (17) (17) (18) PB_ECC_SEL_H CONCEALON_L OUTERON_L INNERON_L PB_I2C_CLK PB_RST_ECC PB_TSR DEDP(4:0) R374 AZTK/JE B PB_C18_ECC >> 121 PB_TM(11:0) P8_TMDT0 PB_TMDT1 P8_TMDT2 P8_TMDT2 P8_TMDT4 P8_TMDT5 P8_TMDT6 P8_TMDT6 P8_TMPT7 PB_TMRW PB_TMSNK PB_TMSTB DEDP9 DEDP1 102 44 10 SPE GND CLX 18 GND VDD GND VDD GND SESTR SESTP DEDR9 DEDR1 IC11 L7A1433 PB_ECC [EPM783200-3] AUDIO_MUTE (VSI2196) TR_SLON_H >> [1] SBC_SLOW_H >>-[f7] FEND SBE LINH_3V >> 1191 TP108 EXF6CU 18 174WHC74FTP ______SBE SV_REV_H >>-[19]_ 11.211 >> ECC_FEND SBC_REV_H >> [17] > CLK PB_HID >>-- [19] RST_DFF_L >> [16] PB_EMAD(8:0) PB_EMDT(15:0) FLUSH_3V >> (191 ECC_SLOW_ D016 D014 D014 D014 D016 D016 ECC_REV_H ◆PAT=PATTERN ONLY Ref No.3000 Series. **REVERSE SIDE** 2-68 2--68 F5 REC PB 7/23



F5 REC PB 10/23 (PB SHUF) SCHEMATIC DIAGRAM PB_FRP 191 PB_FRPS0 191 PB_REC 191 PB_PWDN ENTSC: MN-1796F J [PAL:MN4787F] PB_SHUF_MEMORY 1016 HNG7372A2 PB_SHUFFLE 80(15:0)

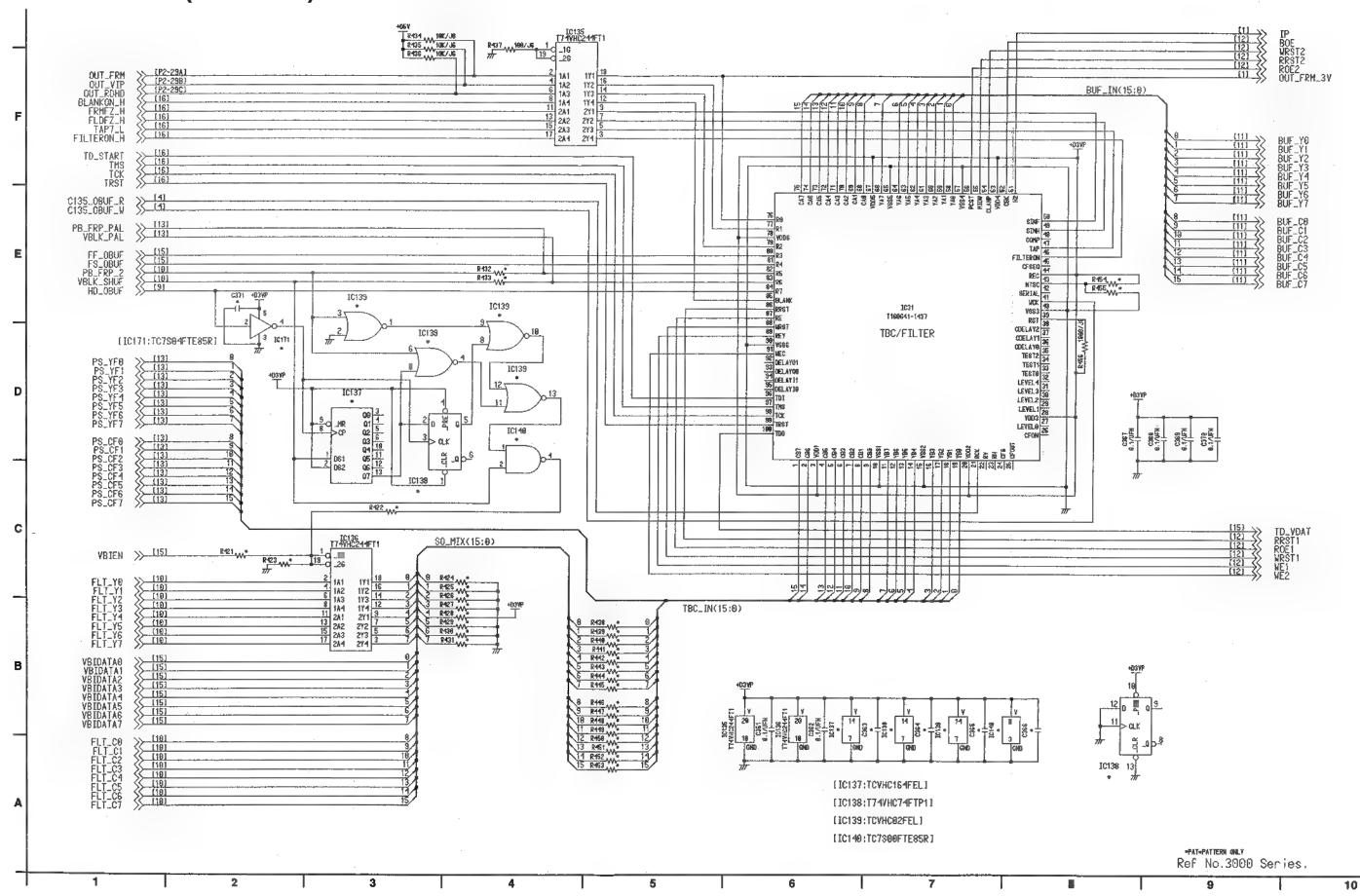
2-70

REVERSE SIDE

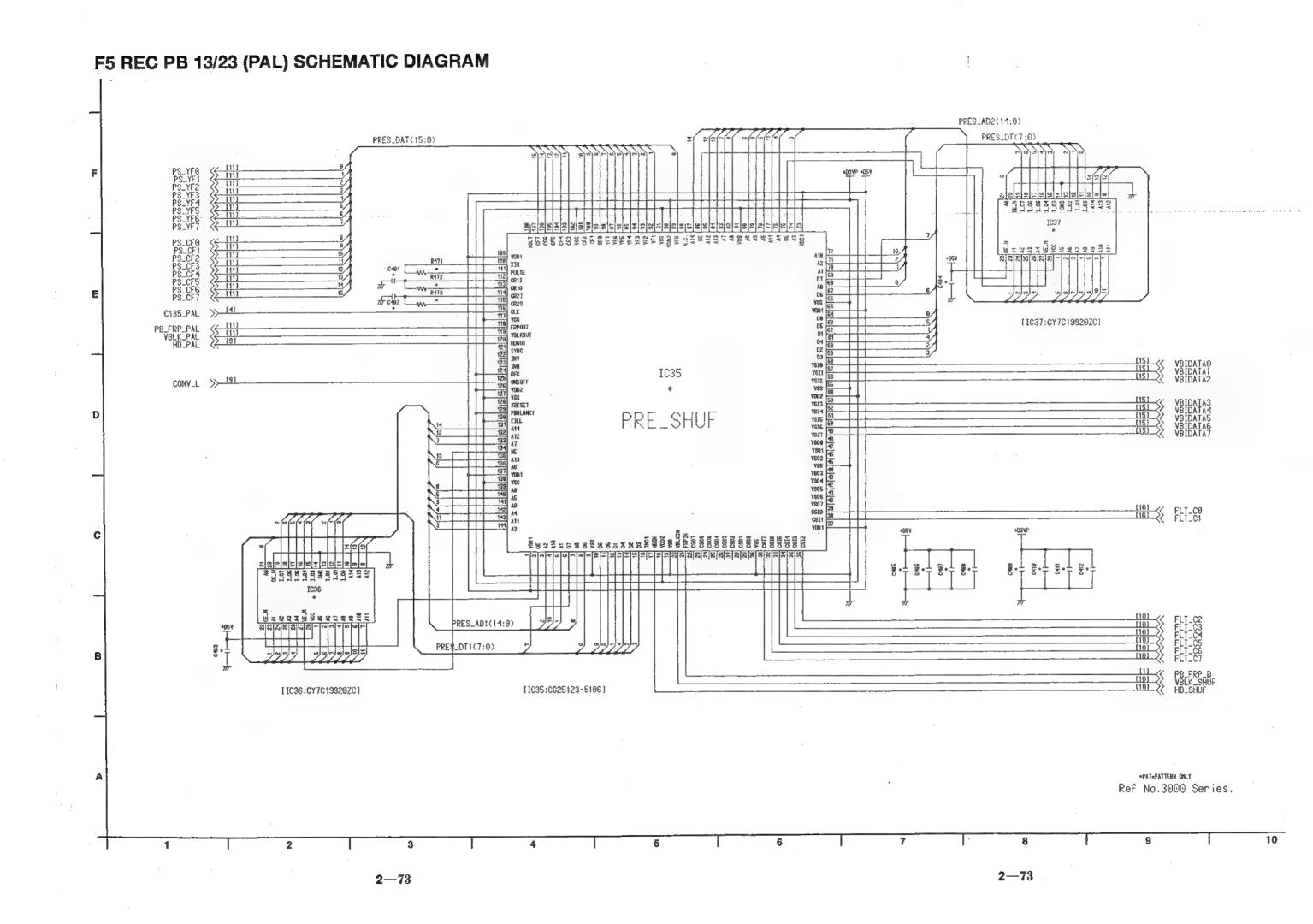
F5 REC PB 9/23

2-70

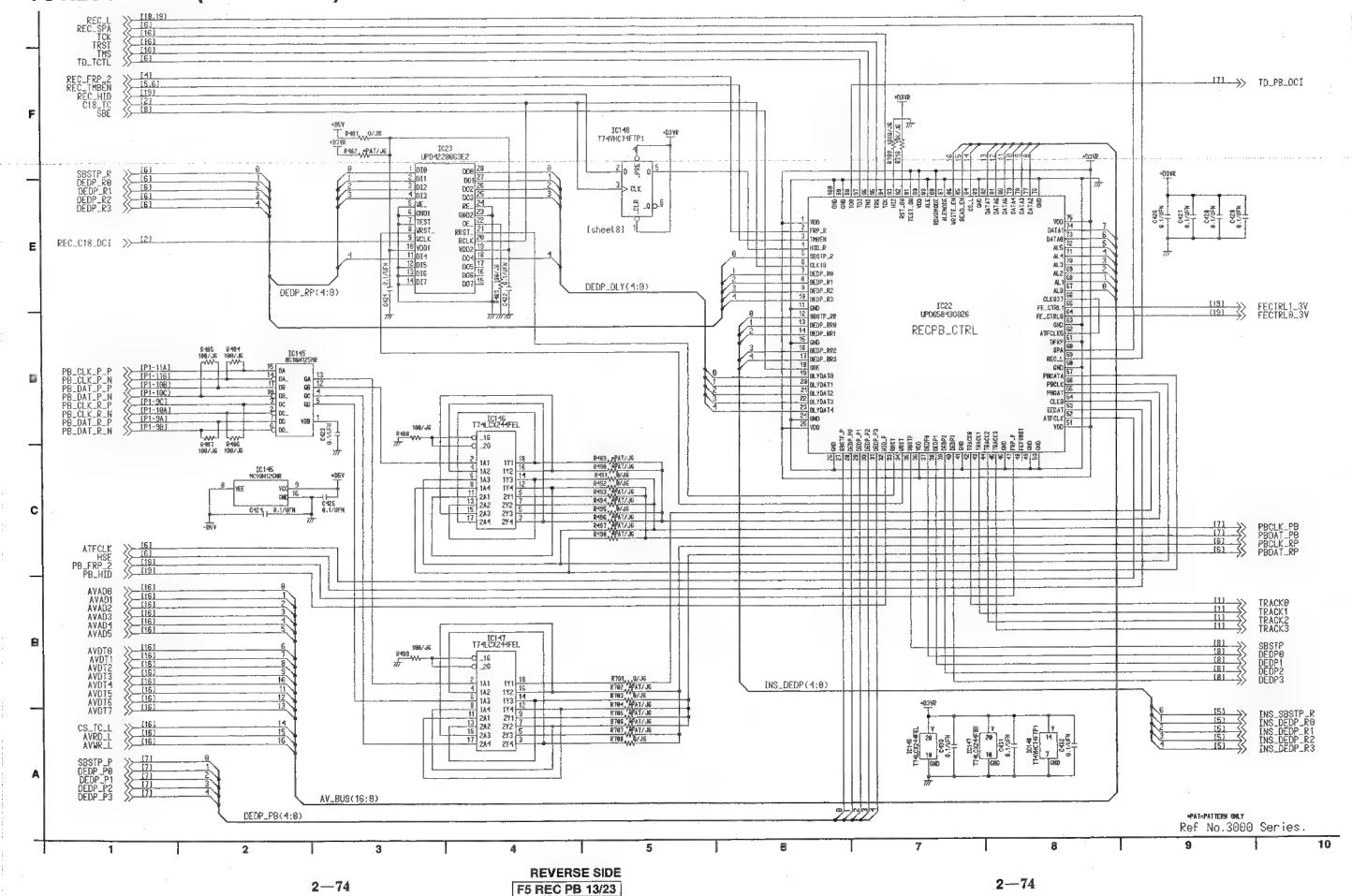
F5 REC PB 11/23 (TBC/FILTER) SCHEMATIC DIAGRAM



F5 REC PB 12/23 (OUT BUFF2) SCHEMATIC DIAGRAM BUFF_IN(15:9) IC143 74ALS541SJR R462 AAA PAT/JG BUFF_OUT(15:0) C135_FIFO_R RRST1 ROE1 RRST2 ROE2 BOE

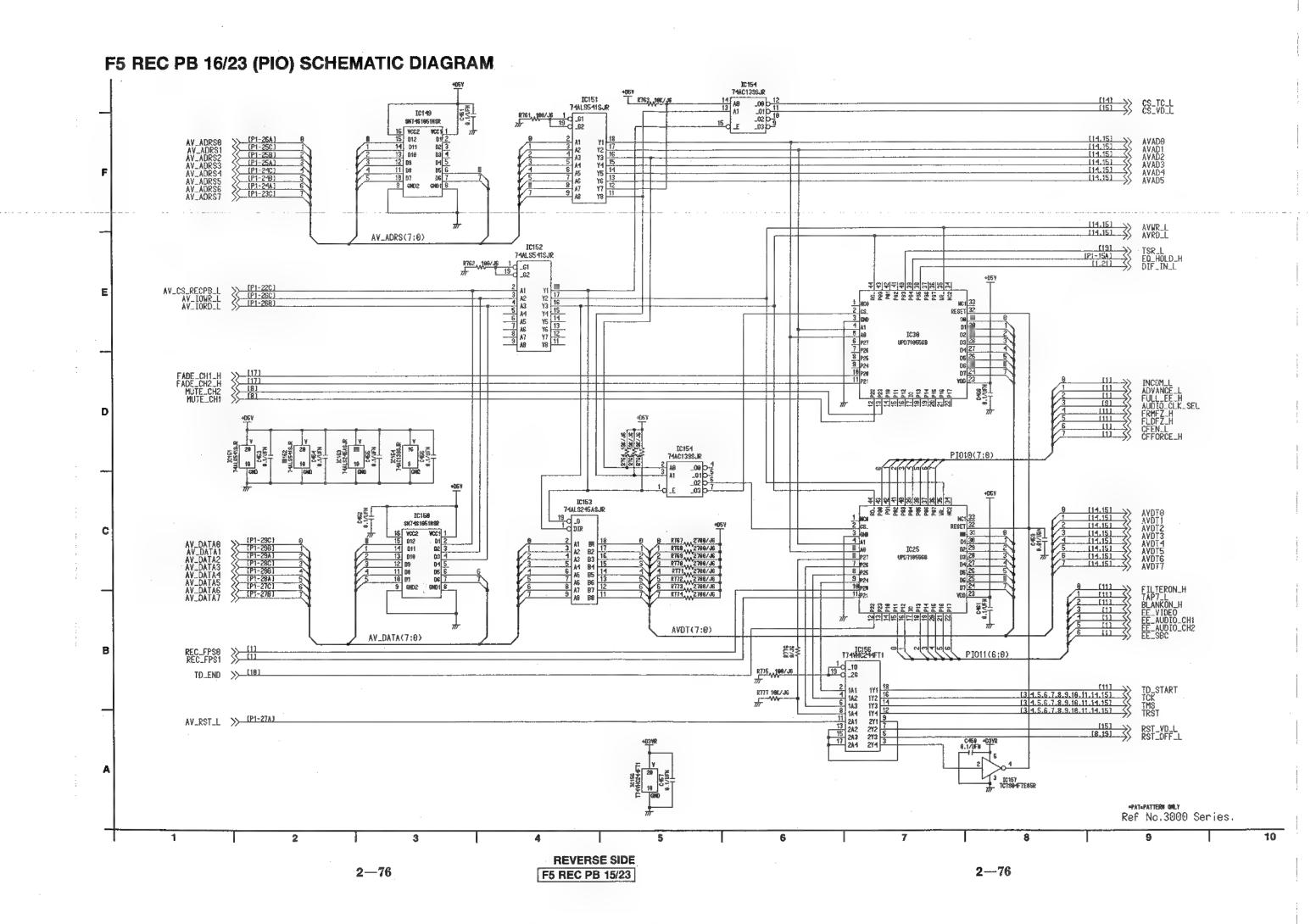


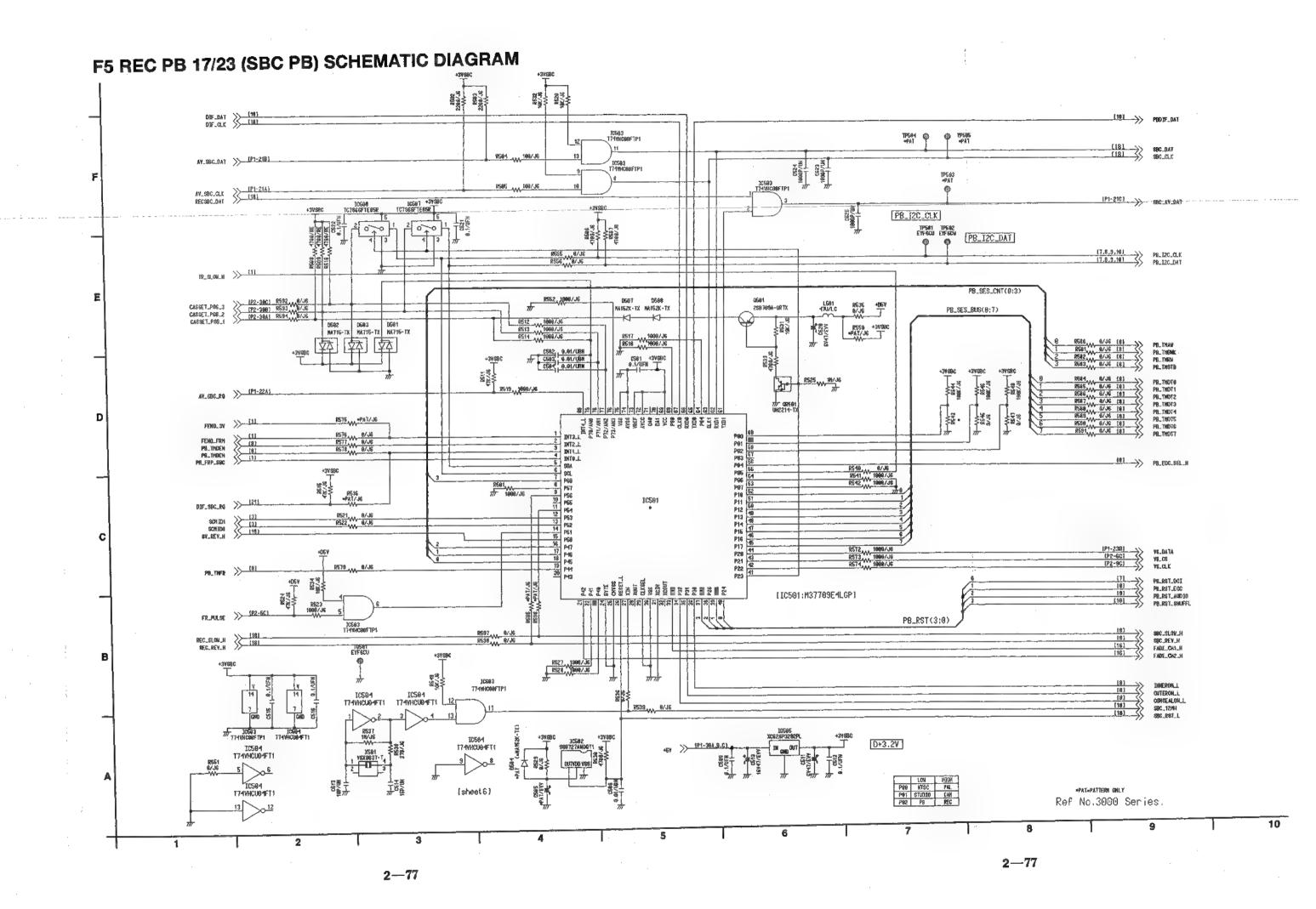
F5 REC PB 14/23 (REC PB CTRL) SCHEMATIC DIAGRAM

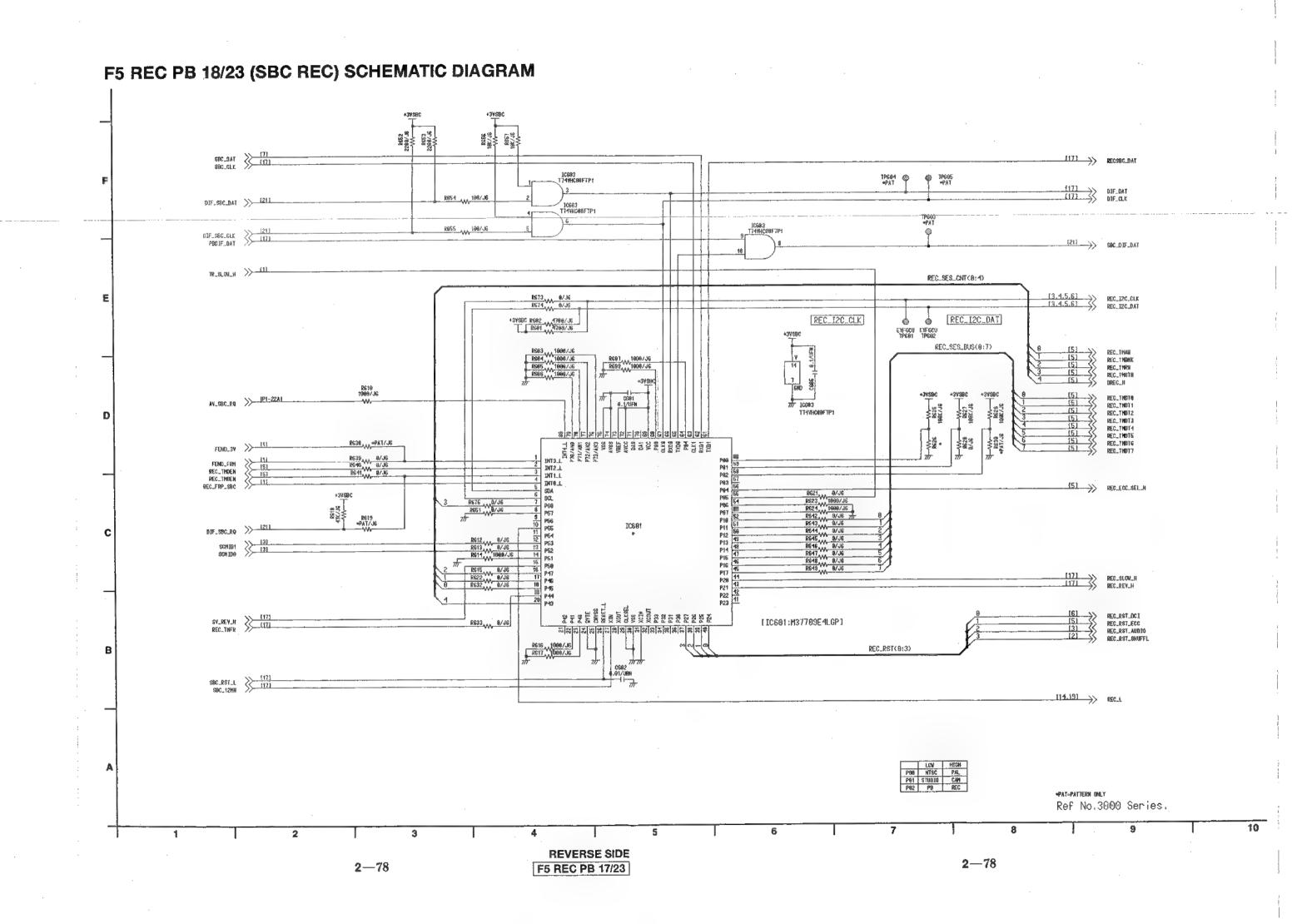


F5 REC PB 15/23 (VIDEO DATA) SCHEMATIC DIAGRAM (10) << P8_FRP_2 TCK TRST TMS TD_VDAT TD_REC_SHUF RST_VD_L REC_VDAT_BBEN REC_BDCK REC_COMP_BQUIET | [16] | [16] | [16] | [11] | [13] | [16] | [14] | [15] | [14] PB_BO_VD(7:0) PB_BD0 PB_BD1 PB_BD2 PB_BD3 PB_BD4 PB_BD5 PB_BD6 PB_BD7 PB_SAS_V(4:0) REC_BD_VD(7:0) K123 MBK\16 REC_VDAT_BDO REC_VDAT_BD1 REC_VDAT_BD2 REC_VDAT_BD3 REC_VDAT_BD3 REC_VDAT_BD5 REC_VDAT_BD6 REC_VDAT_BD6 REC_VDAT_BD7 [4] [4] [1] [4] [4] [4] [4] (10) (10) (10) (10) (10) (11) (11) YBD5 163 TST3 193 TST ### WOOS | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 150 | 1 JP137 REC_AUD_HIZ (11) ADR0_YD ADR1_YD ADR1_YD ADR2_YD ADR3_YD ADR5_YD ADR5_YD ADR5_YD ADR1_YD ADR3_YD ADR10_YD ADR10_YD ADR112_YD ADR13_YD ADR13_YD REC_Ct8_VD >> [2] UPD658680822 [2] << PB_C135_VD REC_C135_VD >> [2] VIDEO_DATA AUD_ADRS(13:0) 191 X LEAP 193 APF 18 ---E) JP131 CF0_VD CF1_VD REC_Y_VD0 REC_Y_VD1 REC_Y_VD2 REC_Y_VD3 REC_Y_VD4 REC_Y_VD5 REC_Y_VD6 REC_Y_VD7 AVDTE AVDTI AVDT3 AVDT4 AVDT5 AVDT6 AVDT7 REPARAMENTAL STATES OF THE STA V_DATA(7:0) K1339 40K\1e 101 W BLK_VD HD_VD AV_DT(7:0) -USI → VBIEN AV_AD_VD(5:9) -PAT-PATTERN ONLY Ref No.3000 Series. 2-75

2 - 75

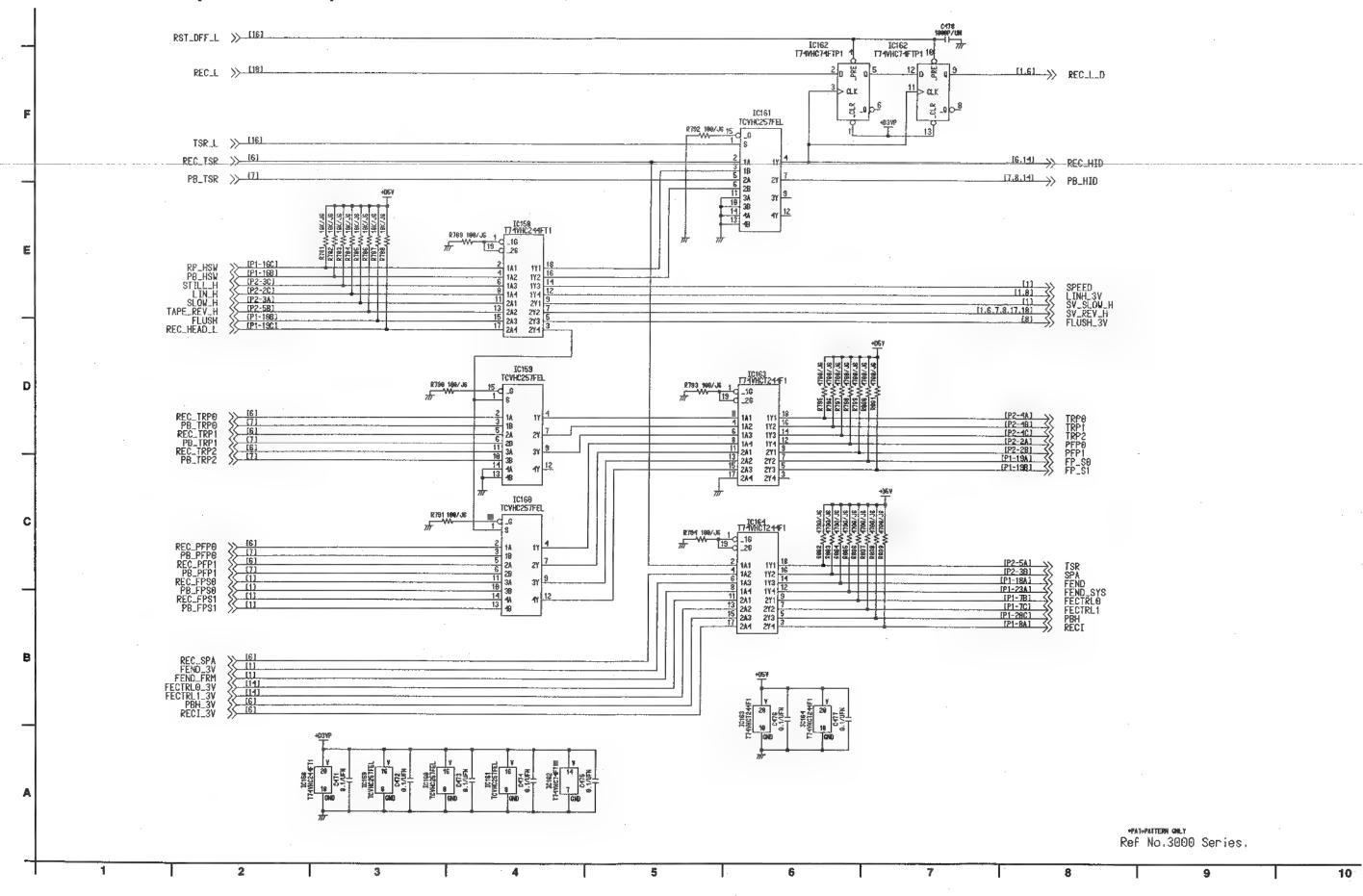






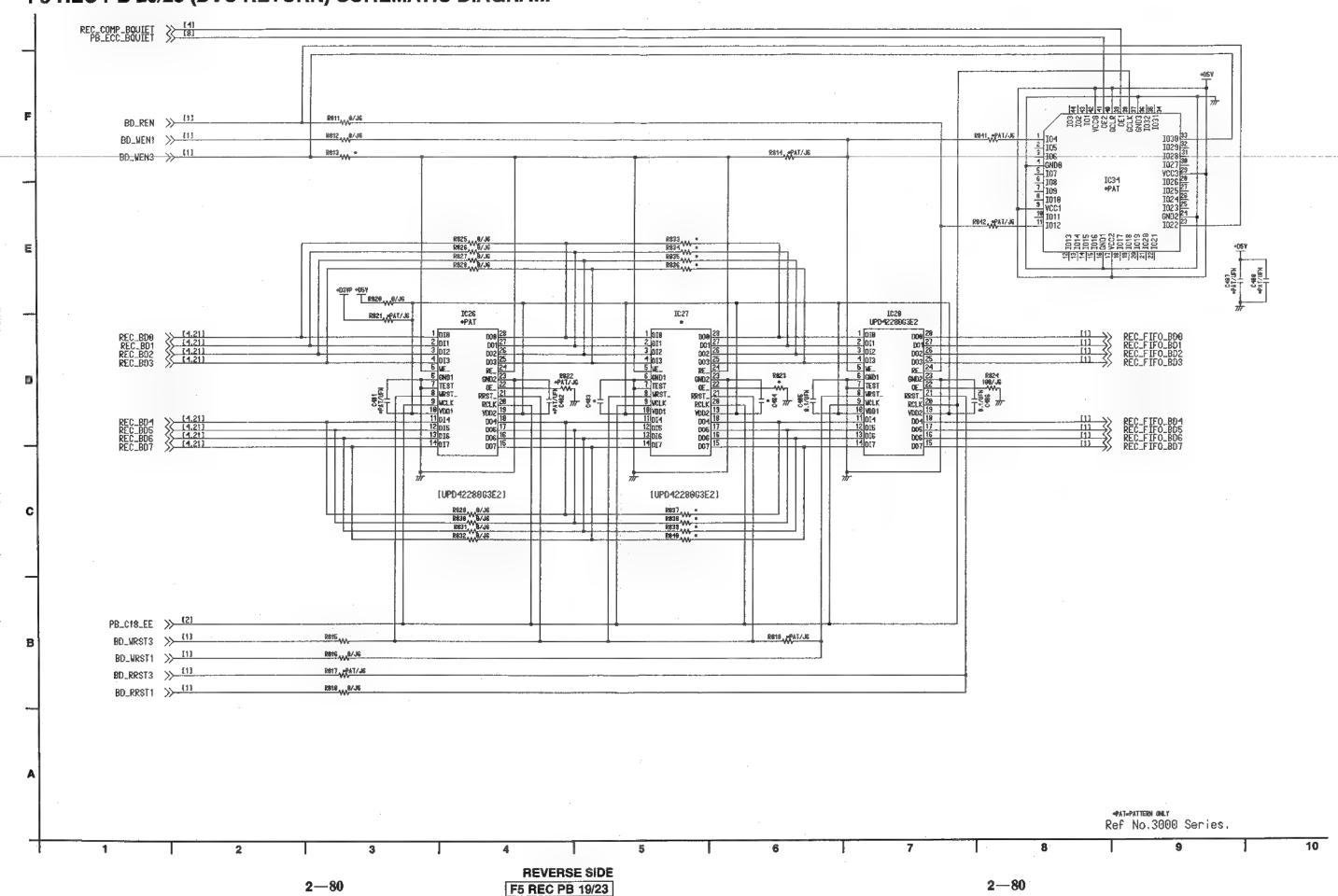
F5 REC PB 19/23 (SERVO SEPA) SCHEMATIC DIAGRAM

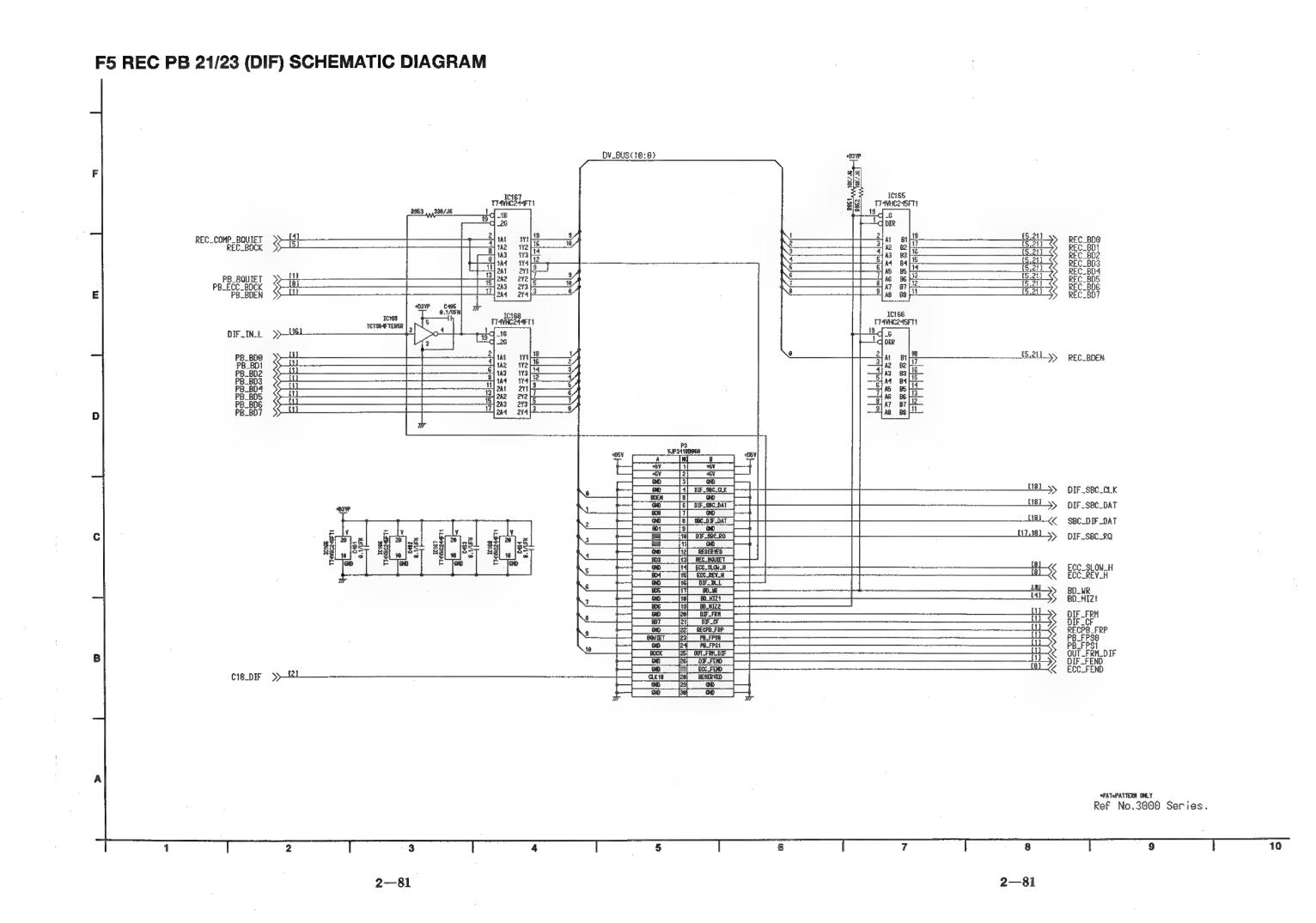
2-79

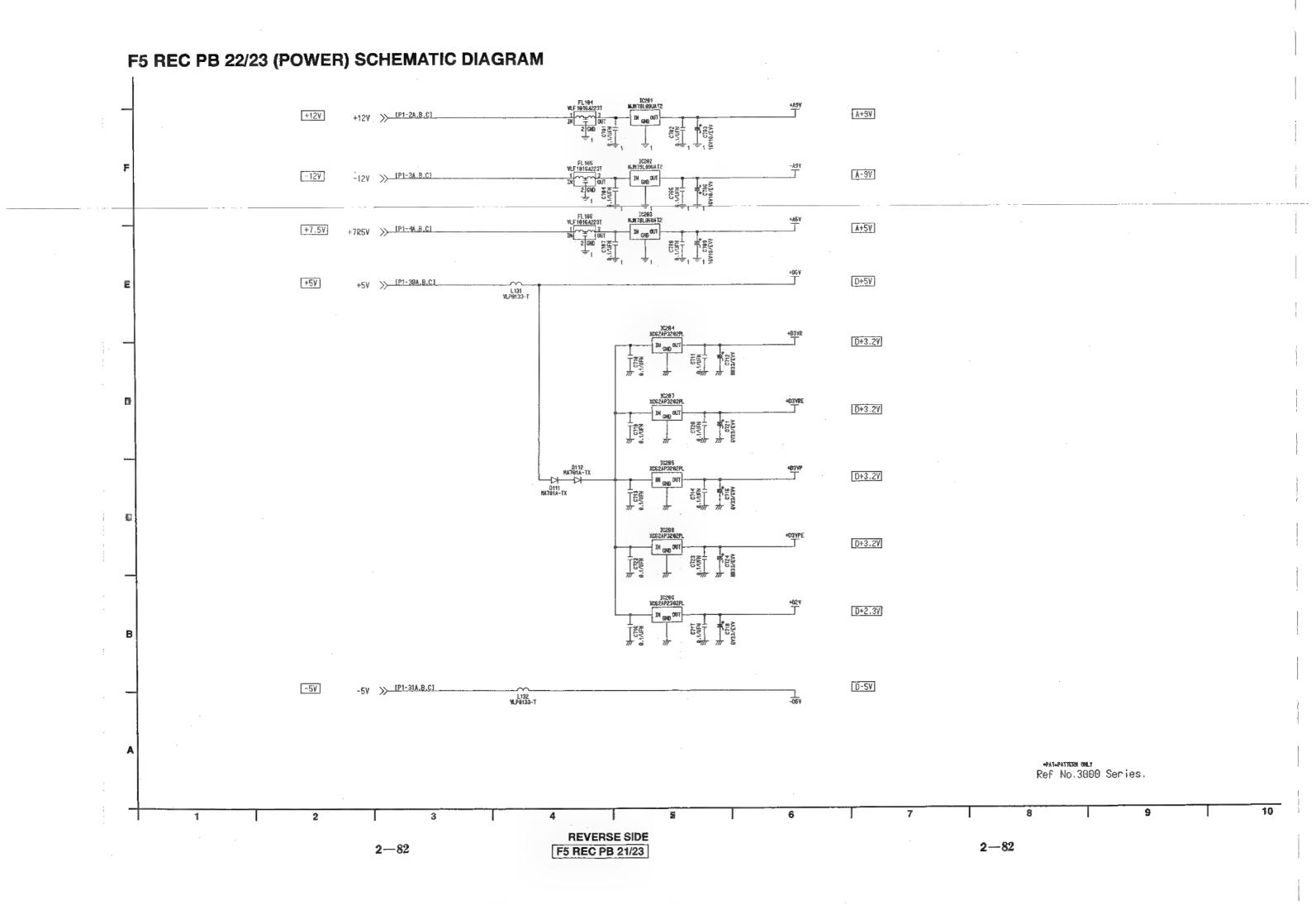


2-79

F5 REC PB 20/23 (DVC RETURN) SCHEMATIC DIAGRAM



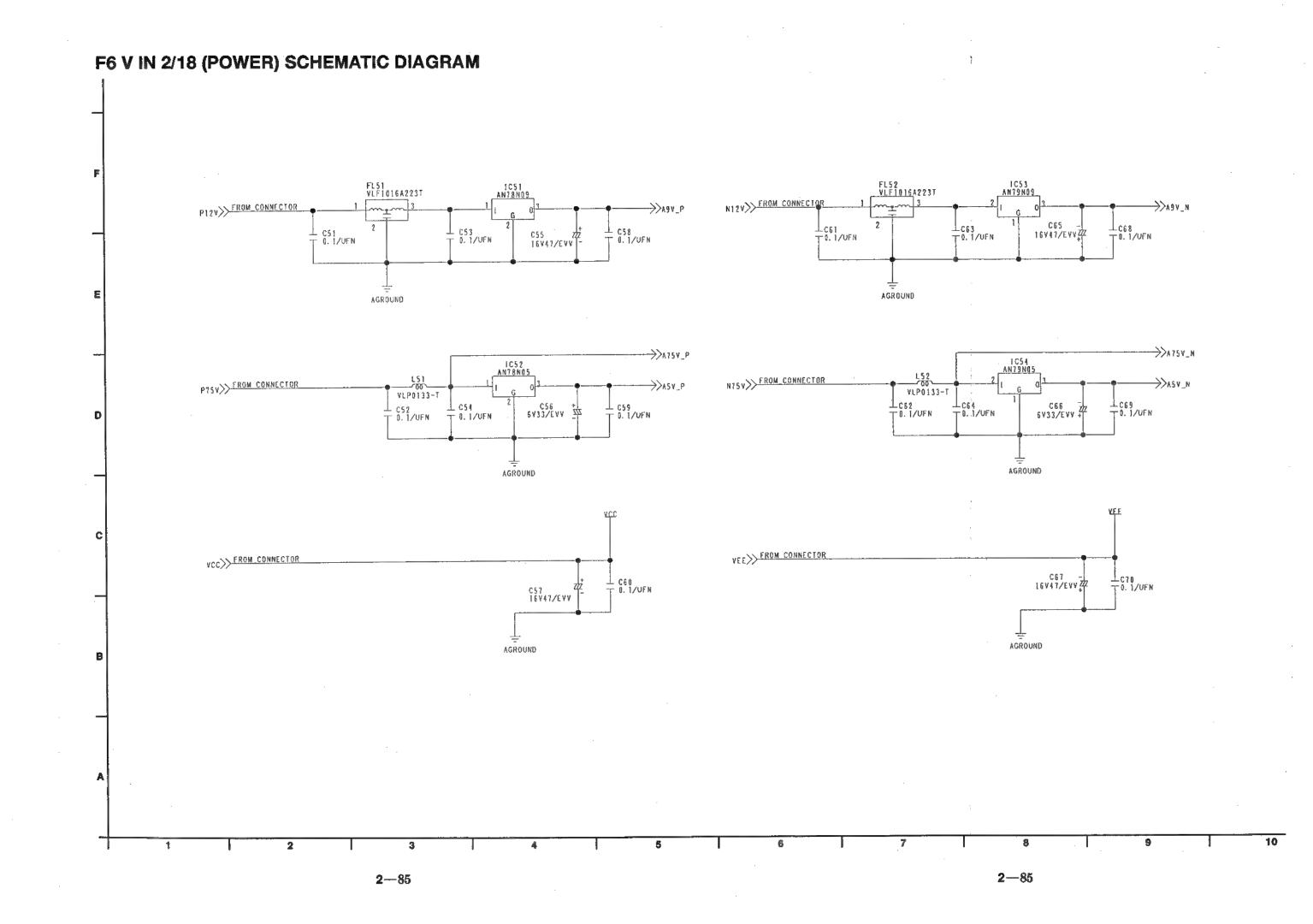


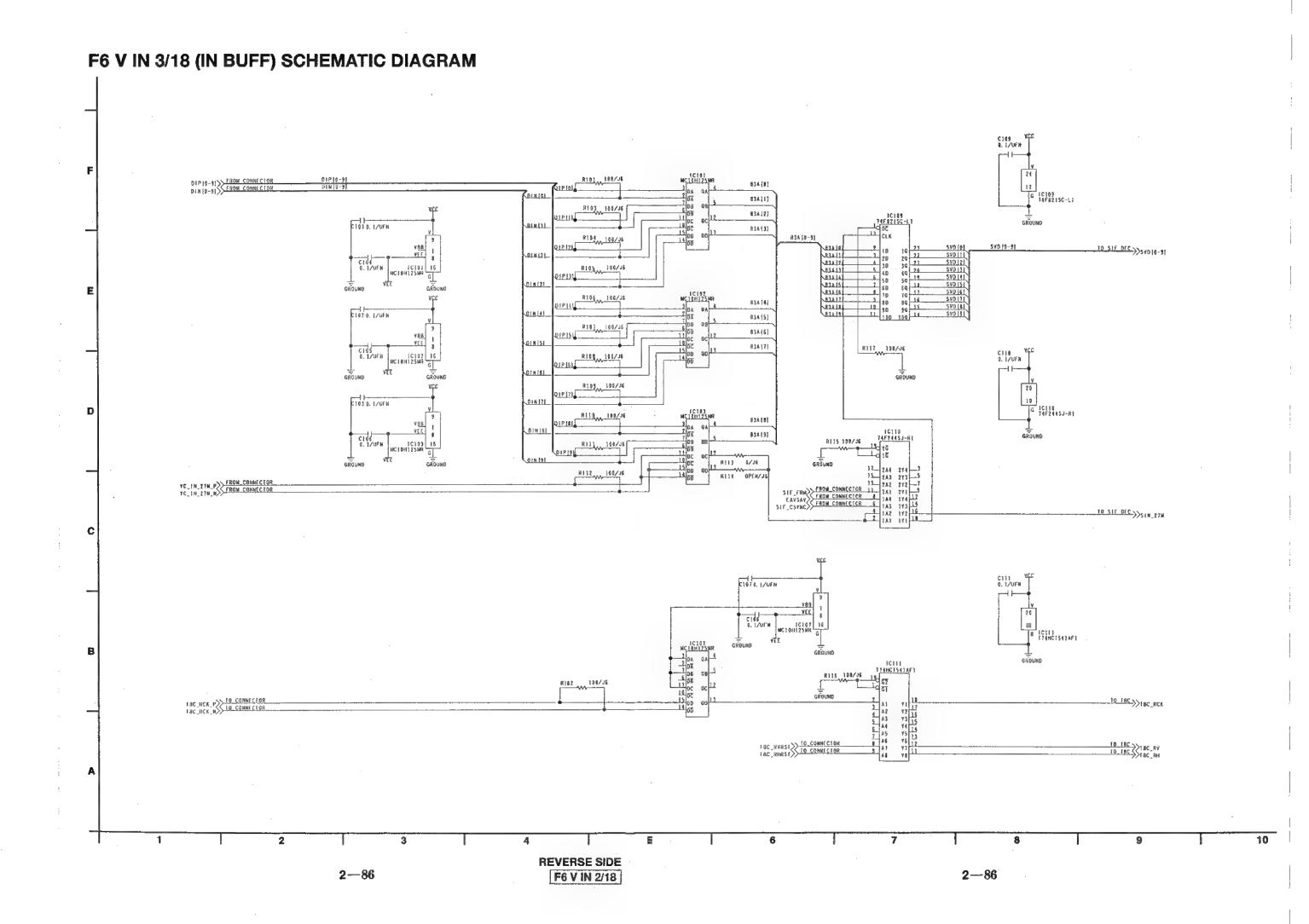


F5 REC PB 23/23 (MOTHER) SCHEMATIC DIAGRAM AND COMPARISON CHART

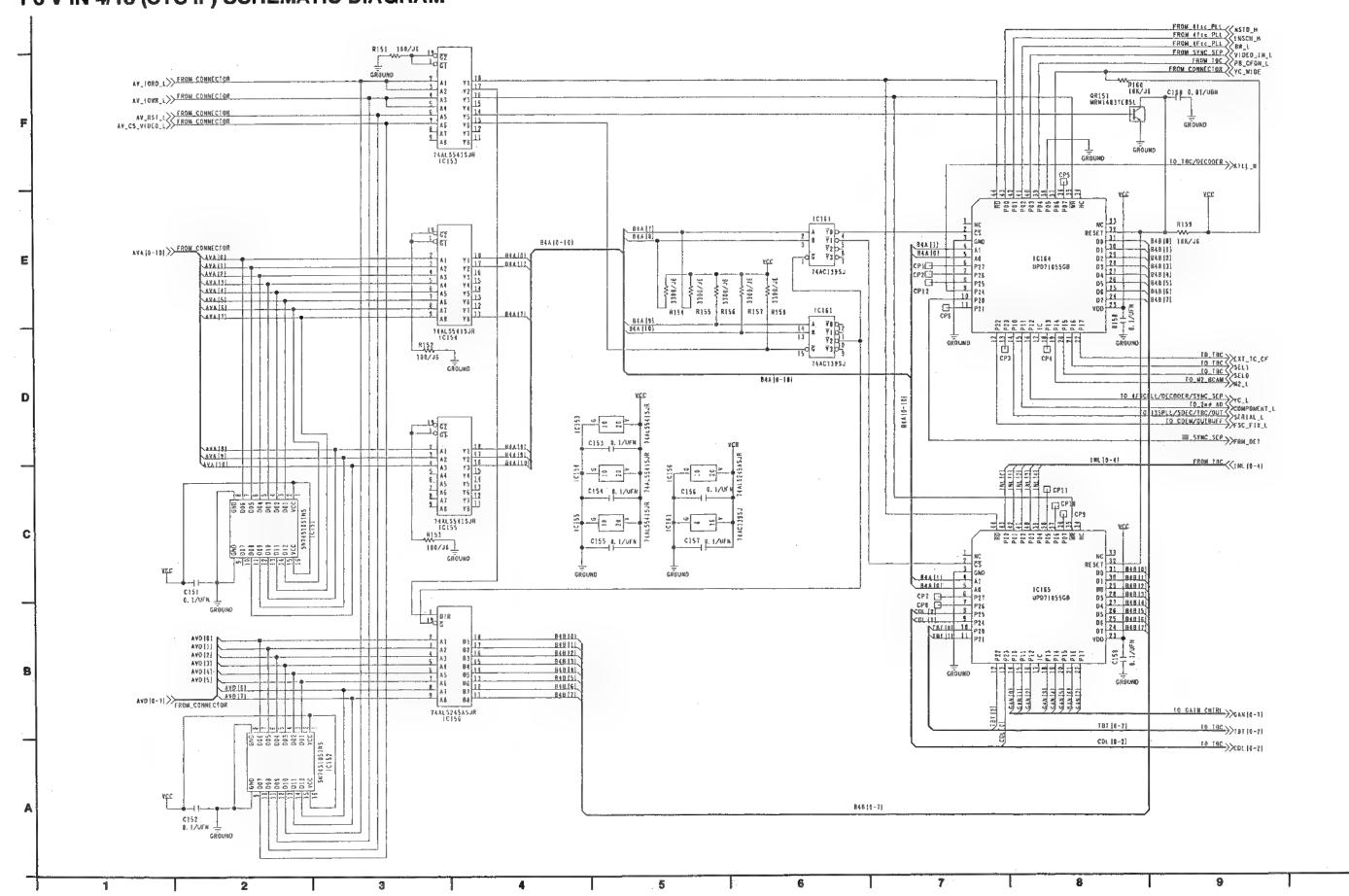
	DIN96P_OSU a b c	DIN96P_08U	COMPARISON CHART
\dashv	1 1/20 / 25	1 1 33 65 77	SREFS NTSC PAL SREFS NTSC PAL
*	33- 65- # 12V +12V	8076 W96/JE [19] < PFP0	C109 *PAT/UFN 0.1/UFN R425 *PAT/J6 1000/J6
i	2 34 66 (22) >> -12V	- 34 66 IIII X LIN.H	C363 *PAT/UFN 0.1/UFN R426 *PAT/J6 1000/J6
_	3 3 25 (07)	3 3 35 67 [19] SPA	C364 *PAT/UFN 0.1/UFN R427 *PAT/J6 1000/J6
F	4 1 1 -7.5V	4 9899 WINGUE 1191 TRP0	C365 *PAT/UFN 0.1/UFN R428 *PAT/J6 10K/J6
	36 68		C366 *PAT/UFN 0.1/UFN R429 *PAT/J6 1000/J6
İ	5 5 37 69 162 FE DATA D	3/6 6S FR_PULSE	C371 *PAT/UFN 0.1/UFN R430 *PAT/J6 1000/J6
_	6 6 38 70 161 EE DATA N	6 6 38 70 121 18M.CK.P.	C401 *PAT/UN 33P/UN R431 *PAT/J6 1000/J6
	(6) 22 FE 2012 11	7 7 9882 1884 K 521 TBC_RCK_P	C402 *PAT/UN 15P/UN R432 0/J6 *PAT/J6
	39 71 Res Weeve 1191 FECTRL1	BEC. RYRST	C403 *PAT/UFN 0.1/UFN R433 0/J6 *PAT/J6
	8 8 46 72 ROSS WOW/JS 161 RECCTRL_A	8 8 49 72 INCOME FRM	C404 *PAT/UFN 0.1/UFN R438 0/J6 *PAT/J6
E	1141 × PB_DAT_R P		C405 *PAT/UFN 0.1/UFN R439 0/J6 *PAT/J6
1	(191 S PB-CLR-N	131 N REC_C_DAYA7	C406 *PAT/UFN 0.1/UFN R440 0/J6 *PAT/J6
	10 10 12 74 (14) PB_DAT_P_N	121 → REC C DATAS	C407 *PAT/UFN 0.1/UFN R441 0/J6 *PAT/J6
	11 11 13 PB_CLK_P_N	11 11 43 75 131 REC.C.DATAS REC.C.DATAS REC.C.DATAS REC.C.DATAS REC.C.DATAS REC.C.DATAS REC.C.DATAS REC.C.DATAS REC.C.DATAS	C408 *PAT/UFN 0.1/UFN R442 0/J6 *PAT/J6
	12 12 /	12 12 REC_C_DATA1	C409 *PAT/UFN 0.1/UFN R443 0/J6 *PAT/J6
	12 12 44 76-	A 19 /6	C410 *PAT/UFN 0.1/UFN R444 0/J6 *PAT/J6
	13 13 13 13 13 13 13 13 13 13 13 13 13 1	13 13 REC. Y. DATA5 REC. Y. DATA5 REC. Y. DATA4 REC. Y. DATA3	C411 *PAT/UFN 0.1/UFN R445 0/J6 *PAT/J6
	14 11 16 78	14 14 / m REC_Y_DATA2	C412 *PAT/UFN 0.1/UFN R446 0/J6 *PAT/J6
-	ROOS 1161 KEQ_HOLD_H	131 SEC_Y_DATAN	C483 *PAT/UFN 0.1/UFN R447 0/J6 *PAT/J6
	1 1 0 1 - 4 (- 7 (9)- 1	10 47600 19-1	C484 *PAT/UFN 0.1/UFN R448 0/J6 *PAT/J6
	16 16 48 80 1191 REC_FRP_3V	16 16 48 80 141 F\$256_M F\$256_P F\$256_	IC137 *PAT TCVHC164FEL R449 0/J6 *PAT/J6
-	17 17 49 85 1997 1997 1997 1997 1997 1997 1997 199	17 17 (3) N886 WAT/36 [9] VCLK	IC138 *PAT 774VHC74FTP1 R450 0/J6 *PAT/J6
	(193) FEND	[4] 7/ [367	IC139 *PAT TCVHC02FEL R451 0/J6 *PAT/J6
	R871 198/ 16 1191 >> FD 60	18 18 50 82 111 REC_FRM_A 18 50 82 111 SERVENTINH_A 18 50 82 111 SERVENTINH_A 18 50 888 W 57/46 191 FS_P 18 50 888 W 57/46 191 FS_P 18 50 82 111 FS_P 18 50 82 111 FS_P 18 50 82 111 FS_P	IC140 *PAT TC7S00FTE85R R452 0/J6 *PAT/J6
	19 19 51 83 1191	19 19 51 83 RESE WIRE 191 W PB_FRM_A	IC171 *PAT TC7S04FTE85R R453 0/J6 *PAT/J6
C	20 20 REC_SPEED	20 20 52 84 FRM5	IC172 *PAT TC7S32FTE85R R454 *PAT/J6 10K/J6
	AV_SBC_CLK		1C17 MN4706F MN4707F R455 1000/J6 *PAT/J6
	SBC_AV_DAT	21 21 53e 85e 882 M77/86 1121 PB.C.DATA6 8834 M77/86 1121 PB.C.DATA6 8835 M77/86 1121 PB.C.DATA2 PB.C.DATA2 PB.C.DATA2	IC27
	22 22 PB FRP 52	22 22 54 86 8835 W 17/36 1121 PB_C_DATA3 8836 W 17/36 1121 PB_C_DATA3 PB_C_DATA4	IC35 *PAT CG25123-5106 R472 *PAT/J6 270/J6 IC36 *PAT CY7C19920ZC R473 *PAT/J6 100/J6
	(171) VS DATA	23 23 55 87 8899 77.06 1121 PB.C.DATAS	IC37 *PAT CY7C19920ZC R543 0/J6 *PAT/J6
		8899 17.06 (12) PB_C_DATA7 8999 17.76 (12) PB_Y_DATA6	IC4 MN4706F MN4707F R626 0/J6 *PAT/J6
	24 24 56 88 1161 AV ADRS AV AD	24 24 56 88 88 88 88	IC501 VSI2159B VSI2282 R813 *PAT/J6 0/J6
В	25 25 576 896 1161 AV_ADRS2	22 22 54 86 886 477.8 1121 PB_C_DATA3 23 23 55 876 8899 477.8 1121 PB_C_DATA6 8898 477.8 1121 PB_C_DATA6 8898 477.8 1121 PB_C_DATA6 8898 477.8 1121 PB_C_DATA6 124 24 8898 477.8 1121 PB_C_DATA6	IC601 VSI2159B VSI2282 R815 *PAT/J6 0/J6
	26 26 58 90 1161 AV_ADES0 1161 AV_IORD_L 1161 AV_IORD_L 1161 AV_IORD_L	26 26 58 90 1888 West-16 112 PB-Y-DATA6 126 127 PB-Y-DATA7 PB-Y-DATA7	R119 *PAT/J6 10K/J6 R823 *PAT/J6 100/J6
	26 58 90 1161 AV 100R L	26 58 90 RS86 W 807-JS 111 PB FRM	R120 1000/J6 *PAT/J6 R833 0/J6 *PAT/J6
	25 25 57. 89. 1161 AV_ADRS2 26 26 58. 90. 1161 AV_ADRS0 1161 AV_	22 22 54 86 885 77.65 1121 PB C DATA2 8881 77.65 1121 PB C DATA3 8881 77.65 1121 PB C DATA3 8881 77.65 1121 PB C DATA4 23 23 55 87 888 888 77.65 1121 PB C DATA4 24 24 56 88 88 889 77.65 1121 PB C DATA7 24 24 56 88 88 889 77.65 1121 PB C DATA7 25 25 57 89 88 88 889 77.65 1121 PB C DATA7 26 26 58 90 888 889 77.65 1121 PB C DATA4 27 27 59 91 111 PB C DATA4 28 889 77.65 1121 PB C DATA4 28 889 77.65 1121 PB C DATA4 29 899 77.65 1121 PB C DATA4 20 889 77.65 1121 PB C DATA4 20 889 77.65 1121 PB C DATA4 21 PB C DATA4 22 PB C DATA4 23 889 77.65 1121 PB C DATA4 24 24 56 88 88 88 88 88 88 88 88 88 88 88 88 88	R121 *PAT/J6 0/J6 R834 0/J6 *PAT/J6
\dashv	CO CO AV DATA4	28 28 60 92 UIT_135M_P	R122 0/J6 *PAT/J6 R835 0/J6 *PAT/J6
	20 29 (161) AV_DATA1	29 29 UII WILL VIP	R381 0/J6 *PAT/J6 R836 0/J6 *PAT/J6
	28 28 60 92 1161 AV_DATA3 29 29 61 93 1161 AV_DATA1 AV_DATA1 AV_DATA1 AV_DATA1 AV_DATA0 4V_DATA0 4V_DATA0 4V_DATA0	28 28 60 92 141 0UT_135M_P 29 23 61 93 1111 0UT_FRN 1111 0UT_FRN 1111 0UT_FRN 1111 0UT_VIP 1111 0UT_ROND 1111 CASSET_POS_1 20 38 62 94 1171 CASSET_POS_3	R421 47/J6
	30 30 63 94 1	30 38 62 94 (171 CASSET POS.2 CASSET POS.3	R422 *PAT/J6 0/J6 R838 0/J6 *PAT/J6
^	24 31 /	31 31 63 95	R423 1000/J6 *PAT/J6 R839 0/J6 *PAT/J6
	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		R424 *PAT/J6 1000/J6 R840 0/J6 *PAT/J6
	32 32 64 96 #	32 32 64 96 W PATERN ONLY Ref No.3000 Series,	
\perp	P1 VJP3454B096	P2 101310036	
I	1 2 3	4 ; 5 l I I	7 8 9

F6 V IN 1/18 (CONNECTOR) SCHEMATIC DIAGRAM DEN[0-9] DIN[0-9] P1 YJP34549695 P2 YJP3454B096 P1 VJP34548096 P1 VJP34548696 P2 YJP34548496 P7 YJP34548096 AGRÖUND 18 GNO 26 +124 18 -124 18 +7-54 58 -1-54 1C GHD 2G +12V 1C -12V 1C +1.5V 14 GND 24 +12Y 34 -12Y IA GMD 1B GMD 1C GNO 1A +7, 5Y 5A -7, 5Y 6A VIDED IN G SA VICEO IN G 1A Y IN G RA PRING 9A PRING 10A 4P Y IN G 11A AP C IN G 11A AP C IN B 11A AP C IN B 11A YC IN B | 118| 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 128| | 12 AGROUND DIN(3) OIN 21 15C YC IN NS 17C YC IN P7 18C YC IN NB ENCOME_HST_H>>-DIN(6) DIN(9) | 13C | YC | M MB | | 13C 10_(N_21N_P< SIF_CSYNC RS WALLE 200 SIF_CSYNC YC_[N_21M_N< C_IN_2IM_N | C_IN_2IM_N | C_IN_2IM_N | | C_IN_2IM_N | C_I AVA [5] AVA [3] 30C 15V 31C -5V 32C GND 32A GNO AVA [8-10] RTD [9-7] AVA [0-10] (RYD [0-7]>>-AVD (0-7) AVD [8-7] AVD 18-11-L1 VLP0133-T L2 YLP6133-T TIGAGE L 163 VJR1616 105 VJR0646 2 **REVERSE SIDE** 2 - 842-84 F5 REC PB 23/23





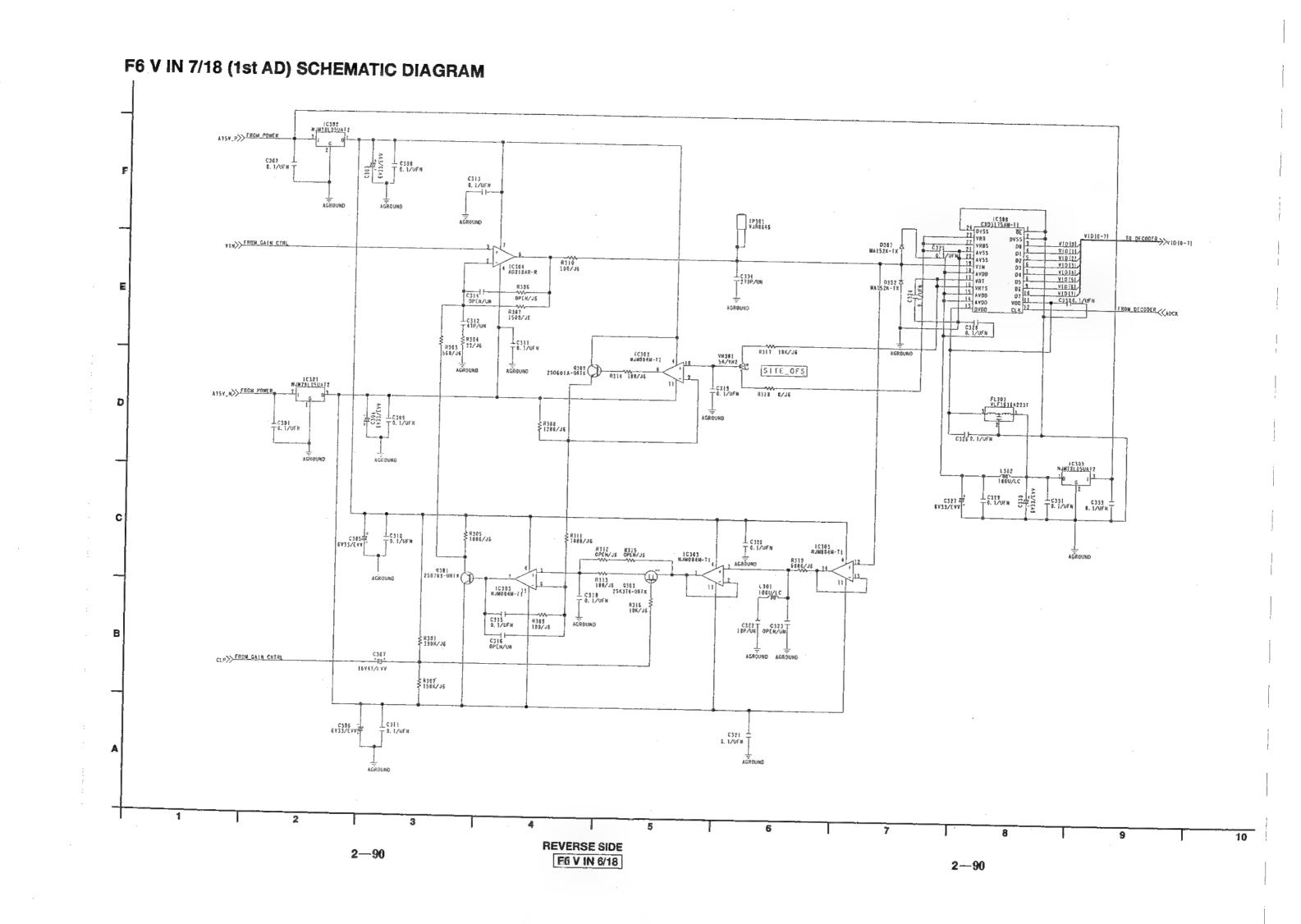
F6 V IN 4/18 (SYS IF) SCHEMATIC DIAGRAM



F6 V IN 5/18 (SIF DEC) SCHEMATIC DIAGRAM TZ4HC1541AF3 SERIAL_L>> FROM SYS IF 10 18C | TBC WH 10 18C | TBC WH 10 SYNC SEP | DET_H 7 A1 A2 4 A3 5 A4 E A5 A5 A6 A7 A6 A8 TBC_WCK 85A (0-T) SVD (0-9)>> FROM | MAUFF 5VD [0-9] SIN_21M>> FROM INBUFF | B5A [6] | 1 D8 | B5A [1] | 18 D1 | B5A [2] | 4 D2 | B5A [2] | 11 D3 | B5A [2] | T D3 | B5A [2] | T D3 | B5A [2] | T D3 | B5A [2] | B5A T1Y (0-7) 18C)>11Y (0-1) MIOW>> FROM IBC T1Y[1] T1Y[2] T1Y[3] T1Y[4] 0. 1/UFN (SIM_FRM>> FROM TRC RZBZ U/JG BSA [0] RZGI OPEW/JE SIN_FRM_D COULDNEE R203 0/J6 10205 1<u>4657453-8</u>1 85AJ1L 95AJ2L 85AJ31 -10 8C TIC (0) TIC (1) TIC (2) TIC (3) TIC (4) TIC (5) TIC (6) TIC (6) 10 18C >>r(c(0-7) 110(0-2) 95A | 61 95A | 61 95A | 61 95A | 71 10201 EPM103200-3 85A [3] 85A [4] 85A [5] 85A [6] 85A [7] TC_L FROM SYS IF COMPONENT_L FROM 135M PLL RSTM_SIF FROM 575 IF CDL105 FROM 575 IF 135M_SIF CZ01 0.1/UFN VS.2402 TO 2nd AD PR/2nd AD PB >> ADCK_PC 10 18C WEY 0 WEY_I FROM TBC FROM_TBC MEC_I FROM_135M_PLL GROUND C201 .D. 1/UFN \$40 [1] EC281 5V0 [2] C212 0. 1/UFA 2 2 2 2222555222 10204 590 [3] SVD [4] SVD [5] C213 0. 1/UFN B5A171 B5A171 B5A151 B5A131 B5A131 B5A121 B5A111 <u>ه</u> = 5 В SVD [6] 10202 EPM703790~3 540 [1] 540 [8] 10705 CZ14 0. 1/UFN C282 E. I/UFN V5.2382 10210 BSA [0] C215 0. 1/UFN <u> 국극국교급국 국 교육 역성</u> 1219 T 0. 1/UFR FROM SYNC SEP KINP FRM FRMO< 2 REVERSE SIDE 2-88 2-88 F6 V IN 4/18

F6 V IN 6/18 (GAIN CNTRL) SCHEMATIC DIAGRAM 0253 2506014-0R1X 1251 100U/LC -<<asv_p 1 c255 T 8. 1/UF - C785/EVV C251 Z 16910/E99 ₹ 1000/J6 0252 2506014-0RTX MC14HCBBAF-R VIDEO_IN>> FROM_CONNECTOR ₹ 8255 ₹ 100/J6 AGROUND MIACCOVTS T 01 C291 YIOEO_IN_G>> ₹1000/J6 8261 330/JE \$8264 \$560/JB IC251 NC74HCD#AF-R C264 0. 1/UFH L252 100U/LC AGROUND D251 AG MA152K-FX 9251 258789-9873 HC759 MC74HC00AF-R FROM SYS LF KYC_L TO TEST PLL >>CSYNC 0. L/UFN TO 1st AD >>clp REGOODER NORTH COLUMN TERMS IC259 NGT(HC00AF-R TG4W53FTE12L 2 4 1 C271 L253 100U/LC ASY_P>> FROM_POWER 16941/EYY 0243 IC255 NUMTREDSUATE | AR9|A125 | 2 | V|DEC | GND3 | 28 | CAMP | AR9|A125 | CAMP | GND3 | J.9 | CAMP | J.9 A75V_P>> FROM_POWER \$V33/EVV 22 上C265 丁0.1/UFN R256 2200/J6 上CZ81 丁G. GI/UBN 0.1/UEN R760 100/J6 EC252 B MJMQQZBM-T[\$ 1255 8 1700/LB R770 600K/J6 C279 0. 1/UFN ASV_N>> FROM POWER 8271 10X/J6 C211 0.1/UFN RETE TOK/JE C262 1 1C251 8.1/UF# T R275 10K/J6 R265 1K/J6 GZZ C298 8267 2200/46 KSE# \$50/76 GAN (0-7) SERON SYS IF 10256 NUMPSLOSUATE A75V_N FROM POWER -0767 10. 1/UFN

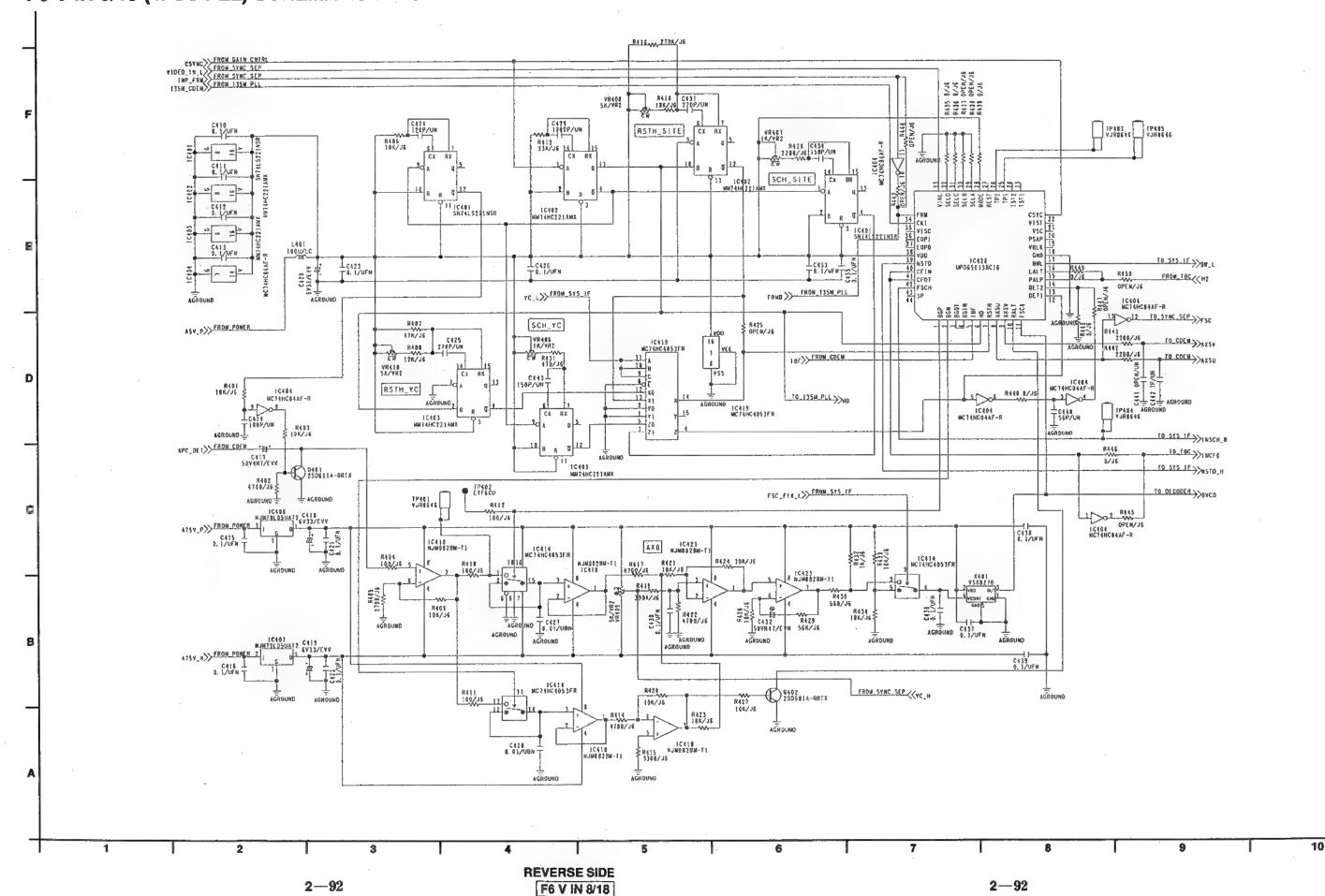
2-89

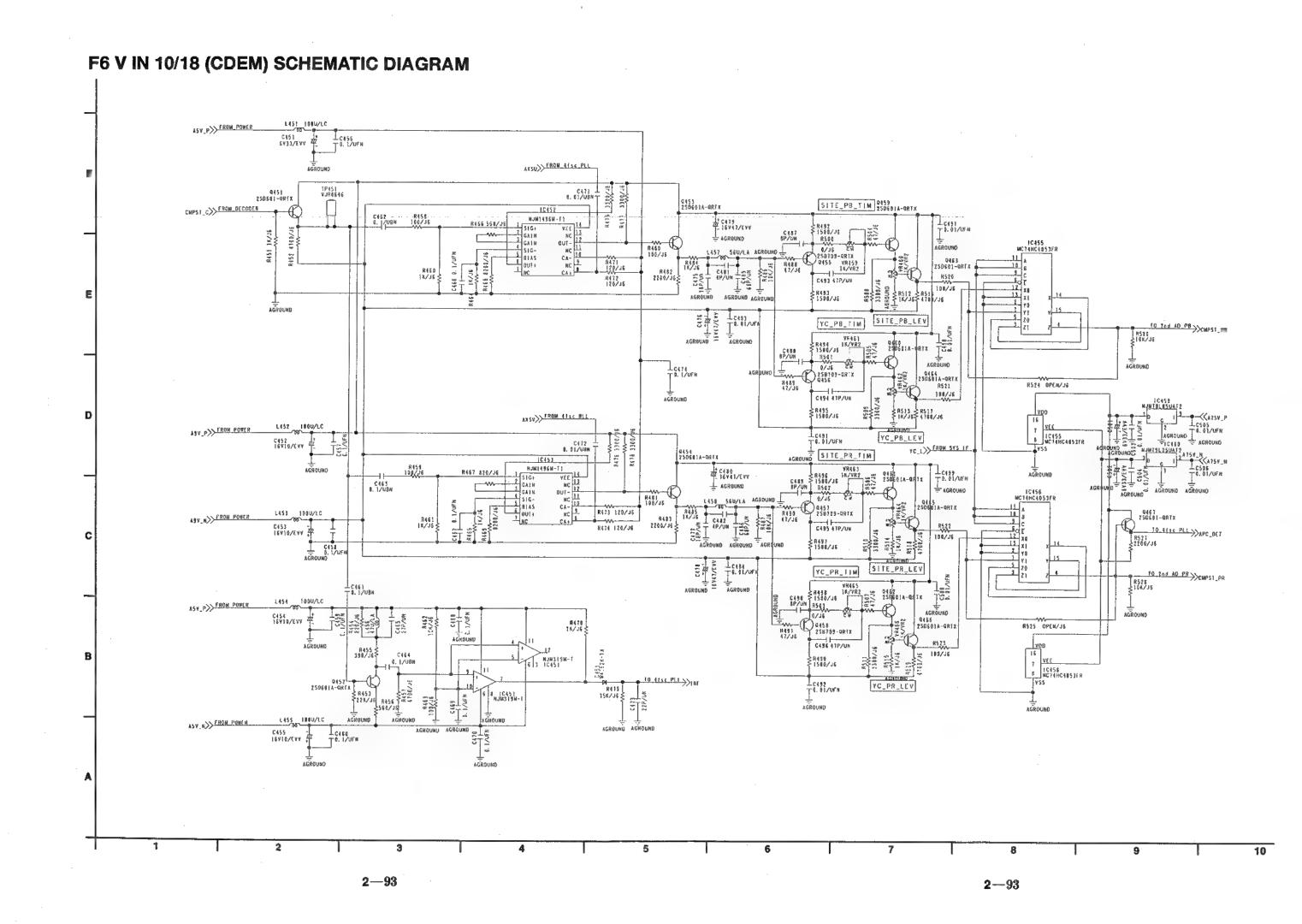


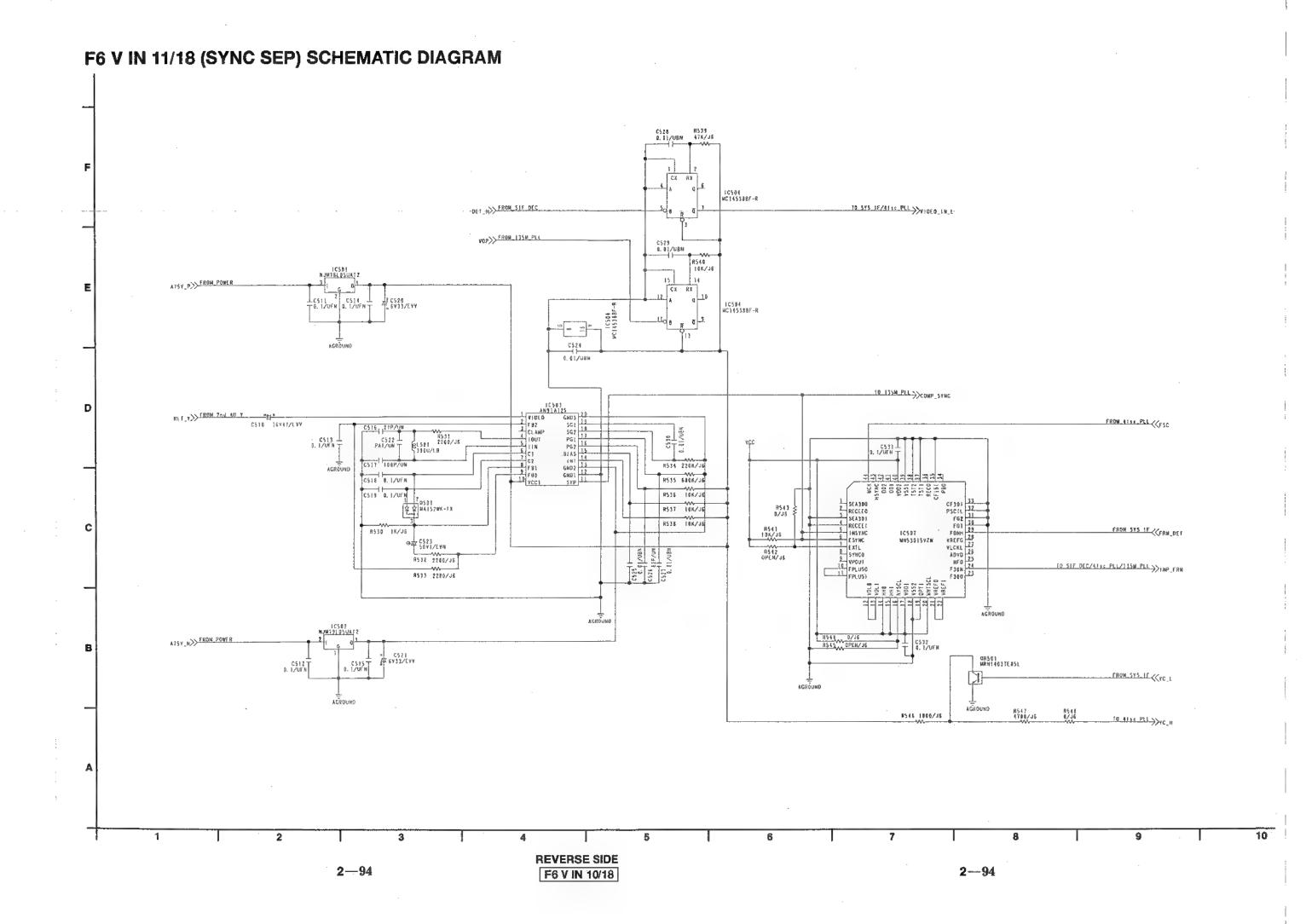
F6 V IN 8/18 (DECODER) SCHEMATIC DIAGRAM 4P_Y_IN FROM_CONNECTOR 4P_Y_IN_G (GARDRING) ID_GAIR_CNIRL >> APYIM_PLL A75V_P>>> FROM POWER 10359 MC74HC4853FR \$8395 \$330/Ja 1C355 AD8047AR-R 10351 A08047AR-R J15 J 2 Y | 13 X | 12 N | 6 O E | 10 B | 11 A R376 100/J6 A361100/J6 10.200 AD Y >> CMP51_Y MPEN/J6 - 0359 -0.1/UFN C184 DPEB/UN ÈRJES ₹IOK/JE R374 \$ AGRICUMD 10.553 NJM.79.054A 2 2 1 G 0 1 4P_Y_LEV 8370 100/J6 YCKORND Ť FL351 VLF1016A8837 CSST B. L/UFN C185 OPEN/UN 1 10351 | INC71HC||053F|| IC356 MC74HC#853FR 0. 17UFN T R3/5 338/J8 C391 V35 R369 C383 Test Test TC_L FROM 5YS 200/VR2 200/VR2 C# R155 166/J VID [0-7] >> FROM 151 AD 8358 100/36 AGROUND R316 C416 108/16 4.1/V8N TO C DEM CMPST_C. R119 330/J6 C319 D. 1/VBN C481 C395 10 B C3967P/UN ADCX C391 10P/UN AGRŪUNO C394 22P/UN C39#土 128P/UN丁 #367 10K/J6 #300 \$ 18/36 } evco>> FROM 411c PLL 9353 250601A-9RTI CAMA 6. 17 UFN 8564 0FEX/36 4P_C_IM> FROM CONNECTOR 0. 1/UBM KILL_H>> FROM 5Y5_1E 4P_C_LEV R168 228/J6

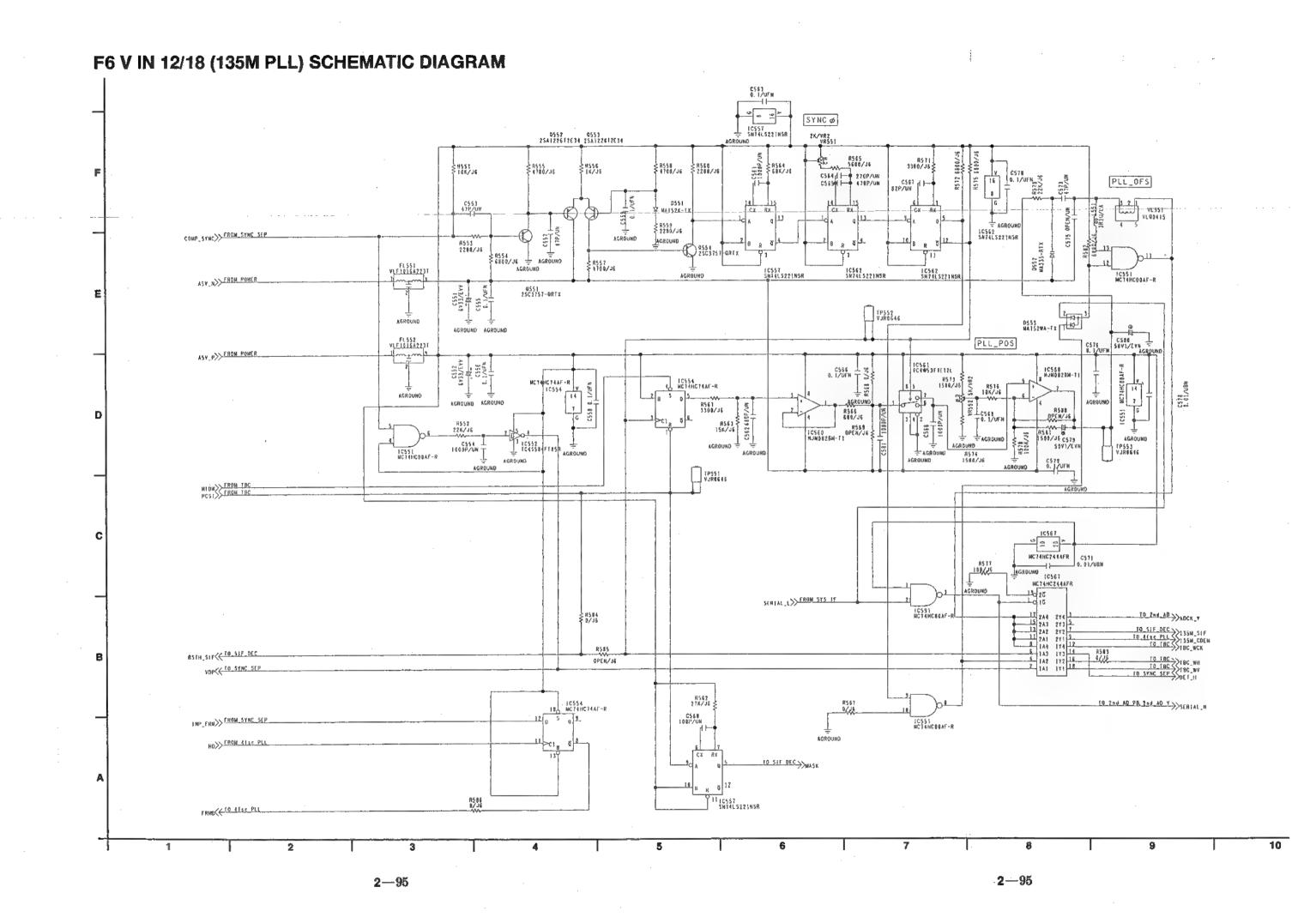
2-91

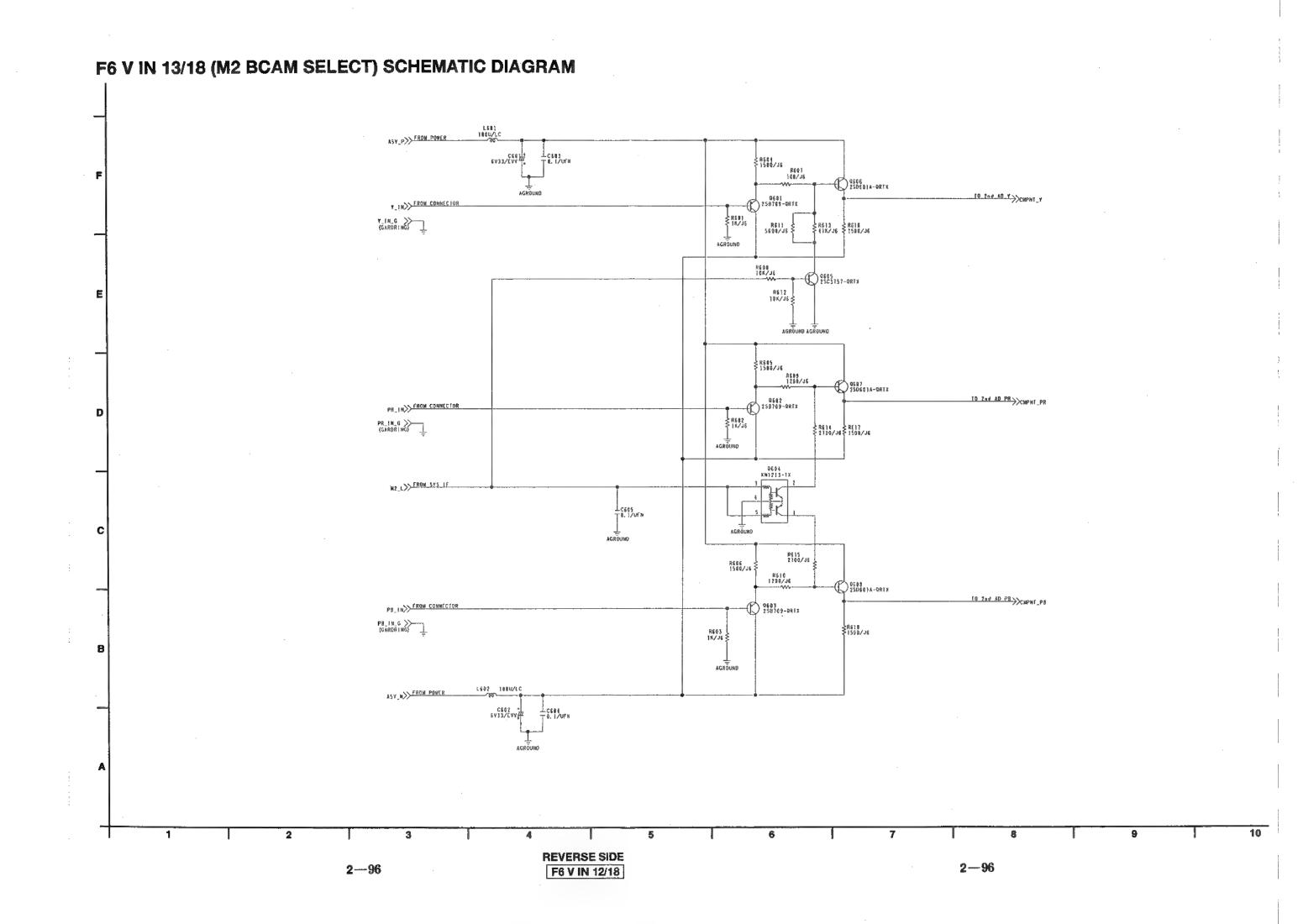
F6 V IN 9/18 (4FSC PLL) SCHEMATIC DIAGRAM

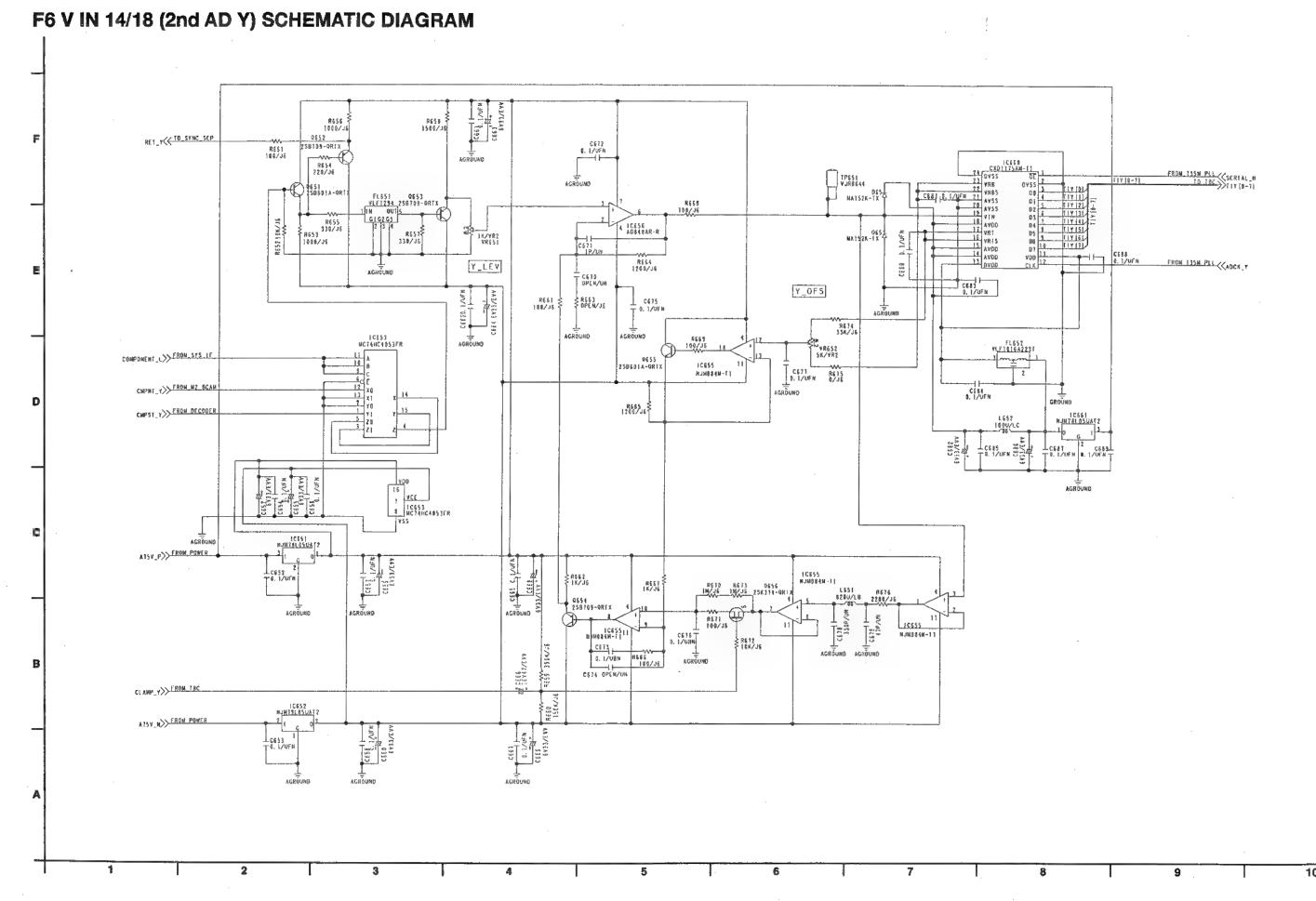


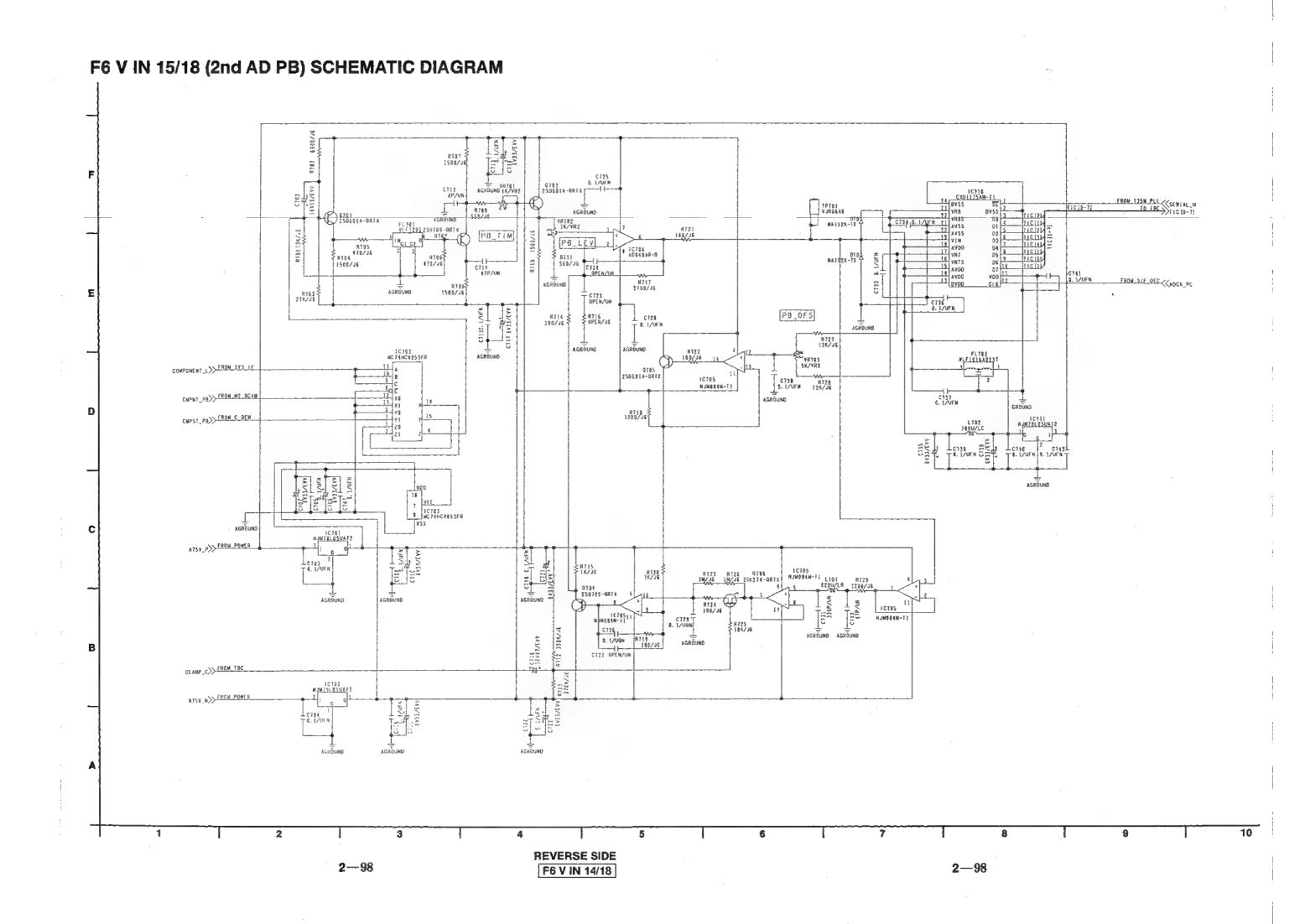








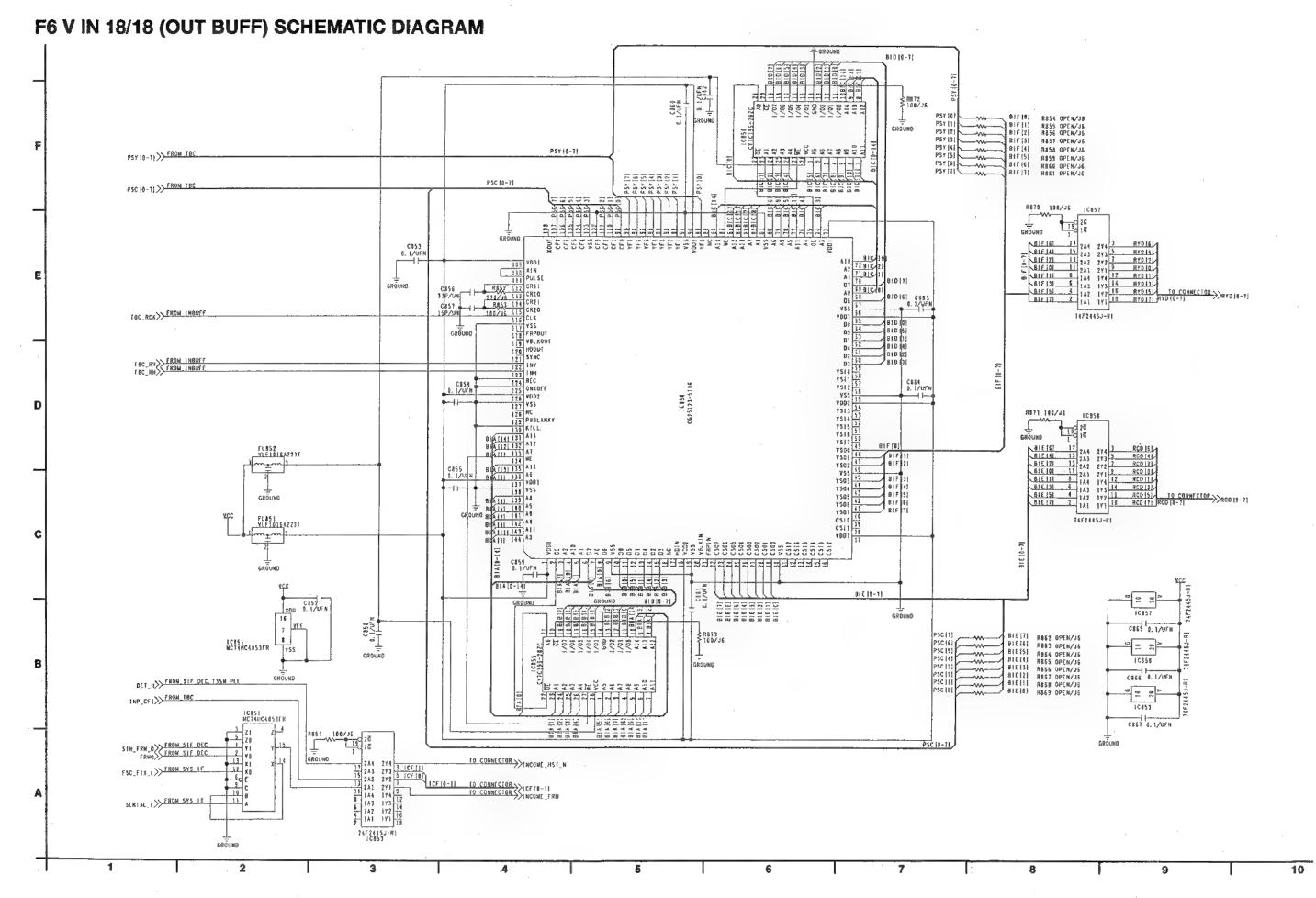


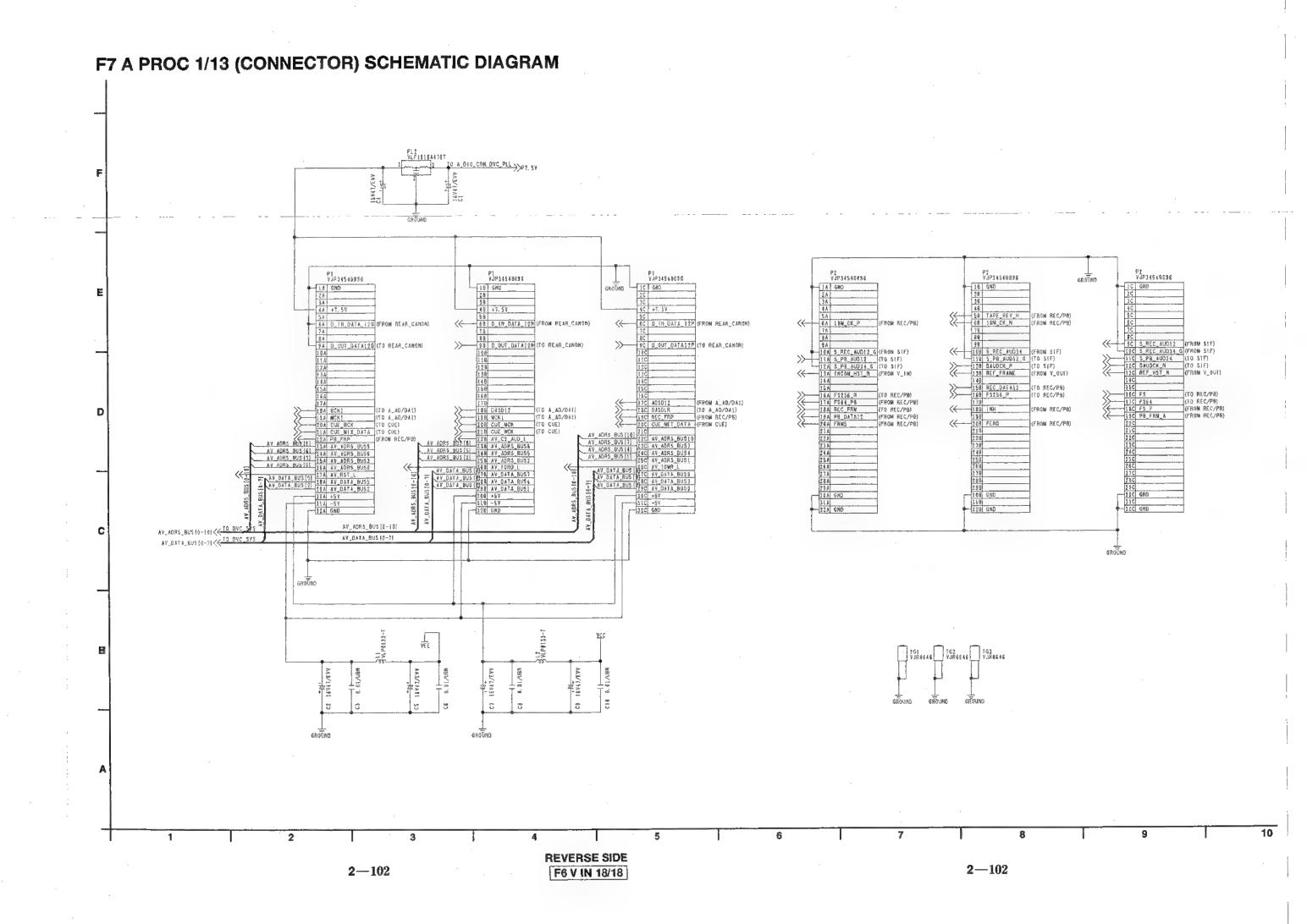


F6 V IN 16/18 (2nd AD PR) SCHEMATIC DIAGRAM TIRIO TIRIO TIRIO TIRIO TIRIO TIRIO TIRIO TIRIO D151 10758 A0848AR-R FROM_SIF_DEC KADCK_PC 8767 2788/J6 PR_OFS ₹ 8166 ₹ 0PEN/J6 C778 T 0.1/UFX R711 12K/JS COMPONENT_L>> FROM 545 IF 0755 2506014-0818 0.1/0FN 8778 0.1/UFN 12K/J6 0, 1/UFN 1200/16 CMPST_PR>> FROM C_DEM B WC74HC405JFR AGROUND 10751 NJM18E05UAT2 A15V_P>> FROM POWER 10155 10754 NJMB64N-FT 258709-08TX 4 GHOONDY GNEST THE STATE OF THE 0. 1/USH (1759 1.08/JE # C1ES CLAMP_C>> FROM_FRC ATSY_N>> FROM POWER 2-99

F6 V IN 17/18 (TBC) SCHEMATIC DIAGRAM TO 125M PLL PCST 10 SIF DEC. 175M PLL AND W 10 2nd AD PB. 2nd AD PR 10 CDEM 10 CDEM 10 CDEM T1Y [0-7] THY [0-2] FROM SIF DEC. 2nd AD Y TIC [0-7] FROM SIE DEC. 7md AD PR CB07 II. 1/UFN FL 101 VLF | 016A2231 1|R[0-7]>> FROM 2nd AD PR FROM SIF DEC/135M PLL (18C WH TIR IN TIBLIL R803 D/J6 R804 DPEN/J6 R805 DPEN/J6 T18 171 TIR[3] BL R5 EZ R6 B1 R1 TIRIS TIRIS! R886 0/J6 FROM SIF DEC/135M PLL SERIAL L FROM SIF DEC/135M PLL SERIAL L CP406 CDL [2: CDL [1] CDL [0] FBT [2: FBT [1] FROM 5Y5 1F (COL (4-2) 1C802 TC160G41-1417 COELAY2 COELAY1 COELAYO TOT |0-2| FROM SYS. IF CIRT (0-2) TEST? TEST1 TEST0 CP801 CP803 CP805 10 575 IF >> INC |0-41 18310 187614 187614 187612 THU [[] FROM SYS IF SELD D R801 10K/J6 TO OUTBUTT >> INP_CF1 C805 8. 1/UFR R807 FROM MOUFF TOC.RH FROM NBUFF TOC.RY FROM NBUFF TOC.RY FROM NBUFF TOC.RCK UPD4Z28QG3E PSY (0) PSY (1) PSY (2) PSY (2) PSC 12 BHAIL. 399/70 3390/70 3390/70 3390/70 3390/70 3390/70 3390/70 RHA 171 AHA 131 PSC (3 RE 21 GNO 22 OE 21 REST 20 #RST RRST 20 RCX 13 PSY 14 PSY 14 PSY 15 PSY 16 P 0HA141 CID 11 014 0HA151 0. 1/UFM 12 045 0HA151 13 015 0HA151 14 015 STATES OUTBUFF >>psc | 1-7| GROUND GROUND PSC [0-7] KILL H

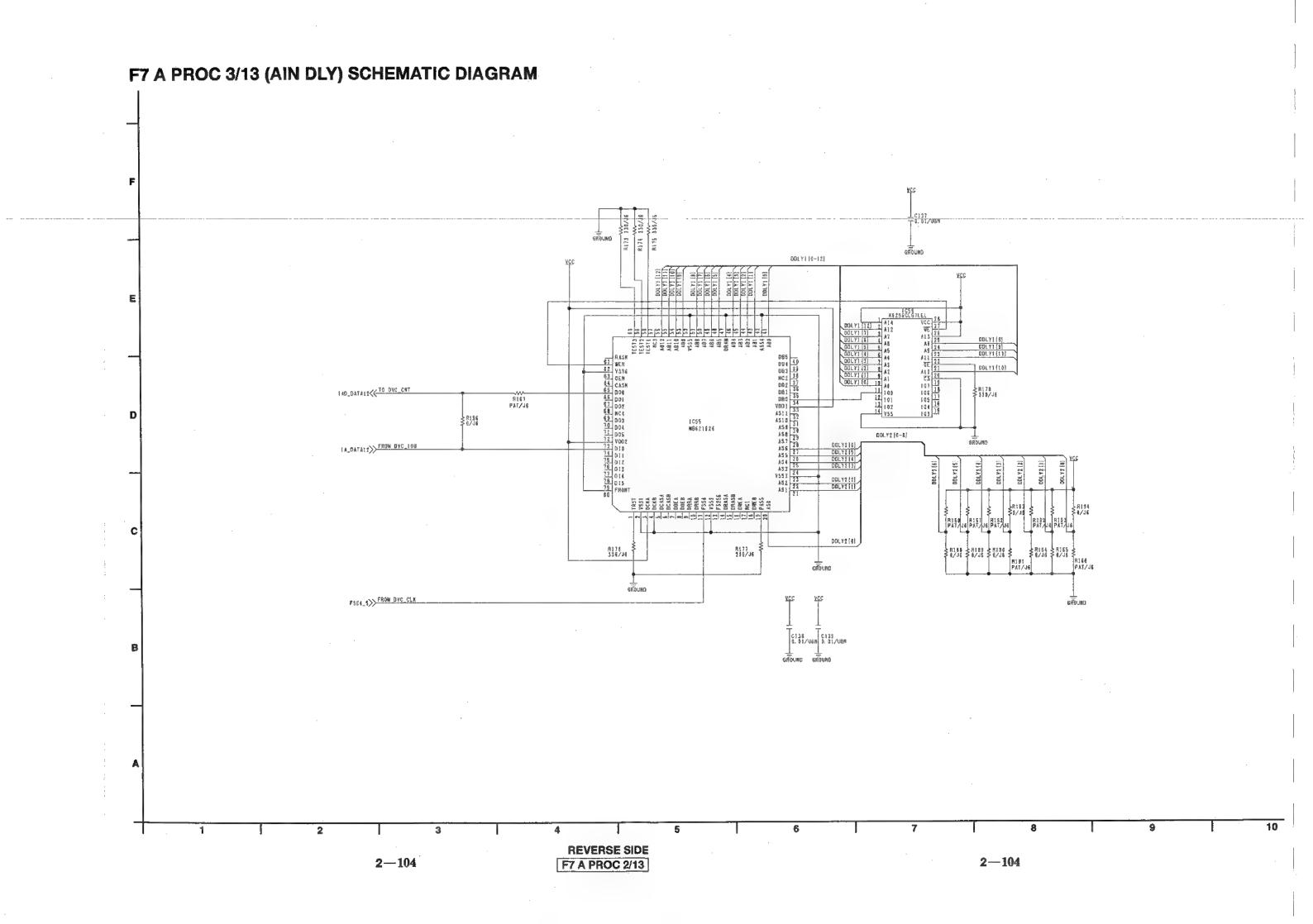
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F7 A PROC 2/13 (IO BUFFER) SCHEMATIC DIAGRAM 1CS1 MC14HC74AF-R R89 10K/J6 CUEN_MUTE_L>> FRON BVC_SYS 101 R13 330/JE \$ 10K/J6 R26 330/J6 TP17 EYF6CU • OREC_DATA12 OPO_DATA12 FROM DVC_CNT MON1_DATA CUE_MIXO \$_QUT_DATA TO DVC_CNT >>CUE_MDATA #37/36 #39 #31 #7/36 #7/36 R40 #32 #7/38 TC54 MC74HC[8AF-R OREC_FRN>> FRON DYC_CNT NC74HCS41AFR CZ R14 330/J& TO DVC_CNT | F554_IP TO ADIO_CON | F5_10 TO DVC_CNT | F6_10 TO DVC_CNT | F6CFRP12 TO DVC_CNT | F6CFRP1 TO DVC_CNT | F76_IP TO DVC_CNT | F76_I TO A_D10_CON | S_IN_DATA12 TO A_D10_CON | S_IN_DATA14 621 0. 01/Jun 9 2 2 MC74HC54TAFR C22 0, 01/UBN MC74HC541AFR R27 330/J6 FS64_1>>> FRON_DVC_CLK MCTAHCS 11 AFR 0. 01/UBN D MC74HC153F-R C25 F5256_1>> FROM BYC_CLK SEL_FHO FROM DVC_SYS TO DYC_PLL >>REF_FH 0. 01/UBN NC74HC5 61AFR 9 = 2 MCTENCS 11 AFR C26 10 10B MC74HCSSTAFR 334/36 R23 0. 0 L/UBN NC14HC01AF-R R25 10K/J6 F5256_2>> FROM DVC_CLK TO DVC_COM >>FS155_P 0. 01/UBN TO DYC_CON MC74HC74AF-R ICS0 T74HCT501AF1 R12 | 108/JE WC10H125MR ICS MCIOHIZAMR FS128_1>> FROM DVC_CLK ISM_CK_P FROM DYC_CON ISM_CH_N FROM DYC_CON TO BYC_COM 78 11 77 12 78 13 75 14 74 15 73 16 72 17 71 18 D. 91/UBM 8 A7 7 A6 6 A5 5 A4 4 A3 3 A2 2 A1 TO DVC_COM >> DAVDCK_N TO DWC_CNT >> CLKER MC10H124MR TATHCTS AT AF 1 ICB MC10H124MR T 0.01/USN ₹8170 130/JE COM 14 ICB NCIONIZANA R28 GROUND GROUND GROUND ICS MC10H124MR 1C4 MCLOHIZSMR ICS4 MC74HCG8AF-R GROUND 2 9

2-103

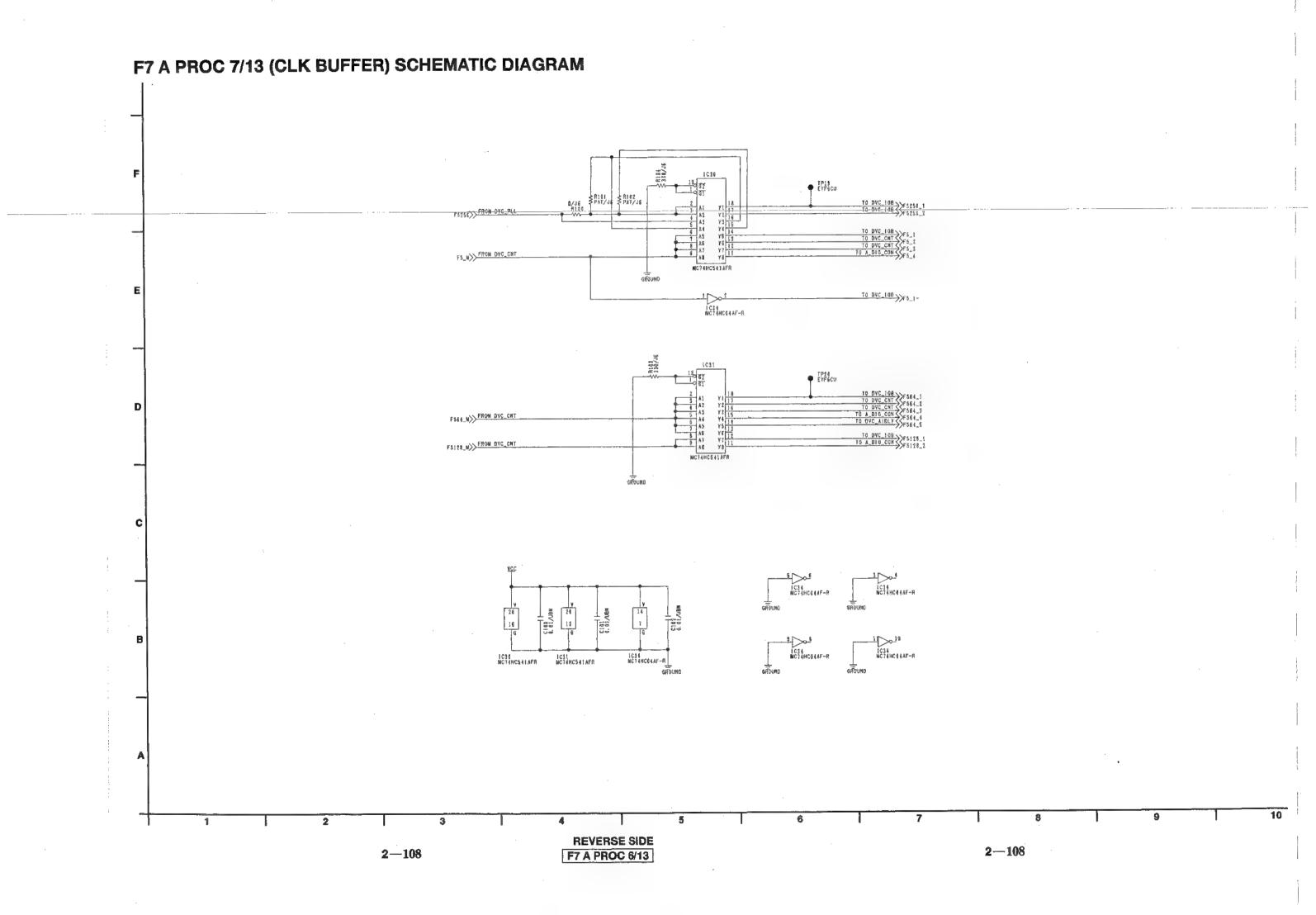


F7 A PROC 4/13 (AUDIO CNT) SCHEMATIC DIAGRAM F4_Z FROM DVC_CLM. FS64_2 FROM BVC_CLE FB04_10 FROM BVC_60B \$18L/ac SYPS_L FROM DYC_SYS SHOW DAC LOS PROJECT ALT SEE THE SEE THE A_GHTGS PROM DVC_SYS SY_MEN FROM GYC_SYS FROM DYC_INF << SCHOOL Tamba sa FROM DVC JOS HOUNA

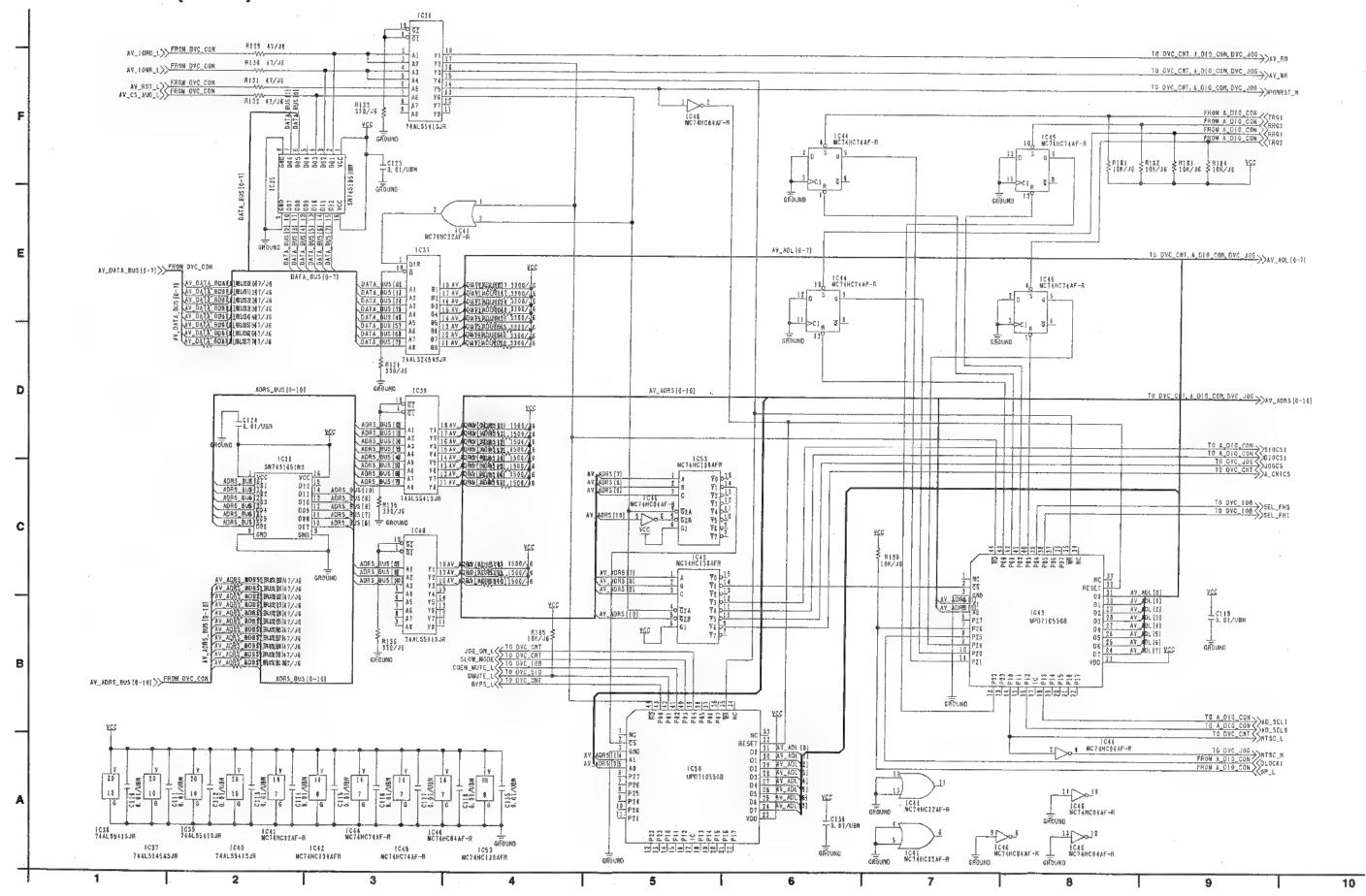
2 - 105

F7 A PROC 5/13 (SLOW CTL) SCHEMATIC DIAGRAM DSP_RST>>> FROM DVC_CNT 1061 TOBN 1063 TO DVC_CRT 1017 7040068J-R1 AV_ADL 10-23 >> FROM C AV_RO>> FROM ! 9 8 1017 7480045J-RJ \$R75 PAT∕J 1073 T0.01/UBN 10. 01/UBM ±0.72 ±0. 01/U8N 1017 14AC045J-RI TO DUC_CHT >> SPOI [8-15] 1017 76AC04SJ-R1 DA((8-15) 1C17 74AC048J-R1 DAT |0-15| >> FROM BYC_GRE 上6.01/Jen DA1 [0-15] GROUND #C74HC 1075FR 11 0 TO DVC_CNT >> 105NWE 1017 74AC045J-R1 2 REVERSE SIDE 2 - 1062-106 F7 A PROC 4/13

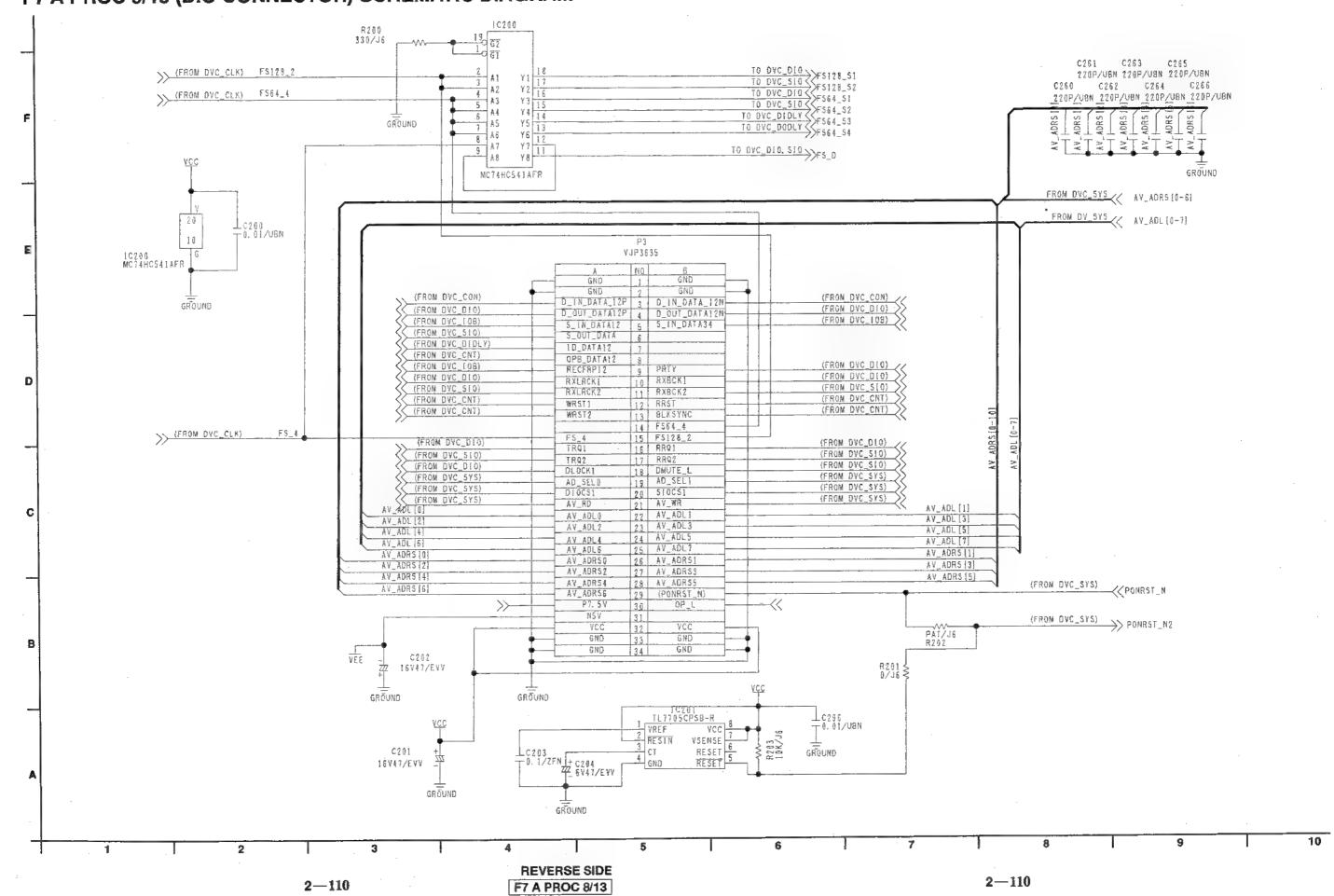
F7 A PROC 6/13 (PLL) SCHEMATIC DIAGRAM P1. 5V FROM DVC_COM CS | 16 V | 4/E VY (R) TPS TPS TP7 YURGE 6 C92 0. 61/UBN | 10%/JE \$ 140/JE \$ 886 ₹ 1000/J8 C_FH> FROM DVC_CNT RBC 1556/JE RM2 C88 5600/JG 50VORT/EVV(R) IC26 74ACD4SJ-R1 TC86 C93 16410/EVV(R) CS4 6. 01/US# R_12>> FROM DVC_CHT ¥887 ¥100/JE \$ 1000/J8 \$ 1000/J8 C_L2>> FROM DVC_CNT TO BYC_CHT >CK12 JANCO127-MI 1+ C87 227 1- 50 V68 L/EVV (R) 10 BYC_CLK >>F3256 11 1626 748C045J-R1 1 026 148045J-RI 11 0 17 1026 7480445J-R1 2 - 1072 - 107



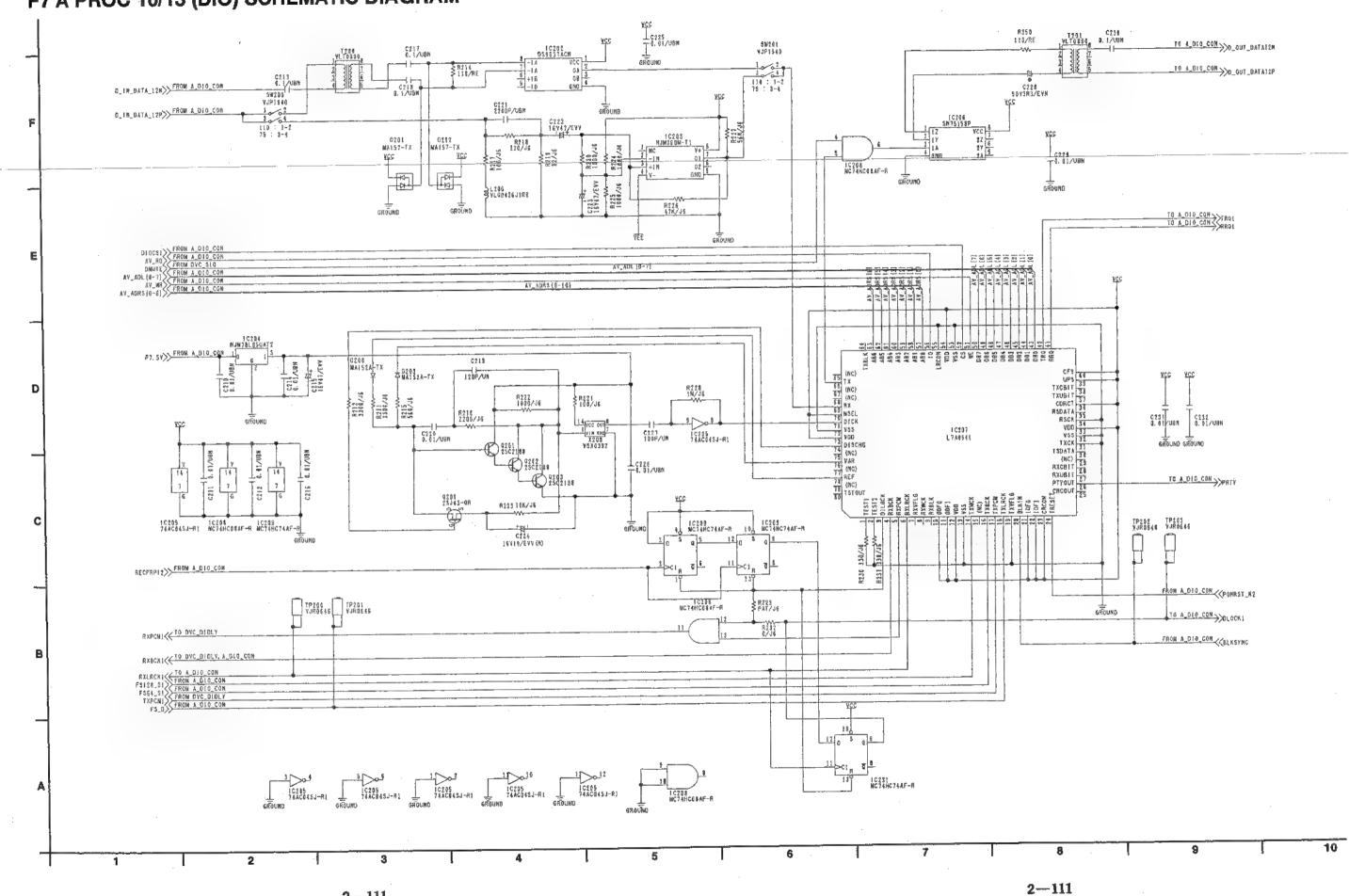
F7 A PROC 8/13 (SYS IF) SCHEMATIC DIAGRAM

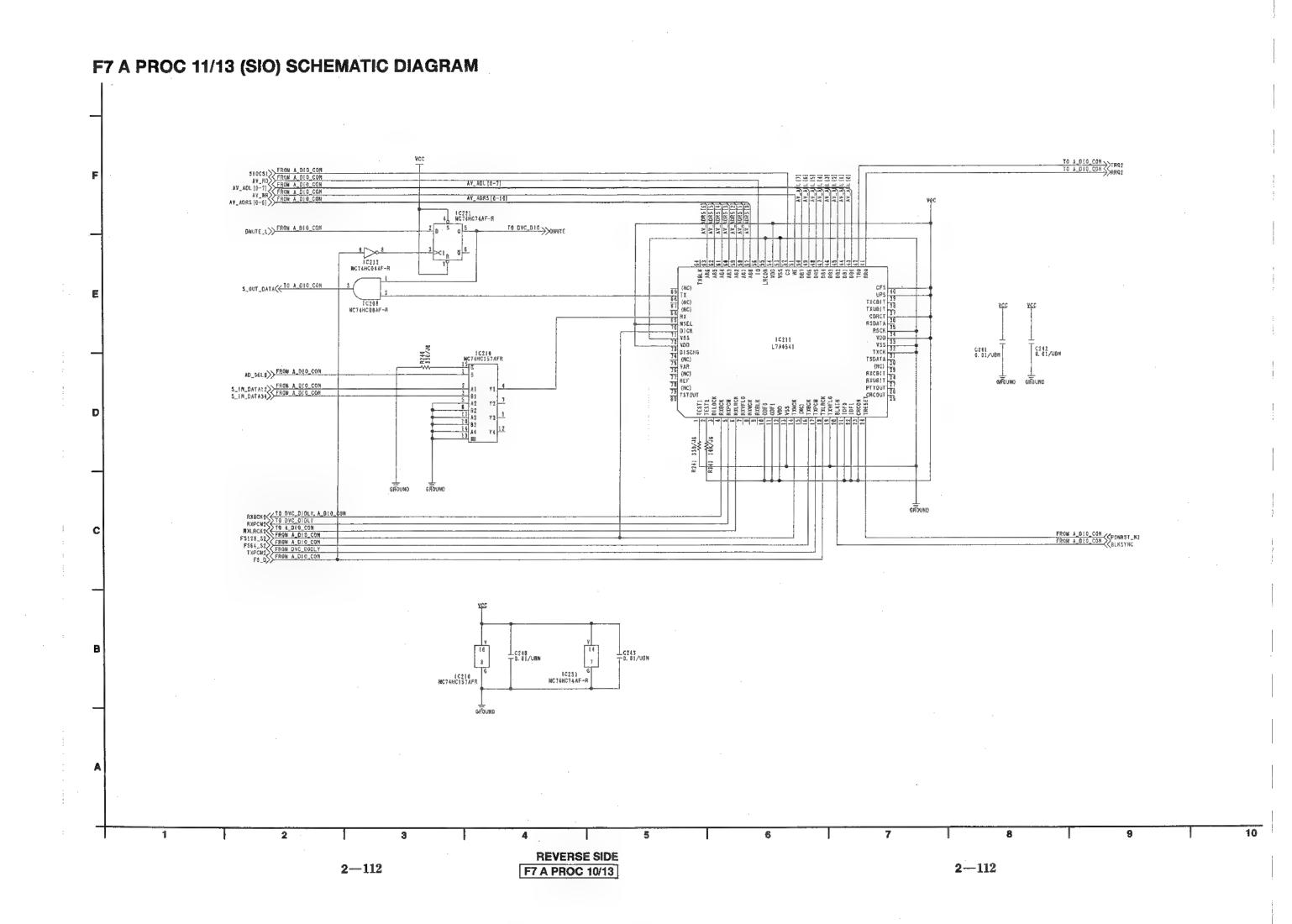


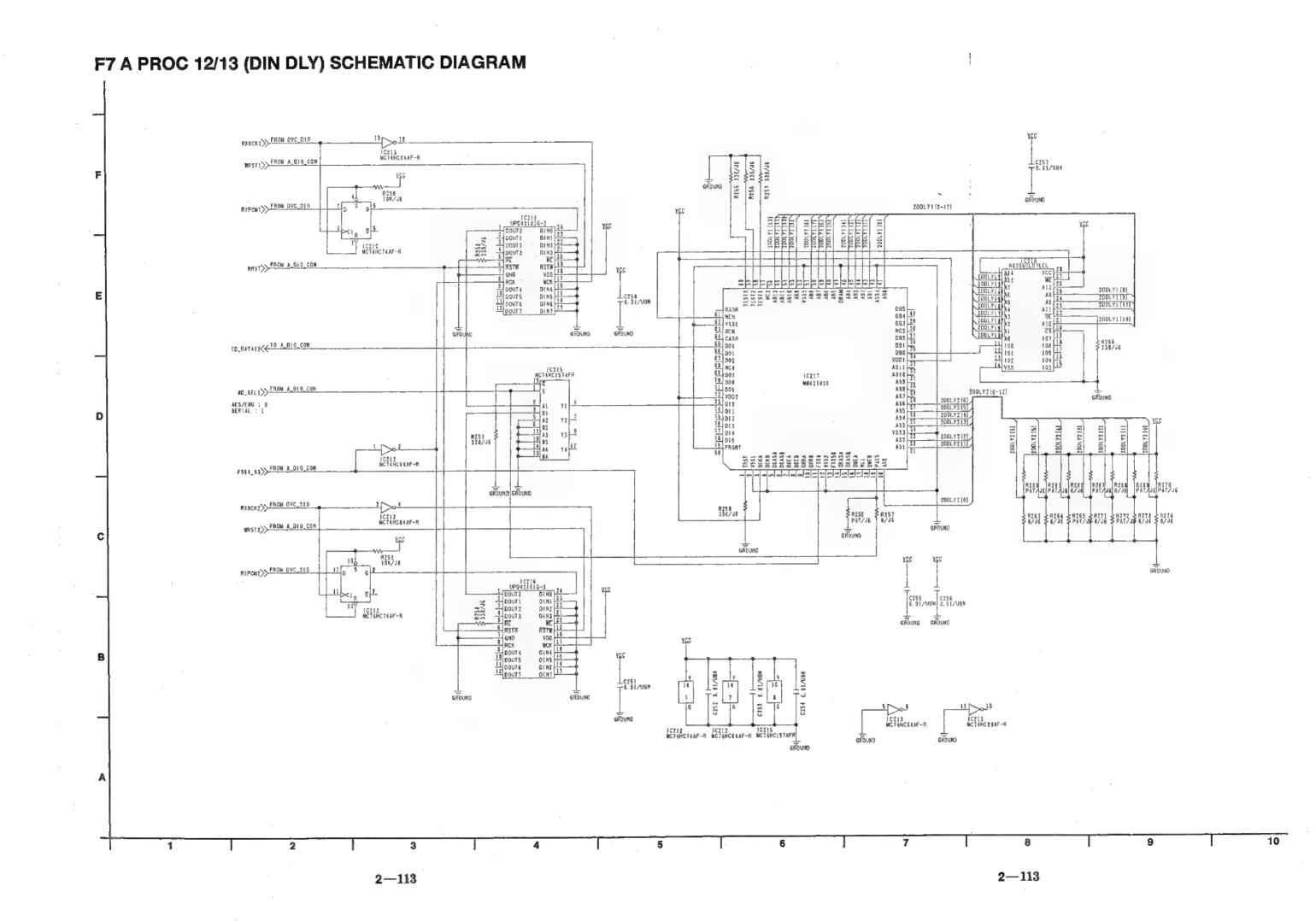
F7 A PROC 9/13 (DIO CONNECTOR) SCHEMATIC DIAGRAM



F7 A PROC 10/13 (DIO) SCHEMATIC DIAGRAM





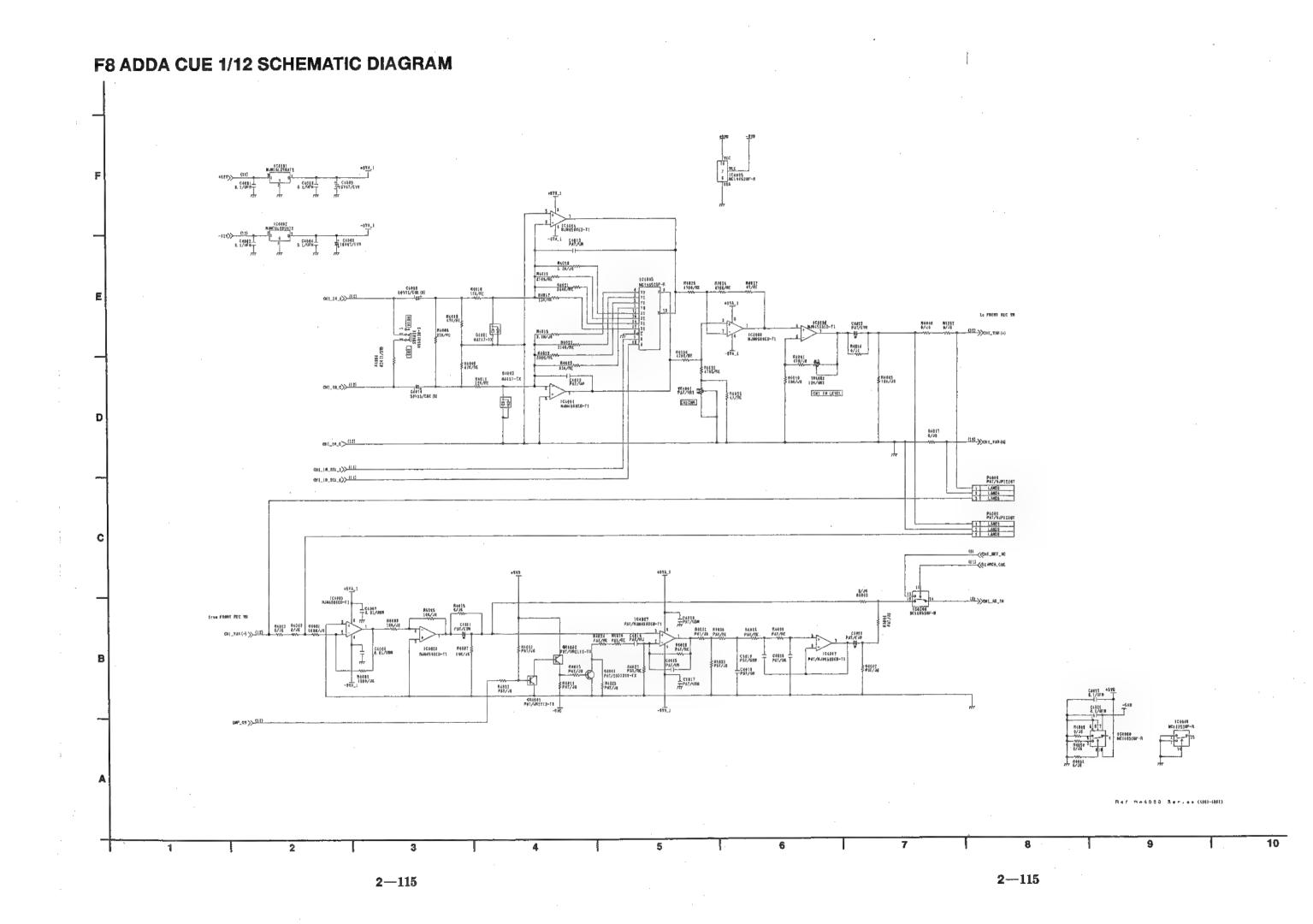


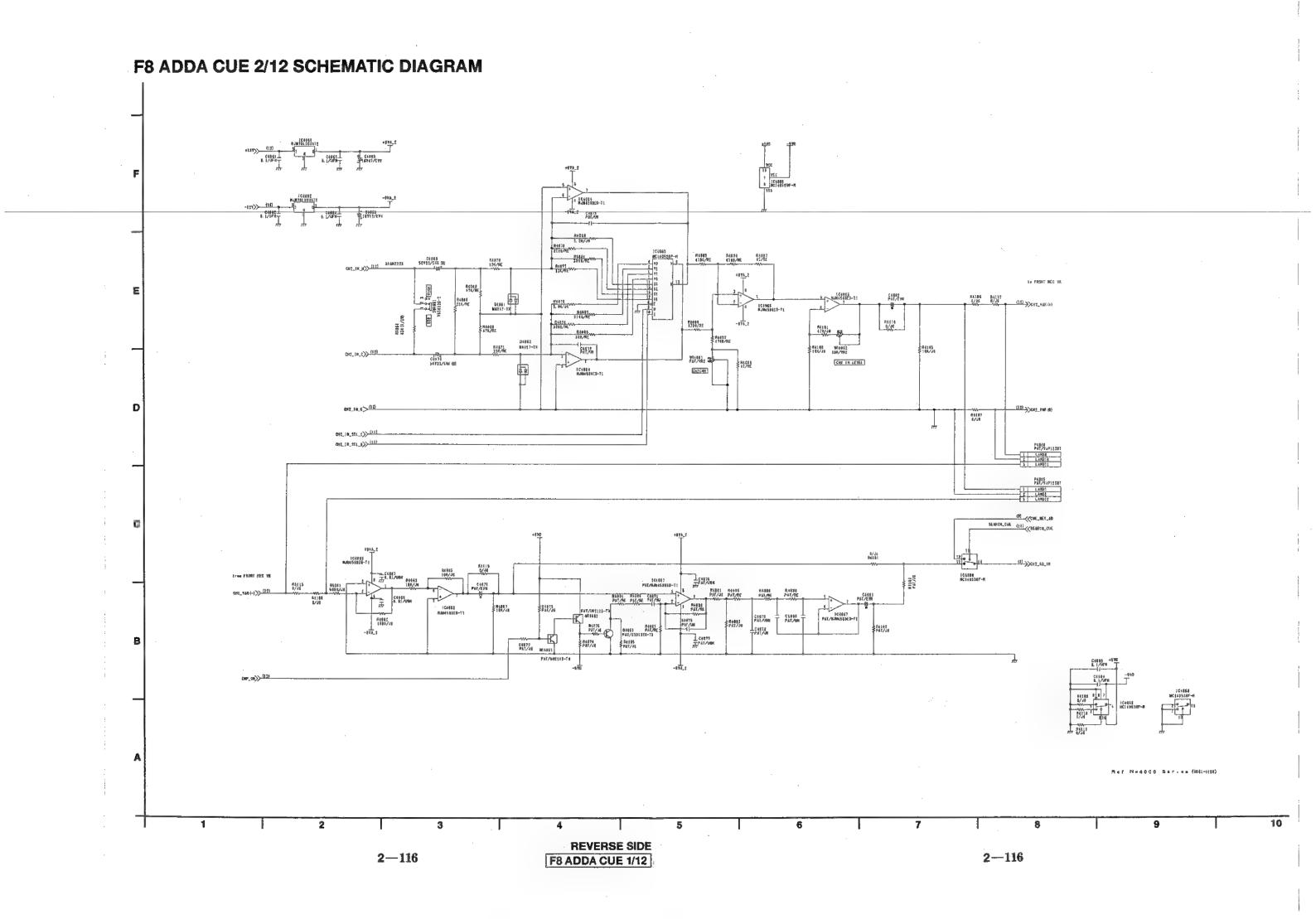
F7 A PROC 13/13 (DOUT DLY) SCHEMATIC DIAGRAM QNADR4 [6-12] R291 330/J6 R291 330/J6 WW R292 330/J6 DMADR4 [12] DMADR4 [6] DMADR4 [6] DMADR4 [4] DMADR4 [4] DMADR4 [2] DMADR4 [2] DMADR4 [2] DMADR4 [2] TEST 53 TEST 53 TEST 53 MA 15 MA 15 MA 15 MA 16 MA RASM 82, WESE 83 OEM 64 CASM 55 DOG 88 001 67 002 88 NC4 71 004 72 005 73 010 74 011 75 013 71 014 71 015 71 014 TXPCM1 CTO DVC_DEG DLY_CNT2[0-6] 10219 M8621326 ⊥c291 ⊤0.01/∪9m R306 \$ 8387 \$ 8388 \$ 8309 \$ 8321 \$ 8322 \$ 8323 \$ 0/J6 \$ 0/J6 \$ 0/J6 \$ PAT/J\$ 0/J6 \$ 8/J6 \$ 0/J6 TRST 44551 00.00 0 C290 0.01/UBN 0.01/UBN รส์งินพอ สสังินพอ 8298 330/J6 ₹R296 \$330/J6 OMAGR3 [0-12] DNADES | 1037 | 103 | 104 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 | 105 R294 336/J6 R294 336/J6 R295 338/J5 085 40 084 33 083 39 082 37 082 37 082 37 080 35 TXPCM2 TO BYC_SIO DLY_CNT3 (0-6) OPB_DATA12>> FROM A_D[0_CON C297 0.01/UBN 0.01/UBN ±0285 ±0.01/UBM FS64_54>> FROM A_DEG_CON \$R299 \$330/J6 R297 \$330/J6

REVERSE SIDE

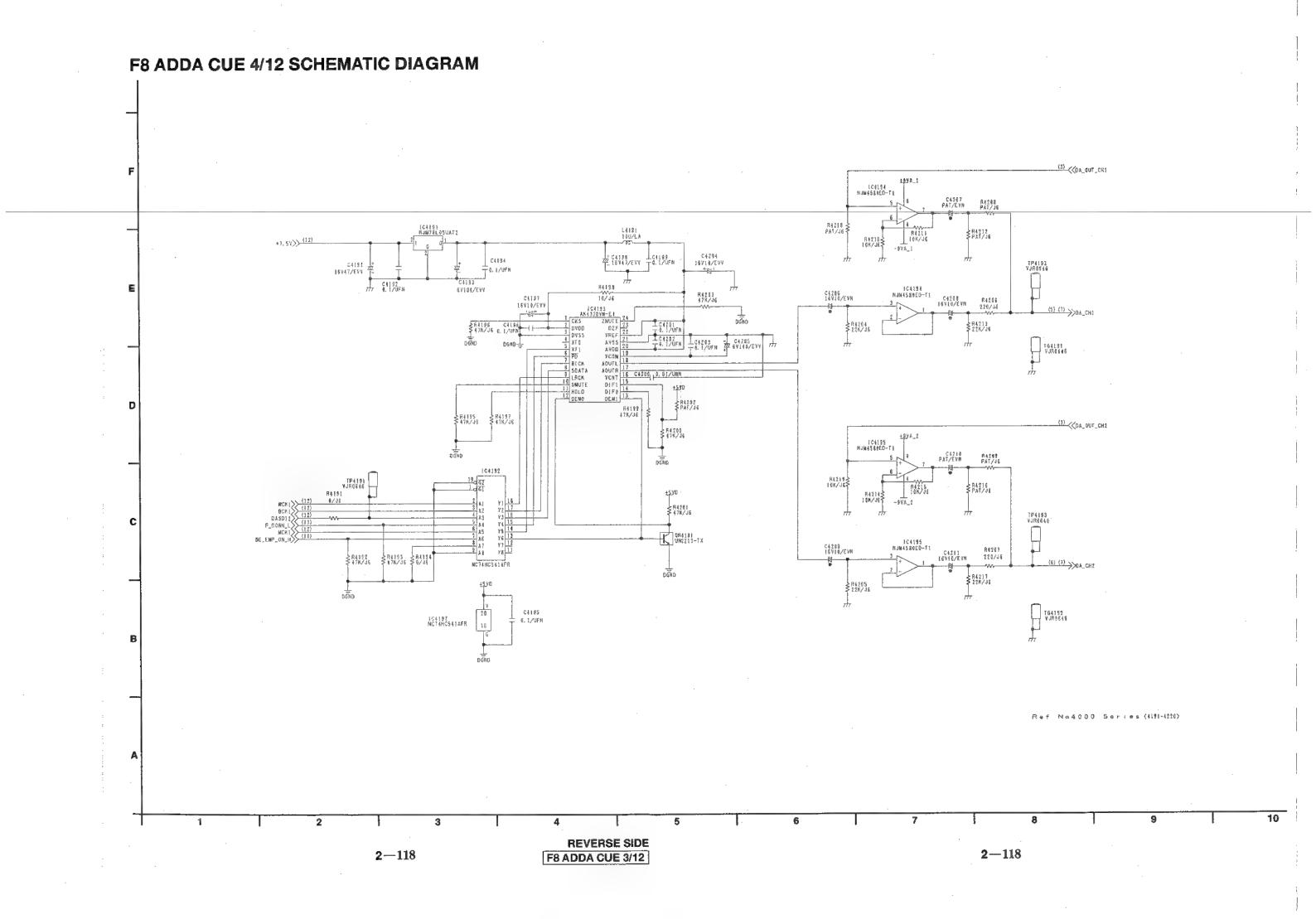
F7 A PROC 12/13

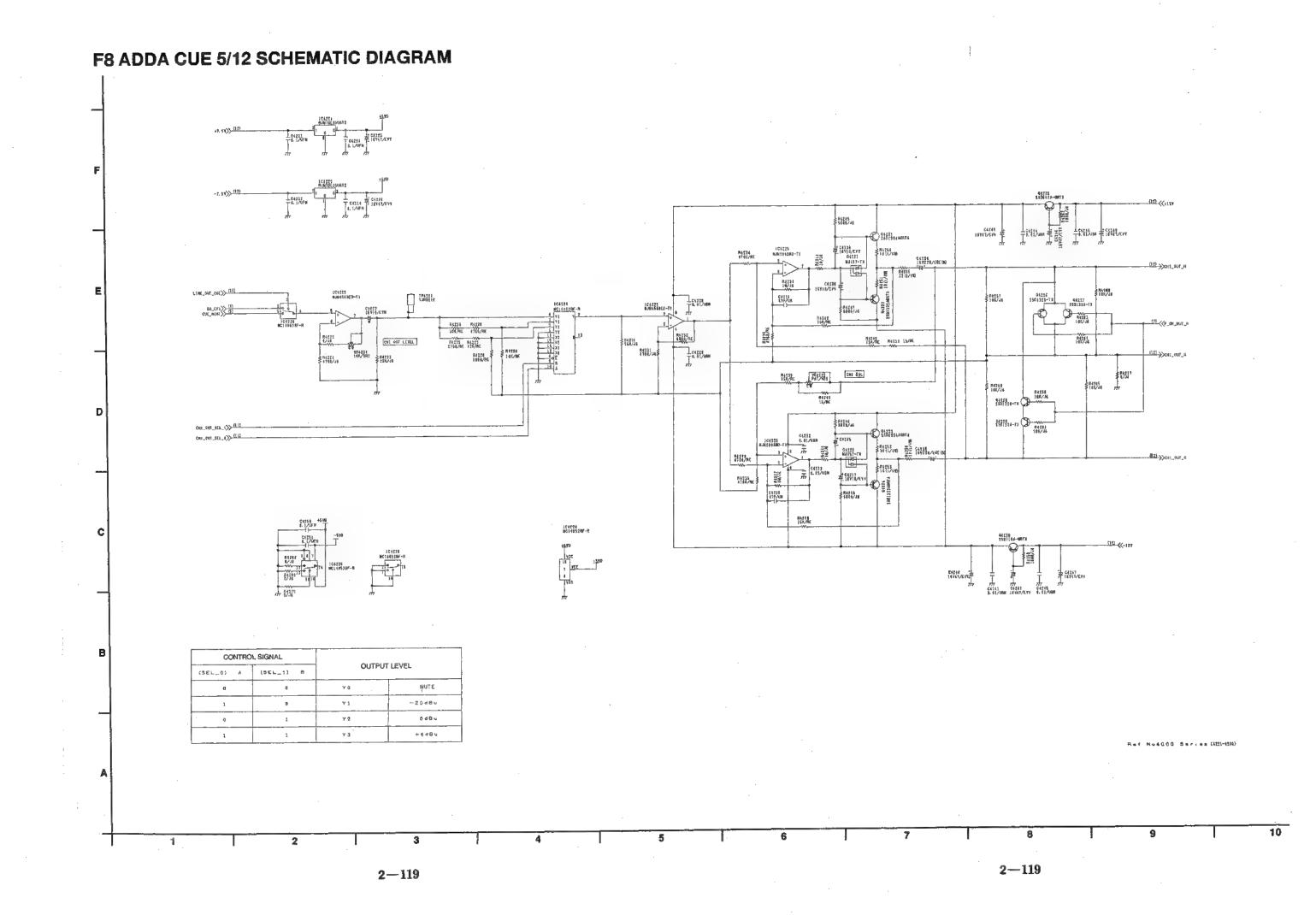
2 - 114



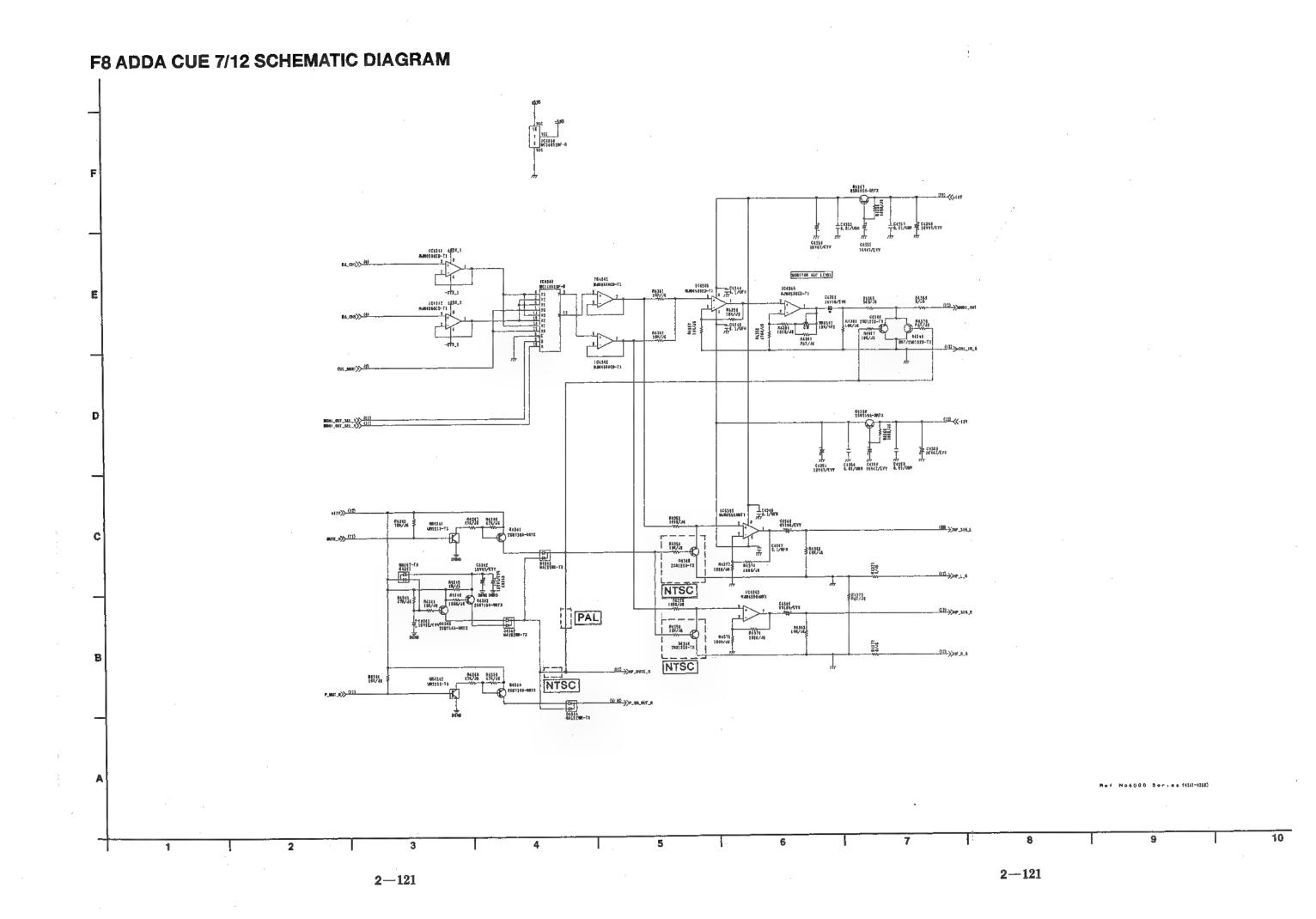


F8 ADDA CUE 3/12 SCHEMATIC DIAGRAM R4137 10K/RE R4156 T0K/R6 TP4121 YJR8646 1C4129 NJM4580ED-T1 ISYZZ/EVV RAINS 104121 C4169 . 720P/UN-C4171 18718/EYM (4) (9) >>>DA_GUT_CH1 NJ442100MD-C4123 16V22/EVV R4125 16V22/EVV TOK/RE 84121 310/46 IOK/RE R4187 7777 77752 R4152 15K/RE C4155 R4147 14722/EVY 10K/RE R4150 € G4187 1000P/UN 1G4129 MJM458DED-T1 43YA_1 上C4143 十22000/WBN ₹ R4162 \$ 10K/J6 \$ R4127 \$ 4700/RE C4134 C4137 8. I/UFN 8947/EVV R4140 470/RE 04127 16VIA/EYY 24160 150P/UN -3YA_1 +9VA_2 |C4130 |PAT/NUMASSGED-TI R4138 10K/RE C4156 84148 PATZEYV PATZRE AINL+ AGND \$ AINL- AOUTR\$ 1991 AOUTR\$ 1 TP4122 YJR0646 C4142 33F JUN CATTO PAT/EVN C4135 33P/UN _______________CH2 PAT/RE PATZETY BATARE ## 155 ## 155 ## 155 604122 NJN2100ND-T1 CH2_A0_IN>\(\frac{(2)}{2}\) 130/J6 C4125 16722/EVV R4126 16722/EVV R4126 | C4134 | PAT/NJN4588ED-T1 PAT/UM R4163 PAT/J6 HALISE PAT/RE 2208P/UBN T R4124 14K/J6 C1151 PAT/UN C4136 C4138 777 8. 1/UFN 8947/EYV ₹4190/RE - 16A10\EAA -9¥A_Z \$81131 \$1706/RE DGMD TP41Z2 VJR8646 TP4124 VJA0648 104123 174VHC244FT1 TP4125 VJR0846 R4168 10K/46 § #C4131 F74VRCT244F1 <u>⟨12}</u>>>A0S012 RATE PAT/JA TP4126 VJR0648 R4164 | BK/J6 ≸ CUE_NEW_DATA > (12) 84168 478/J6≯ -(12) CUE_MET_DATA 33BPF 1 IC4124 C4131 NJM78L05UATZ EVIBO/EYV 104125 NG\$2AP3002PL 01145 6.1/UFN 64162 ± 4.1/2FN T Ref No4000 Series (4171-4140) 2 2-117

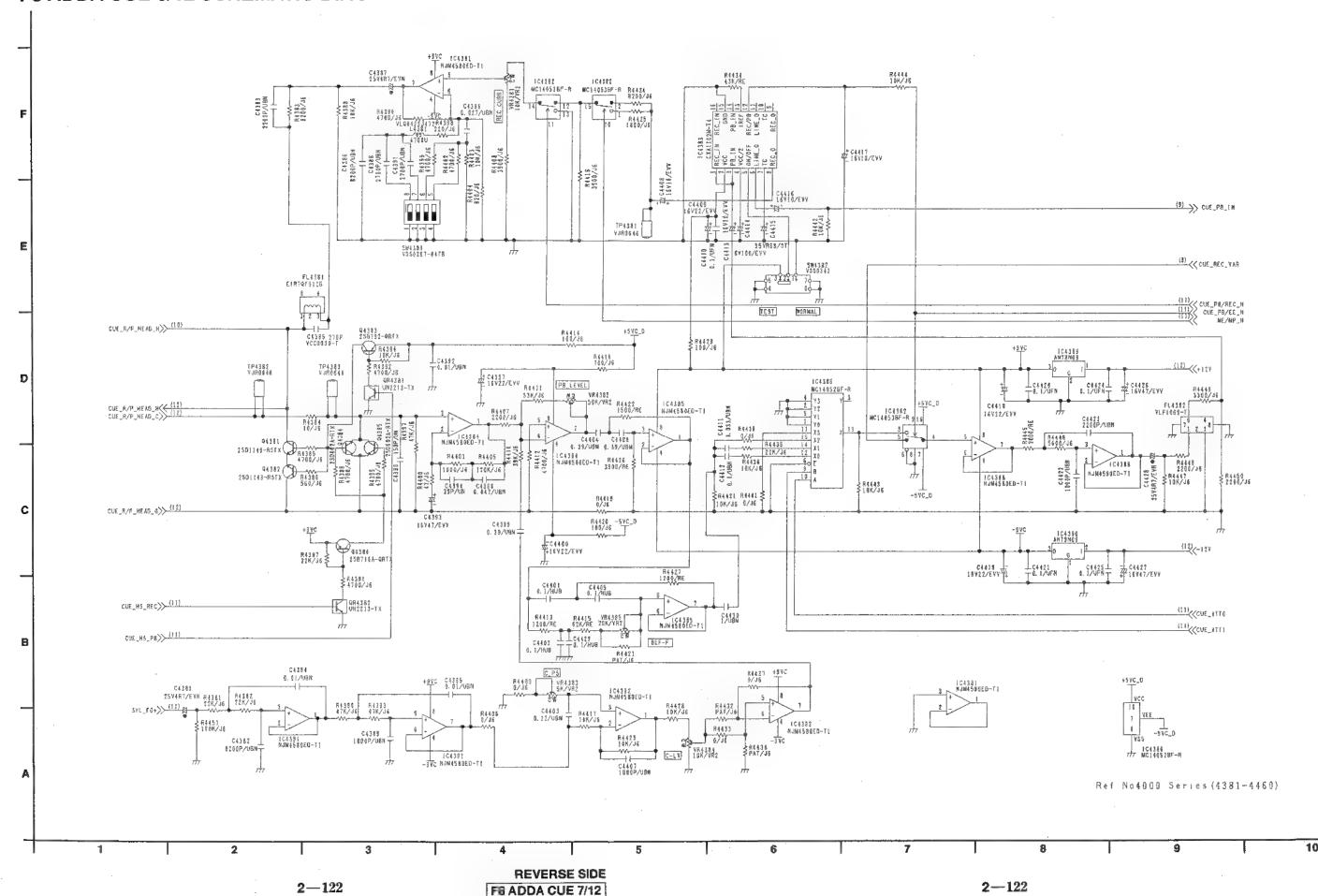


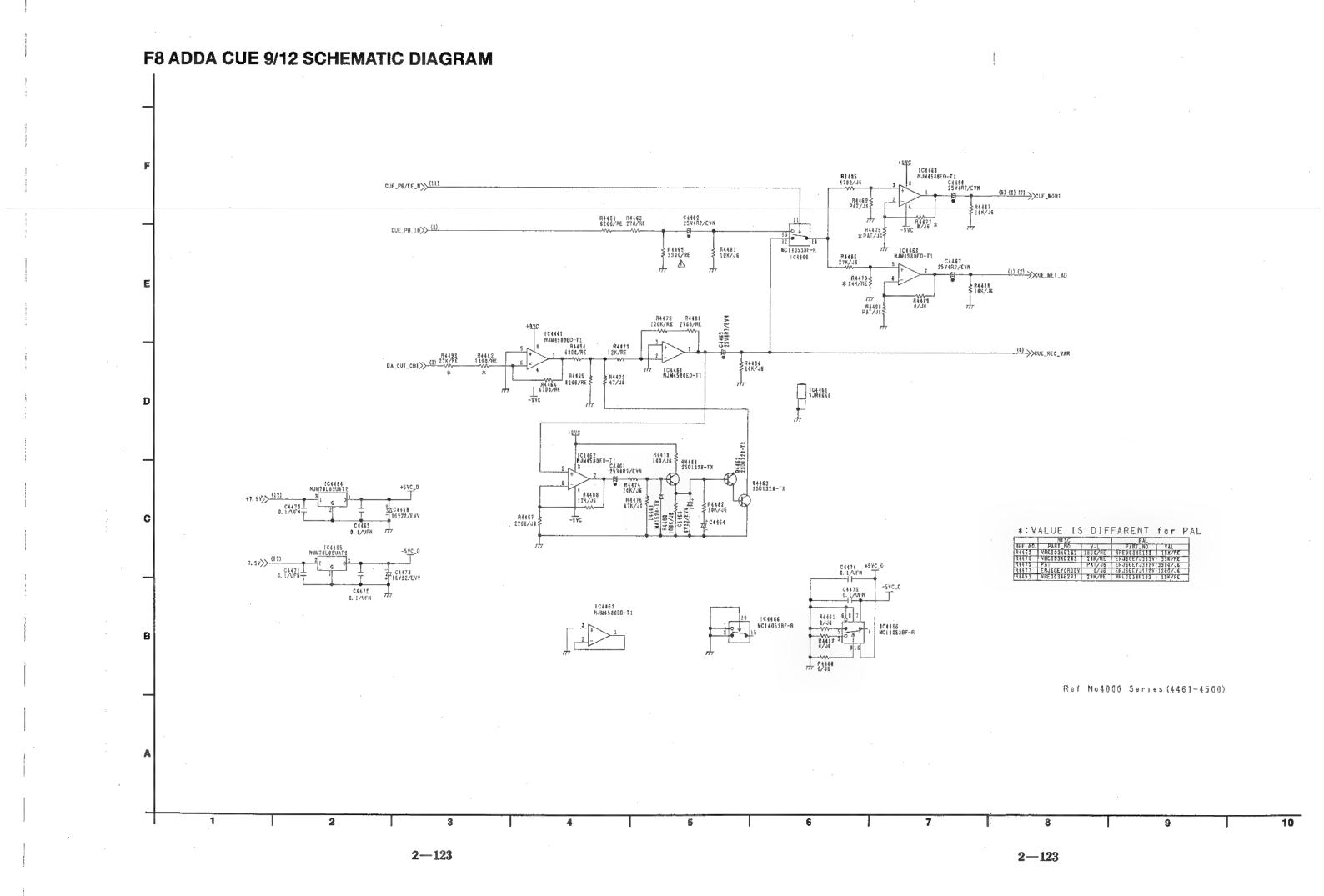


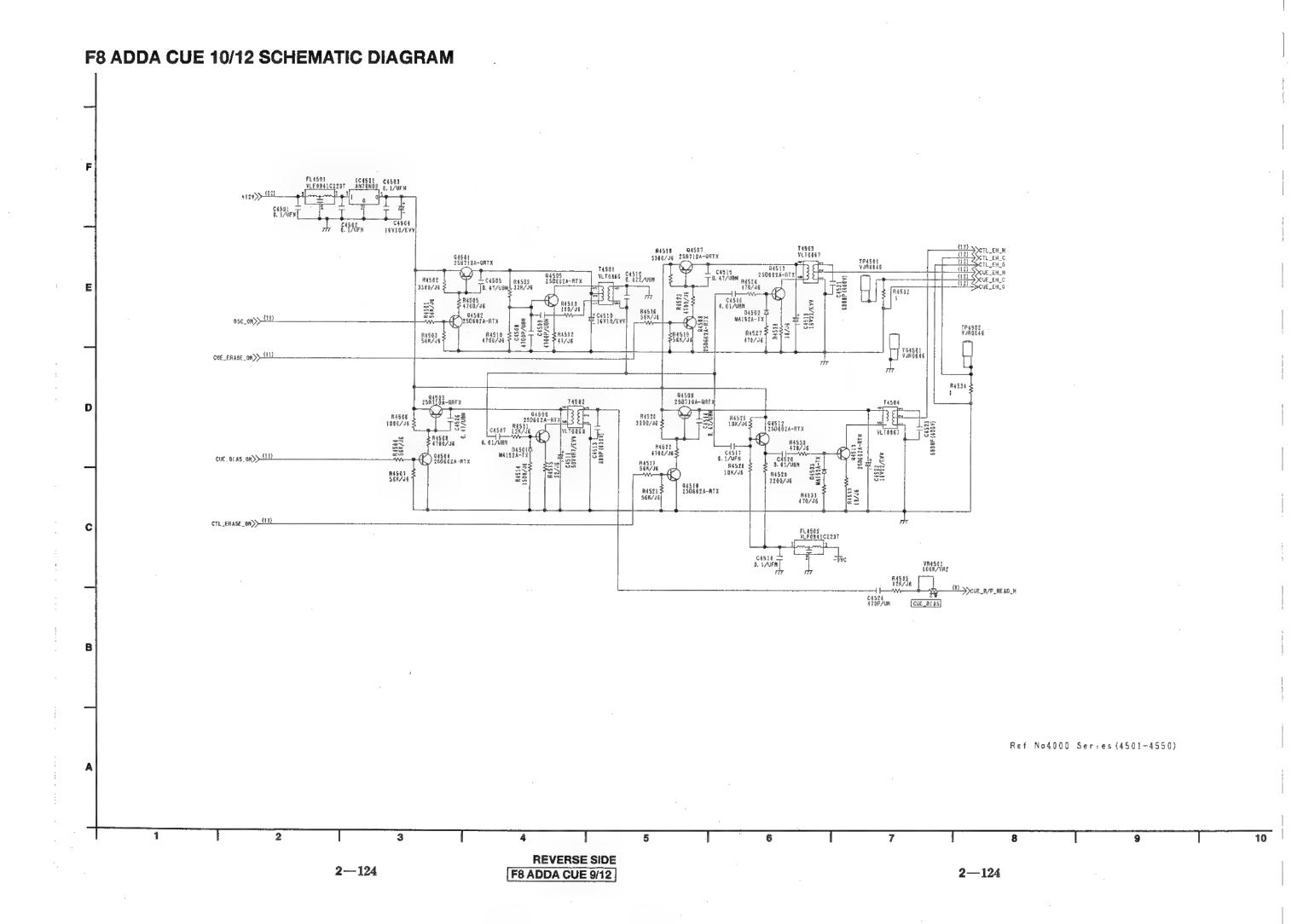
F8 ADDA CUE 6/12 SCHEMATIC DIAGRAM R6211 110/15 LIME_801_00E>>-(1) C4485 41P/VIII ICAZZ] BJUGBUEZ (7) ((P_OM_MUF_H 84186 1480/WE (12) >> OH1_OUT_4 R1525 €148/34 CHI_OUT_SEL_1>>-(LI) CHZ_OUT_SEL_0>> (11) CONTROL SIGNAL OUTPUT LEVEL (SEL_0) A (SEC_1) B Y 0 Υ1 Ref Ma4600 Series (421-440) REVERSE SIDE 2 - 1202 - 120F8 ADDA CUE 5/12



F8 ADDA CUE 8/12 SCHEMATIC DIAGRAM

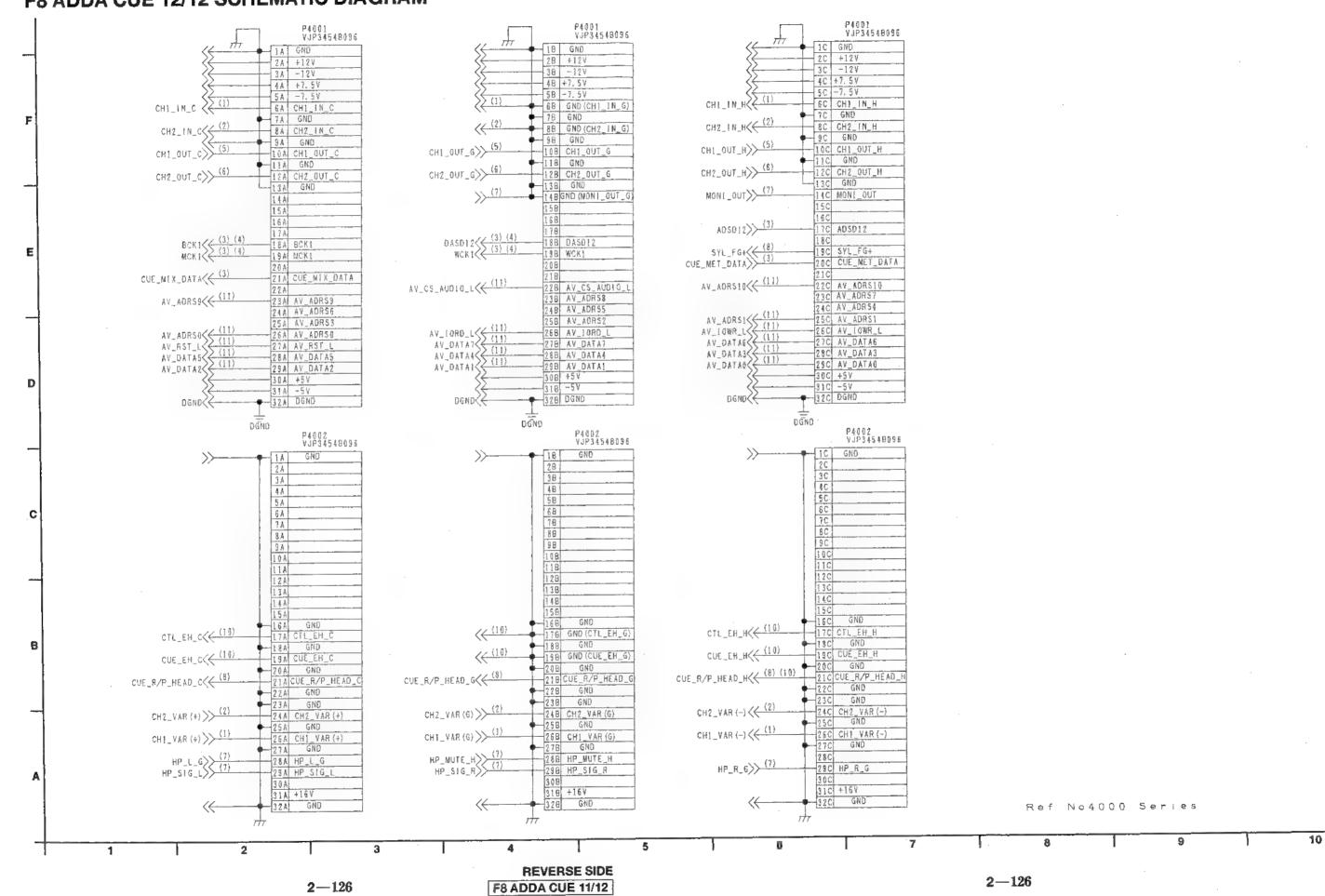


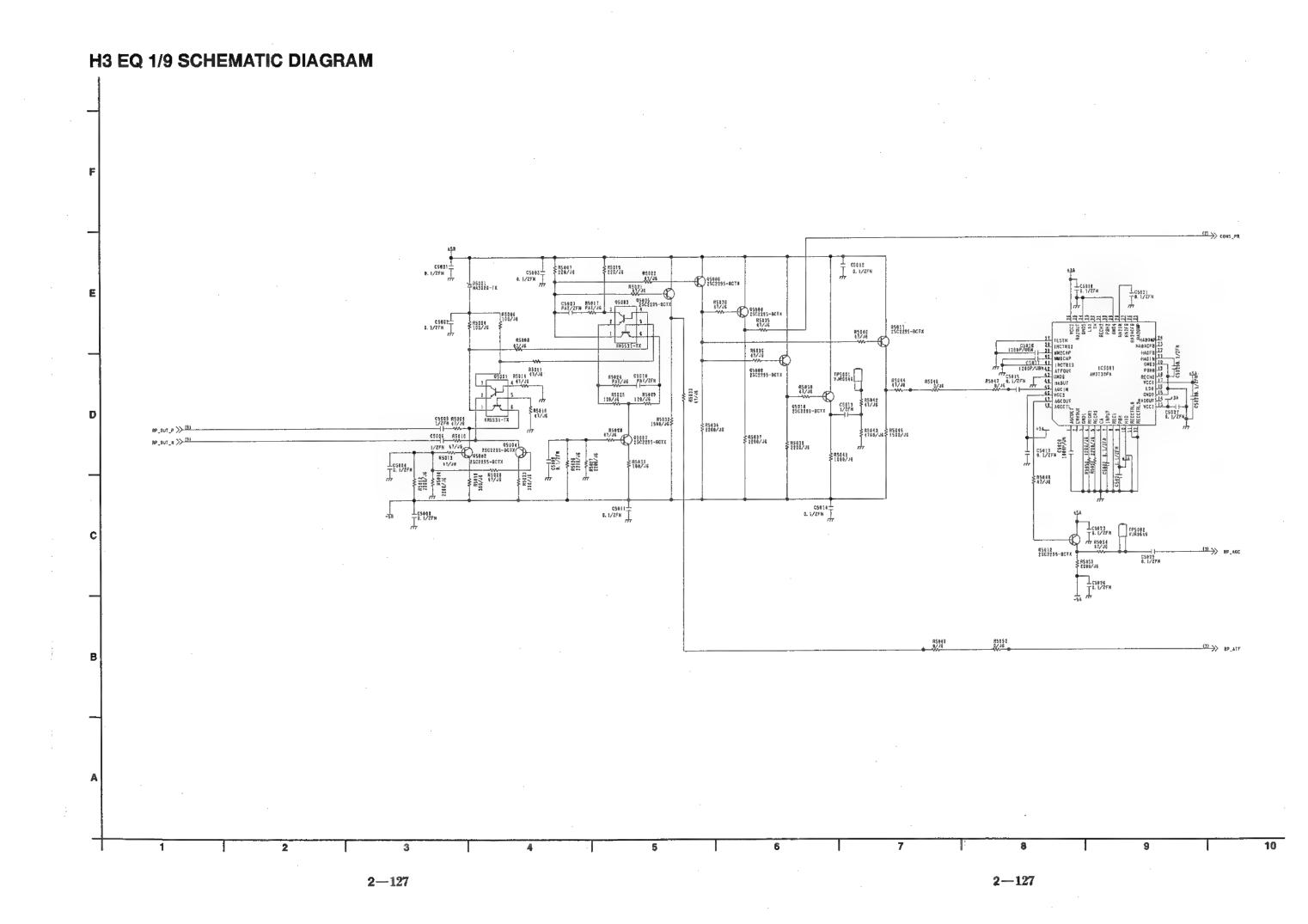


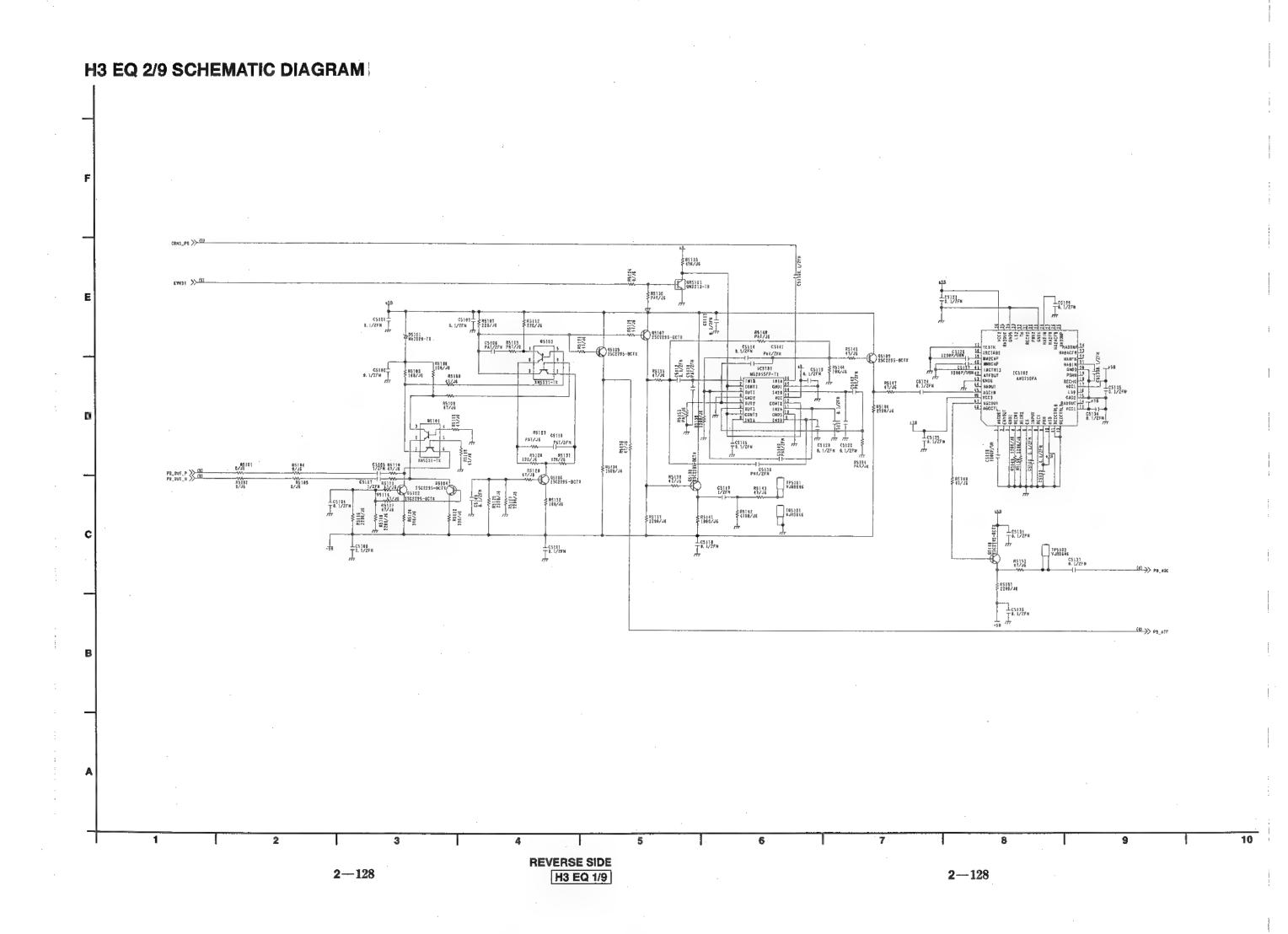


F8 ADDA CUE 11/12 SCHEMATIC DIAGRAM 0. 61/V8N 104553 74ALS245ASJR Post Post Post T DIB AV_DATAQ AV_DATA1 AV_DATA2 AV_DATA2 AV_DATA3 AV_DATA3 AV_DATA3 AV_DATA5 AV_DATA5 AV_DATA5 AV_DATA5 AV_DATA7 104557 UP0710553B (7) P_MUT_H (7) HUTE_H (3), (4) P_DOWN_L (5) CH1_OUT_SEL_0 (5) CH1_OUT_SEL_1 (6) CH2_OUT_SEL_0 (7) CH2_OUT_SEL_0 (7) MON1_BUT_SEL_0 (7) MGM1_OUT_SEL_0 AV_10AD_E (12) AV_10WR_L (12) AV_RST_E (12) AV_CS_AUDIO_E 7 A6 Y6 12 R4553 8/J6 8 A7 Y7 12 R4554 8/J8 164554 GMD 7481554(SJR TAFBASJ-RI (8) CUE_ATT8 (9) CUE_ATT1 (3) CUE_PG/XE_M (9) CUE_PB/REC_N (8) CUE_HS_REC (8) CUE_HS_PB 1C4555 14F04\$J-R1 1F 0 10 14 R4551 |0K/V6 ≸ 104555 746048J-RI ₹ R4552 \$ 10K/Je RESET AV_ADRS0 (12) AV_ADRS1 (12) (10) OSC_GN (10) CTL_ERASE_ON (10) CUE_ERASE_ON (10) CUE_ERASE_ON (10) CUE_ERASE_ON (10) CUE_ERASE_ON (10) CUE_ERASE_ON (11) (21) SEARCH_CUE (15) (5) (6) LINE_OUT_CUE S 5 5 1 C4555 1C4558 UPD710S56B 101556 14AC|395JR 104556 74AC139SJR 74F04SJ-R1 £SYP AV_ADRS9 (12) AV_ADRS10 0.01/UFN 104555 745045J-R1 |C4553 |1481824545JR 104556 1440139548 104554 14AL5541SJR FL4551 YLF0941C223T +57 >>(12) 104555 74F845J-R1 DQN0 C4551 1 C4552 T 0, 1/UFN I 0 SEV47/EYY C4556 0.01/UBN 104555 746449J-R1 Ref No4000 Series (4551-45) 10

F8 ADDA CUE 12/12 SCHEMATIC DIAGRAM P4001 VJP3454B096

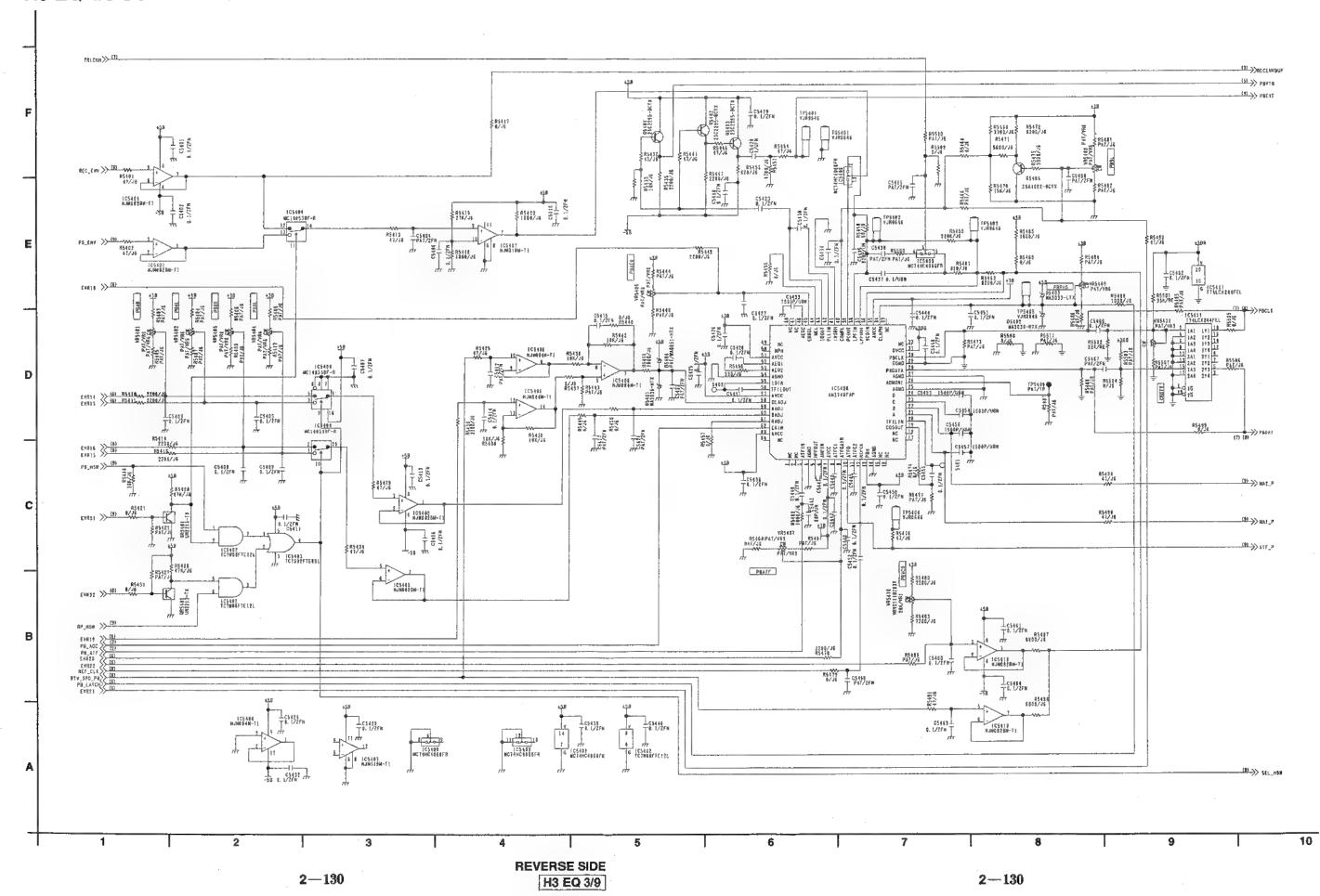


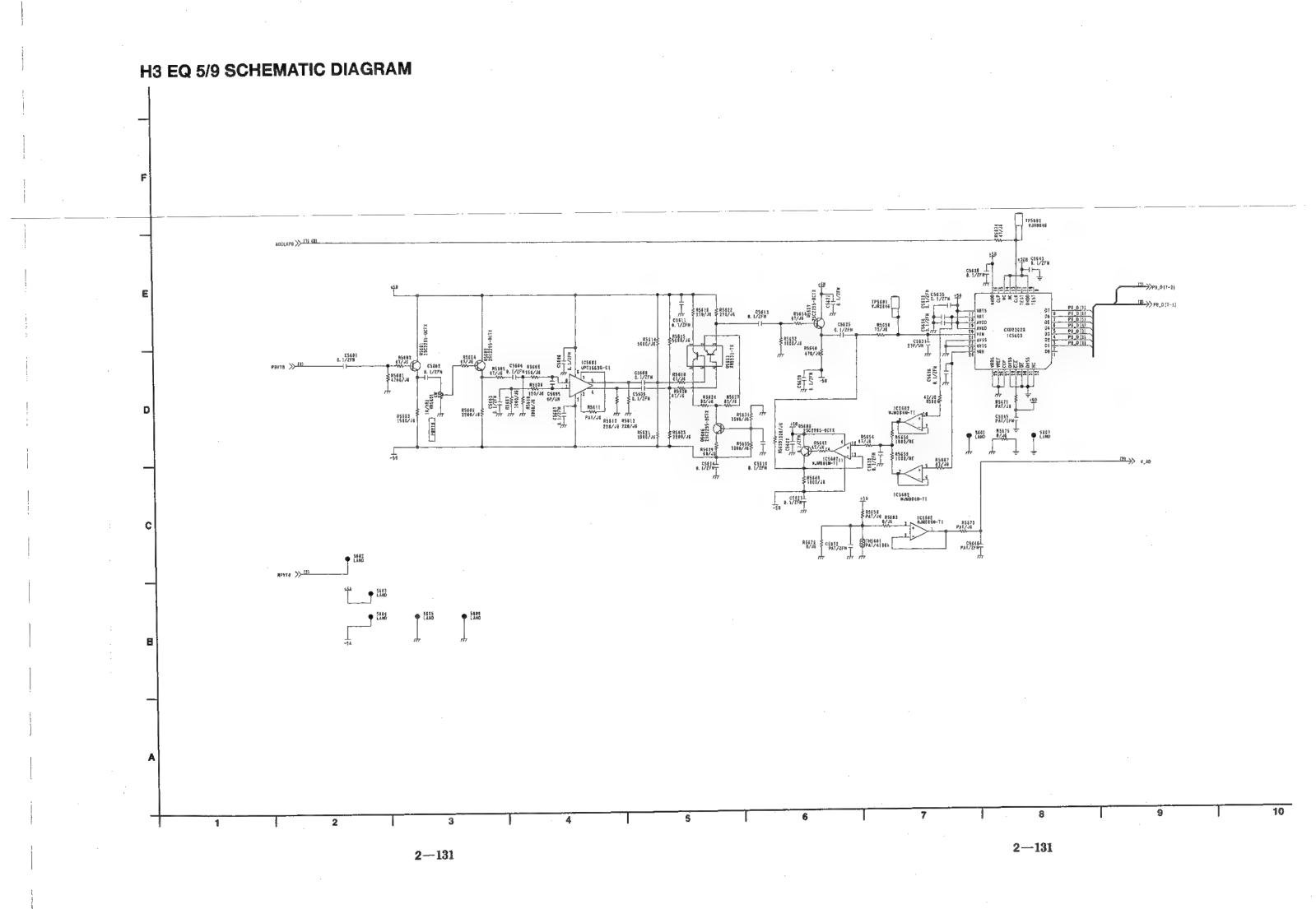


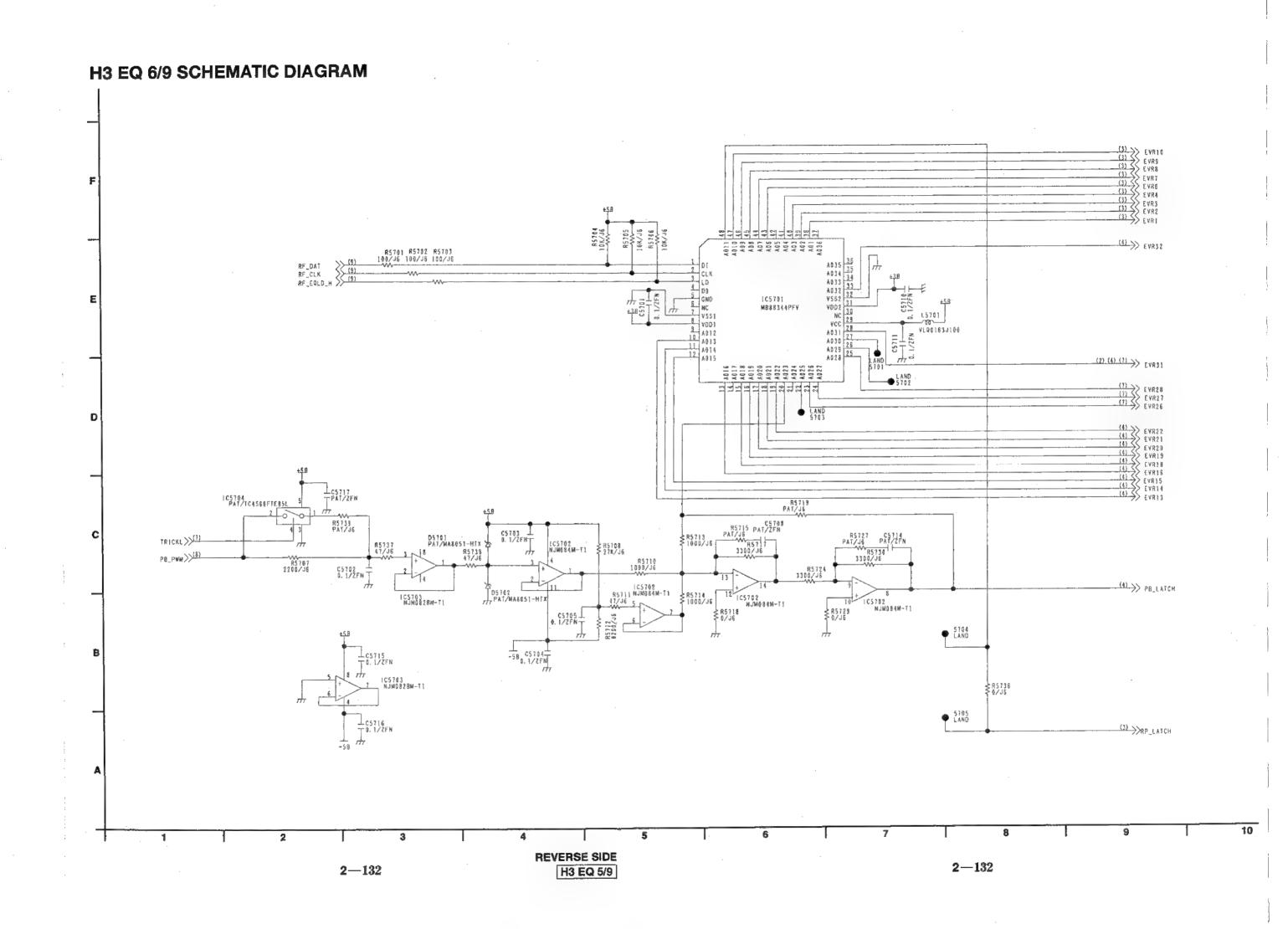


H3 EQ 3/9 SCHEMATIC DIAGRAM 45184 ### PATTO PATTO ALEREAFEL (1) (2) (3) ### PECK 4 | 141 | 171 | 15 | 15 | 15 | 15 | 2 | 141 | 171 | 15 | 15 | 15 | 3 | 141 | 174 | 15 | 4 | 141 | 174 | 15 | 3 | 142 | 174 | 15 | 4 | 142 | 174 | 15 | 4 | 144 | 174 | 15 | 4 | 144 | 174 | 15 | 4 | 144 | 174 | 15 | 4 | 144 | 174 | 15 | 4 | 144 | 174 | 15 | 4 | 144 | 174 | 15 | 4 | 144 | 174 | 16 | 4 | 144 | 174 | 16 | 4 | 144 | 174 | 174 | 4 | 144 | 174 | 174 | 4 | 144 | 174 | 174 | 4 | 144 | 174 | 174 | 4 | 144 | 174 | 4 | 144 | 174 | 4 | 144 | 174 | 4 | 144 | 174 | 4 | 144 | 174 | 4 | 144 | 174 | 4 | 144 | 174 | 4 | 144 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 174 | 4 | 174 | 4 | 174 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 4 | 174 | 86.288 PAT/J6 IE BRESS PAT/J6 THE RESTORAGE OF THE PARTY OF T R52318 85232 105/JE

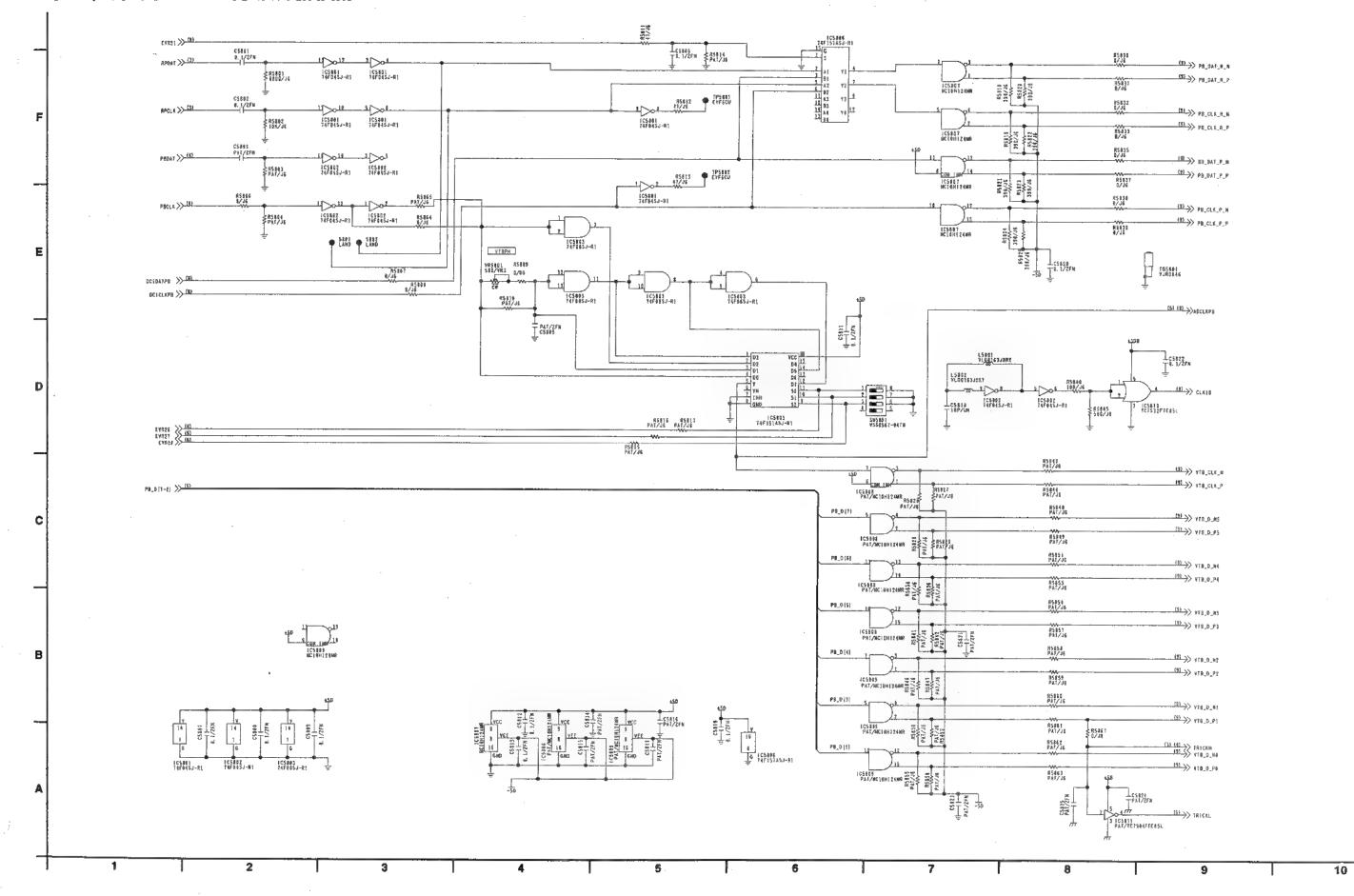
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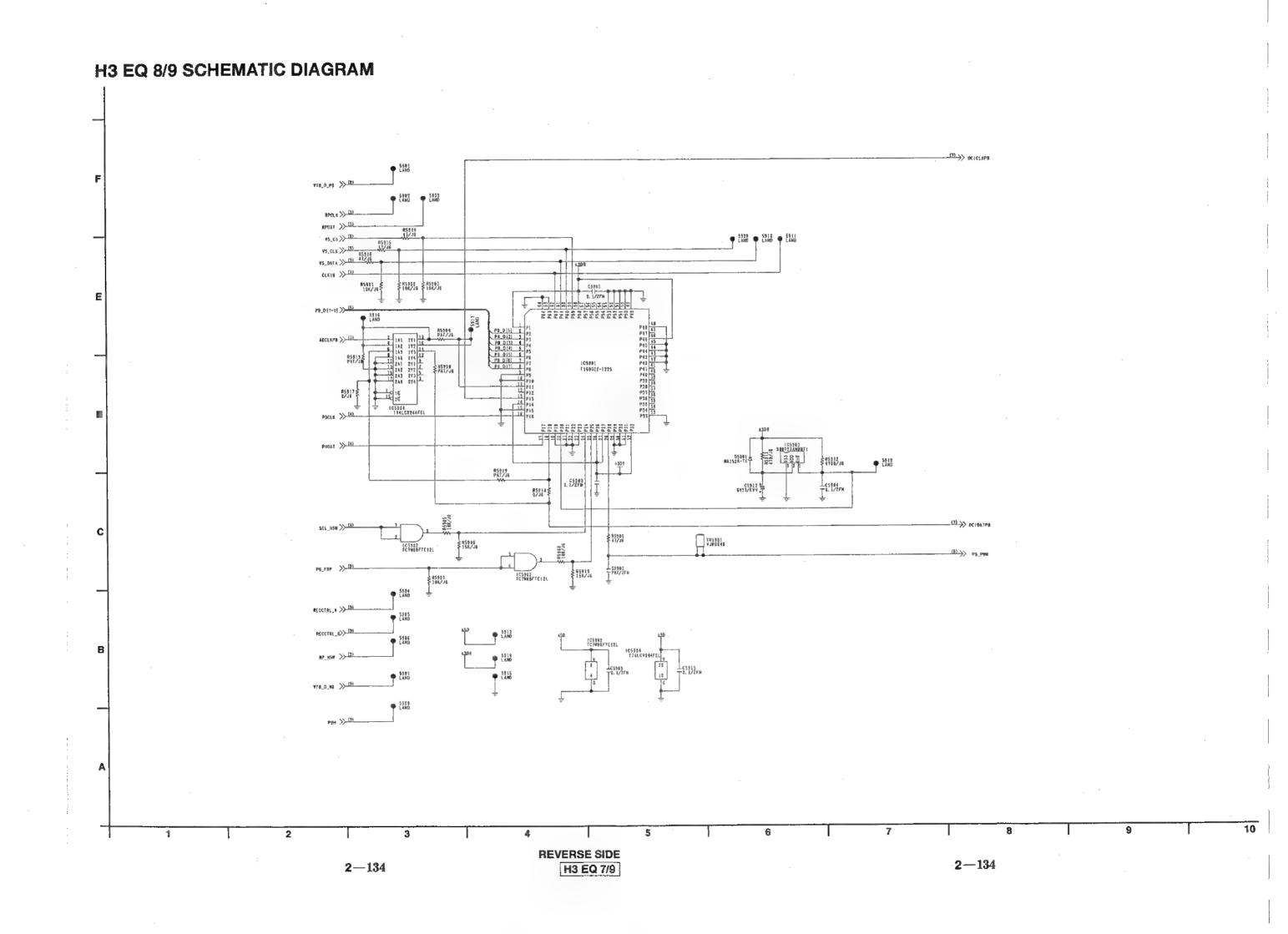




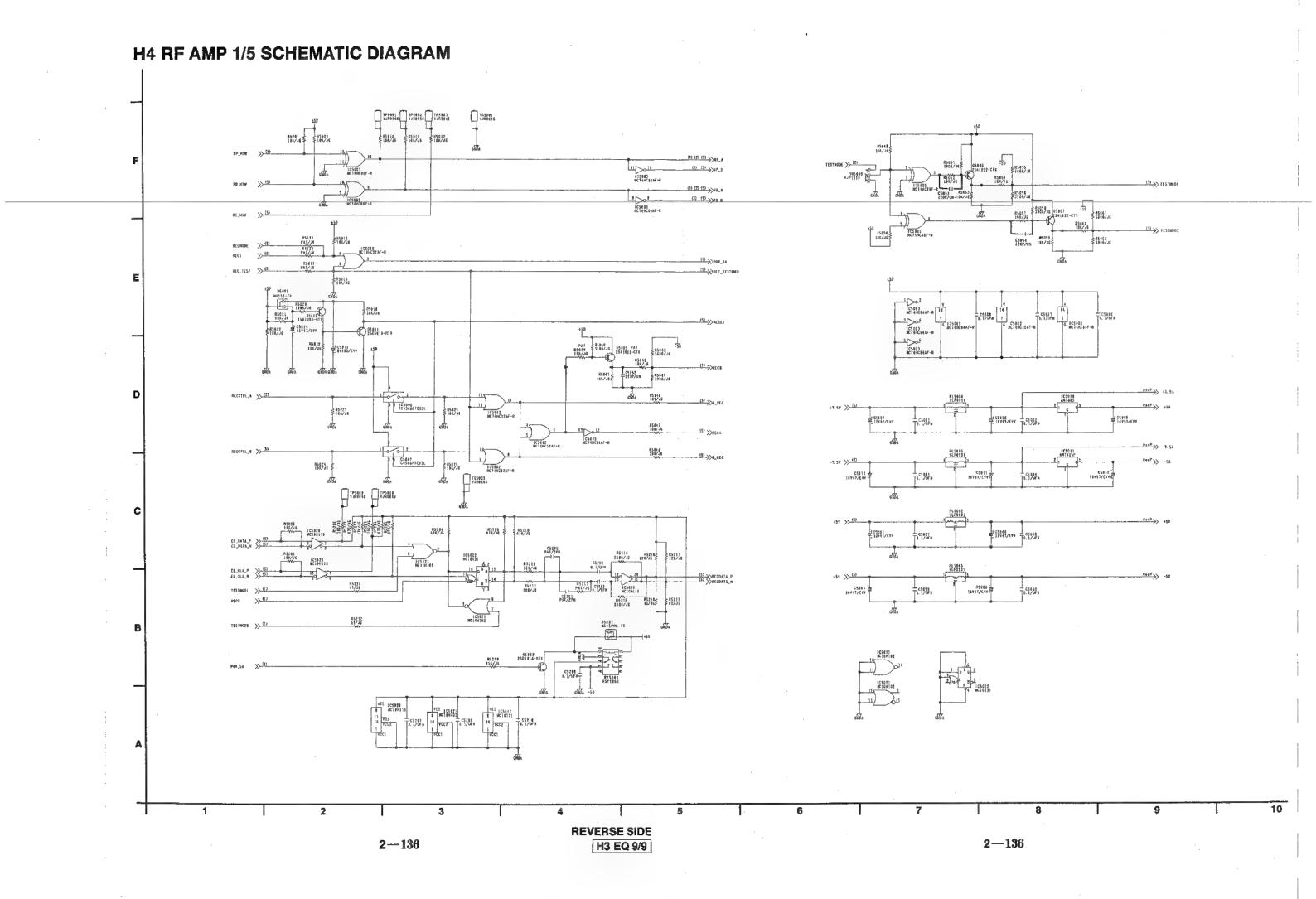


H3 EQ 7/9 SCHEMATIC DIAGRAM





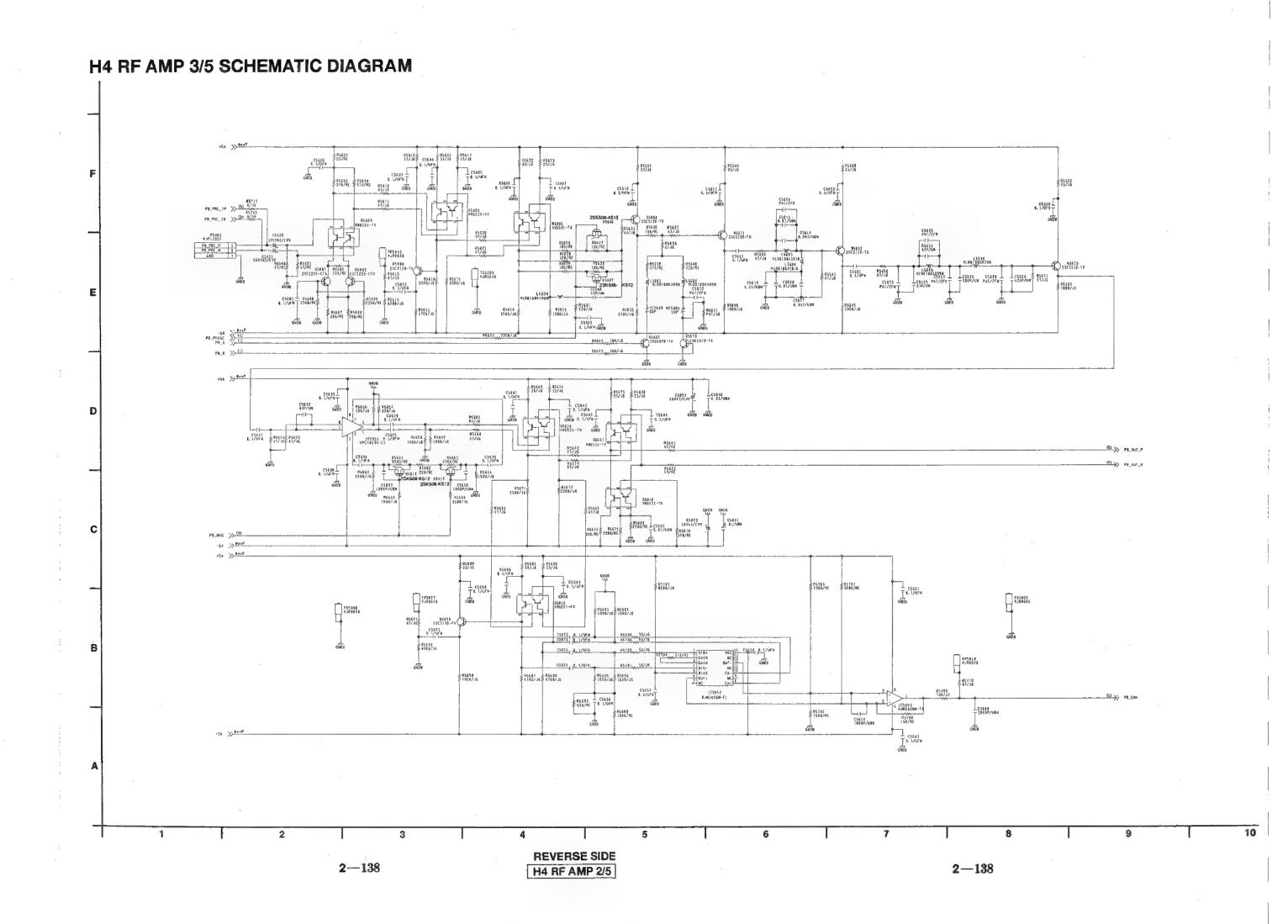
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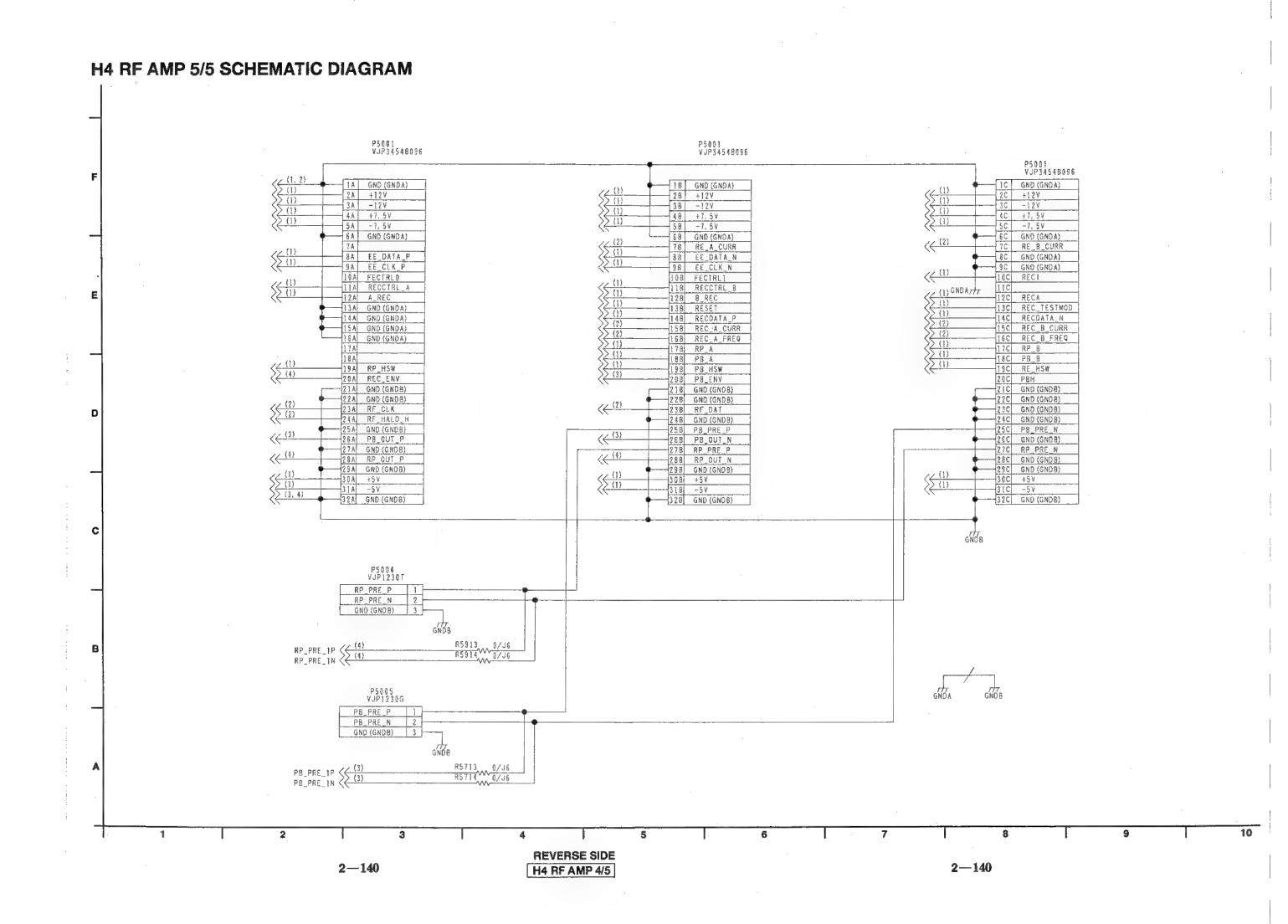
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2 - 137



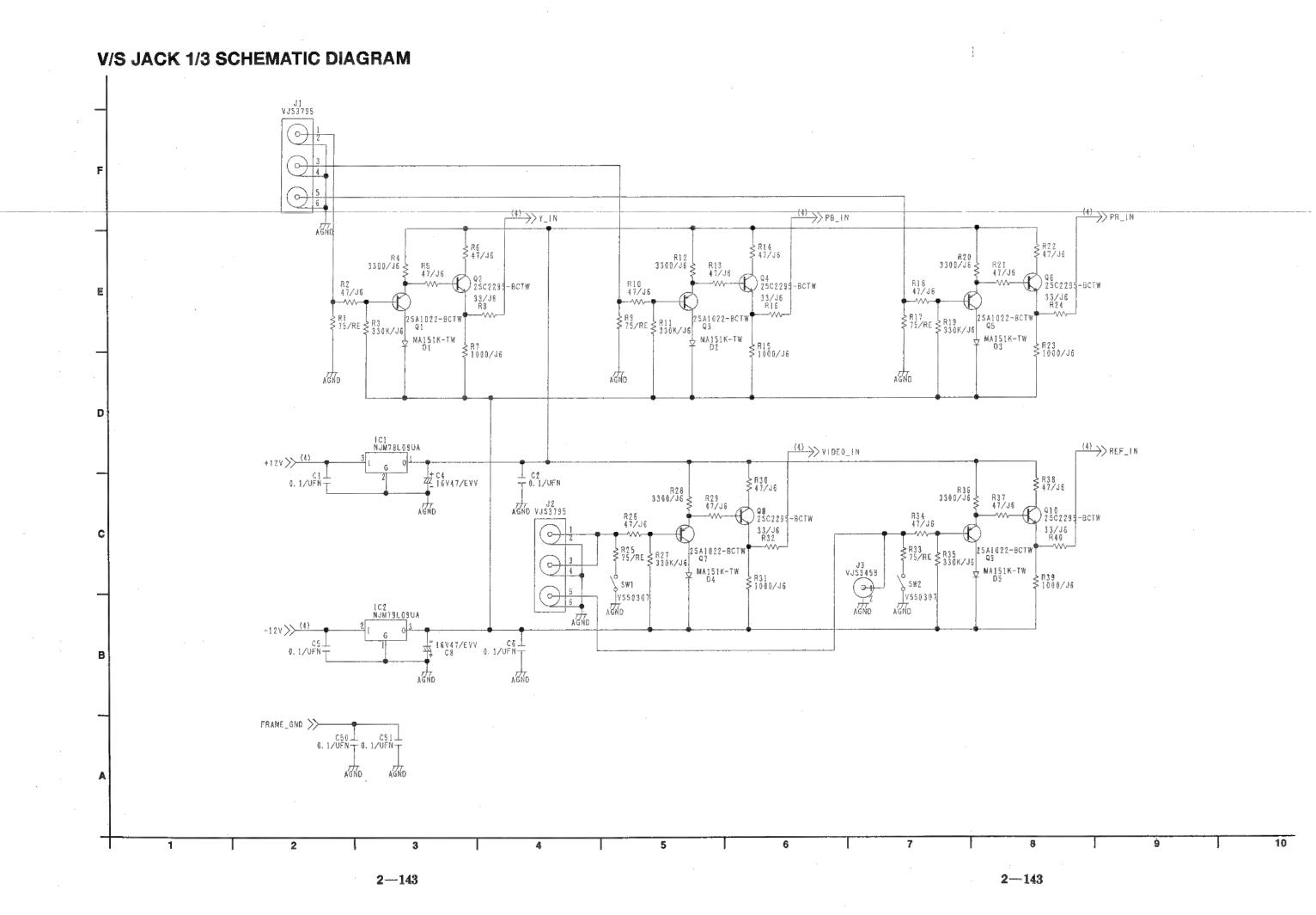
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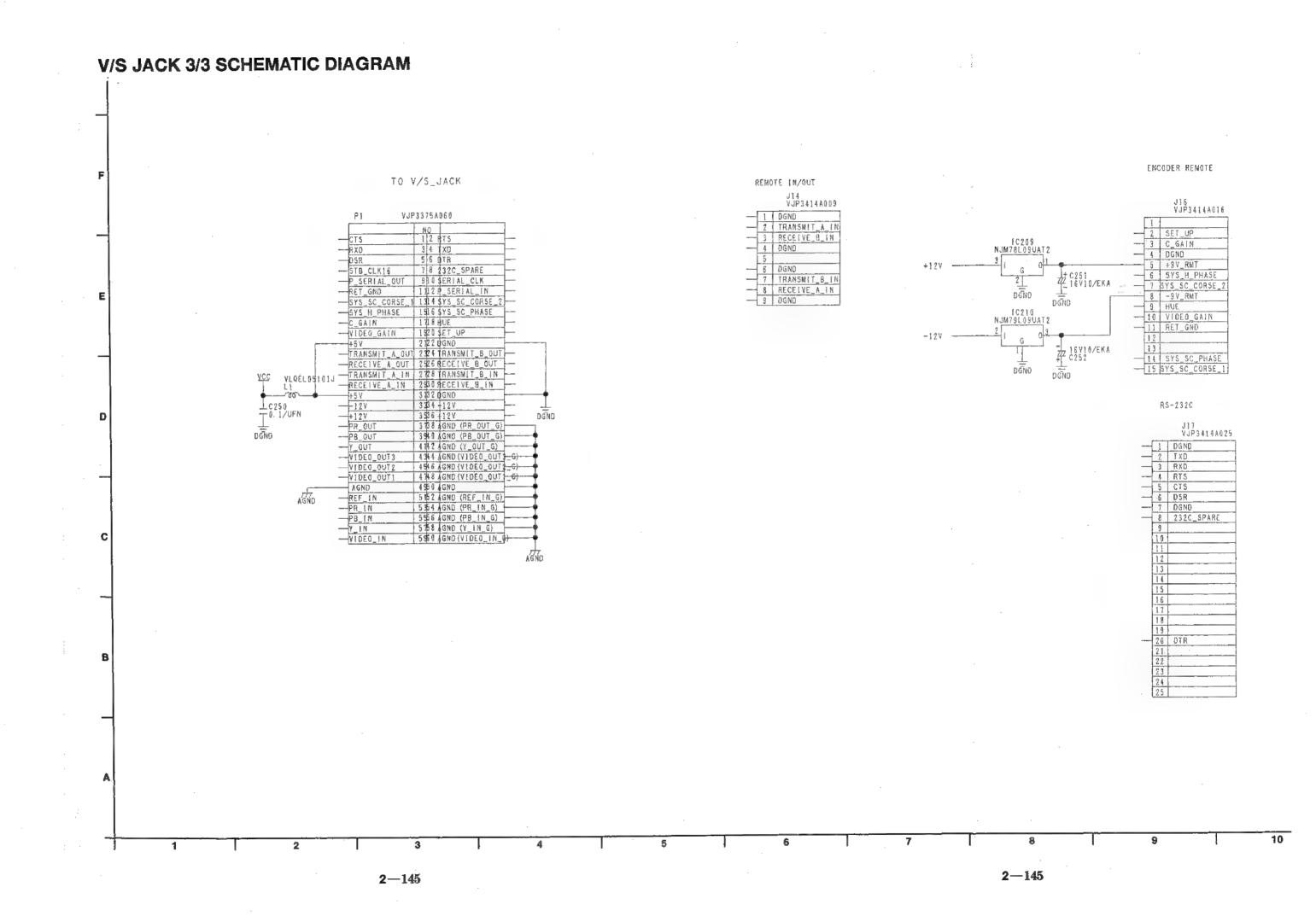
HEAD BUFFER 1/2 SCHEMATIC DIAGRAM PB_A_N \$\frac{111}{10} \csa41 \frac{4.1/4FY}{1.1/4FY} #5115 B/J6 131 GP_PRE_2P #5615 W 1/36 (1K) PB_PRE_2P 85261 3180/J6\$ 95268 F5285 J REC_A_CUMR (2) 05181 1541421-078 #5610 # 0/J6 (1) P8_PRE_1P P8_PRE_1N C5003 | C9650 CGSUES MCT (MCQ EAF—A 1050P3 NCT4HCE EAF-R ICSB01 BCT(HICTEAF 2 - 141

2 - 141

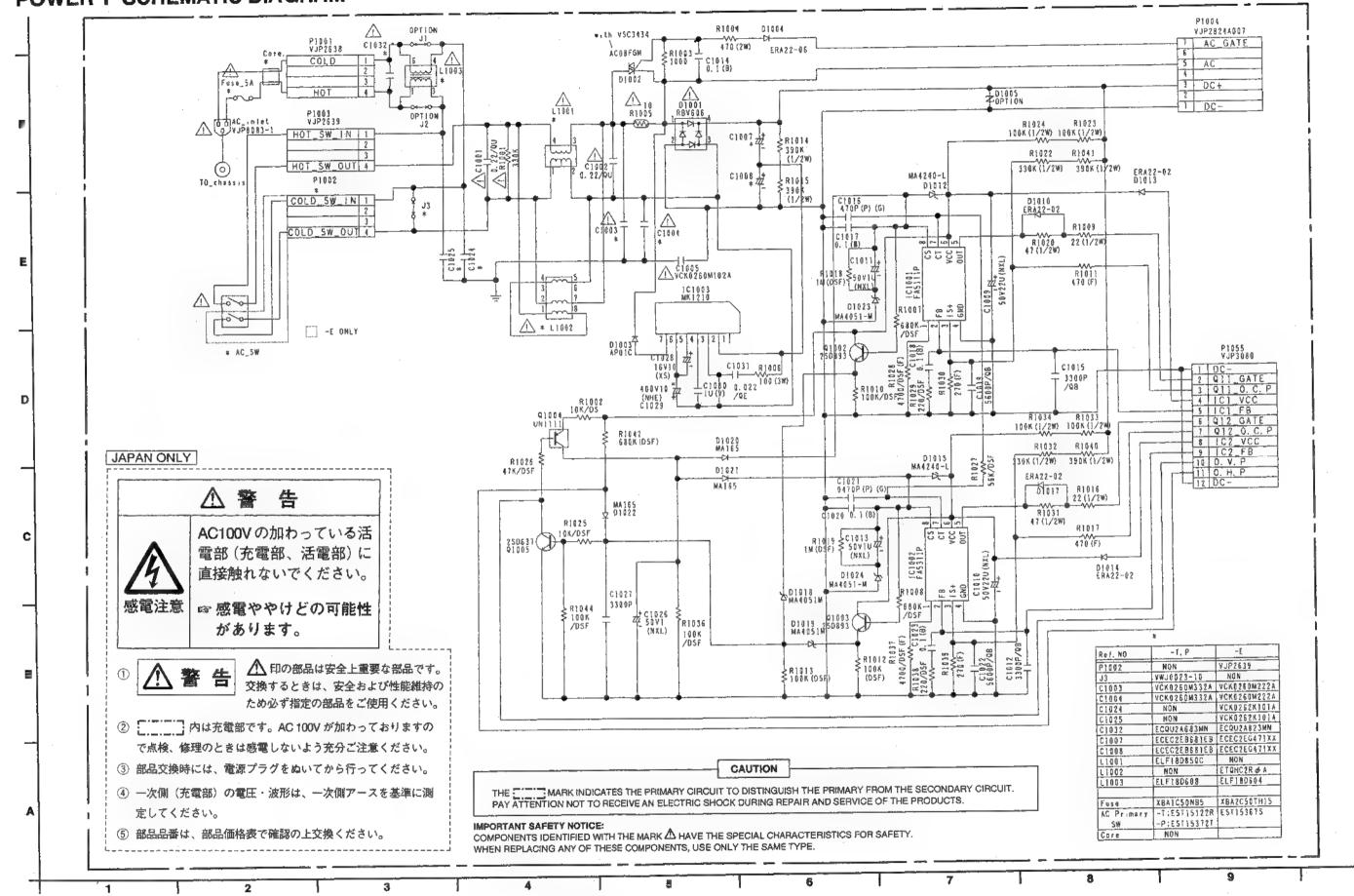
HEAD BUFFER 2/2 SCHEMATIC DIAGRAM C5155 1P/01 CS 141 Q. S. GNOB CS461 85183 REVERSE SIDE 2-142 2-142 HEAD BUFFER 1/2



V/S JACK 2/3 SCHEMATIC DIAGRAM J4 VJS3795 IC3 NJM78L09UA +127>> (4) R42 C33 Z20/J6 Z20/J6 R50 C34 R53 220/J6 世 C29 16 V47/EVV LC12 TO. 1/UFN ⊥c9 ⊤0.1/UFN 1 C15 T0. 1/UFN Q18 L__ 2SD601-9RTW 912 250801-9RTW Q15 25D601-QRTW R43 47/J6 R51 47/J6 AGND Z58709-QRTW 25B719-QRTW 258709-QRTW $Y_0UT > (4)$ R47 75/RE R63 75/RE R55 75/RE ₹841 \$2200/J6 ₹R49 \$2200/J6 ₹857 \$2200/J6 Q13 2SD601-QRTW 916 250601-QRTW 019 250601-QRTW R44 \$ R60 ≥ R52 ≥ R46 \$ 220/J6 R54 ₹ 220/J6 R62 3 220/J6 IC4 NJM79L<u>0</u>9UA AGND. AGNO AGND -12V>>(4) 10.1/UFN 1016 T0.1/UFN 16 V 47/EVV 10.17UFN J5 VJ537**95** PR_OUT >> (4) P8_OUT >> (4) AGND AGND D 1C5 NJM78L09UA R82 2200/J6 100P/UN \$ 220/J6 R66 100P/UN 2220/J6 R74 2200/J6 100P/UN 2220/J6 # C28 # 16V47/EVV 10.1/UFN 921 25D601-9RTW 924 25D601-9RTW 927 2SD601-9RTW R67 47/J6 R15 47/J6 R83 47/J6 AGND AGND AGND R72 2200/J6 R80 2200/J6 R88 2200/J6 258703-QRTW 258709-QRTW 258709-ORTW R71 75/RE R79 75/RE R87 75/RE ₹865 \$2200/J6 ₹ R73 \$ 2200/J6 ₹81 \$2200/J6 Q28 2SD601-QRTW 922 250601-9RTW 925 250601-0RTW R68 ₹ R76 ≱ R84 2200/J6 ≸ R70 } 220/J6 } R86 3 AGND AGND AGND -12V>>>-(4) 1019 10.1/UFN 1025 1/UFN 10.22 10.1/UFN 16 V47/EV C31 VIDEO_0UT2>> (4) V10E0_0UT3 >>-(4) AGND VIDEO_OUT1 >> (4) REVERSE SIDE 2 - 1442-144 V/S JACK 1/3

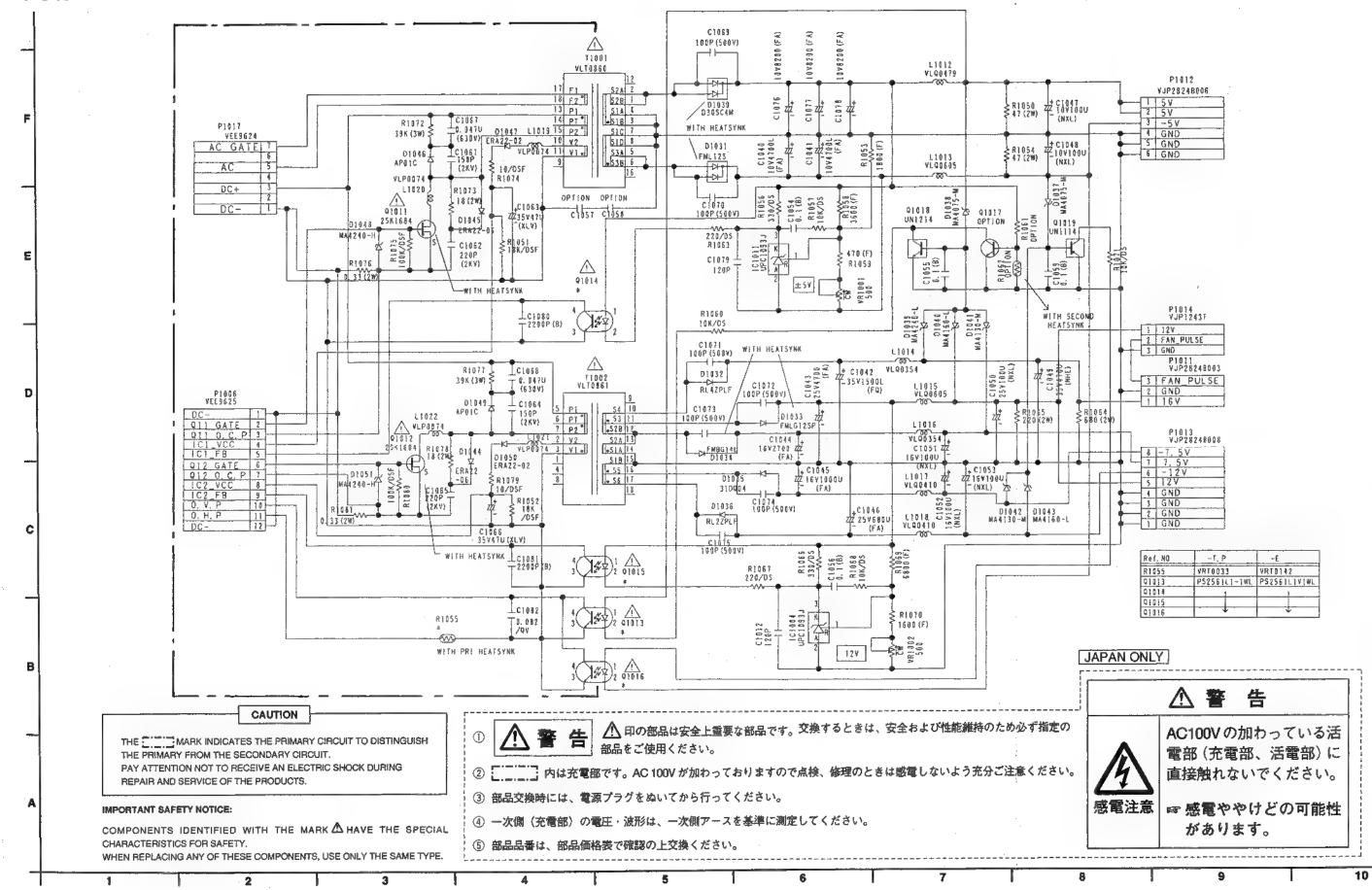


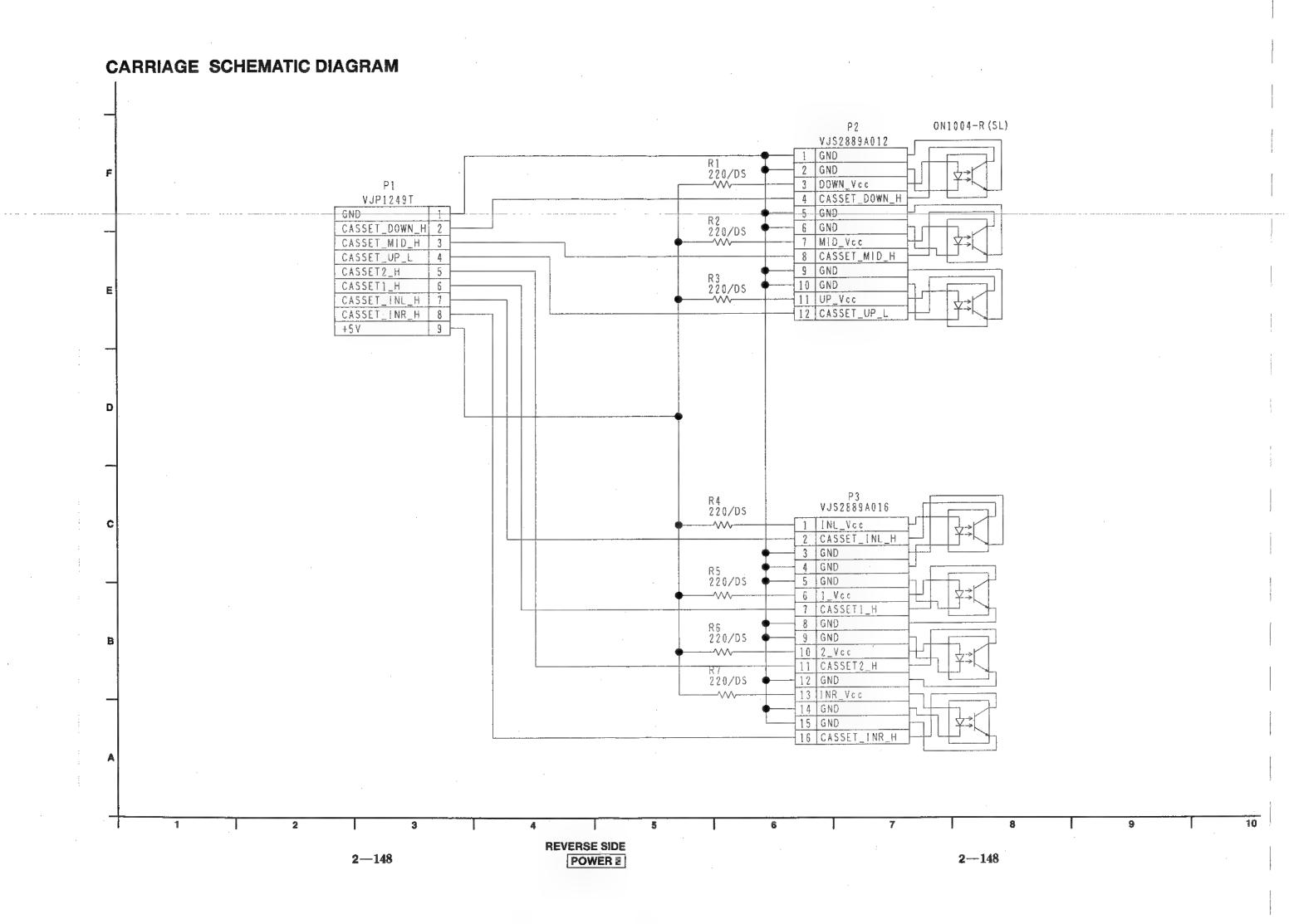
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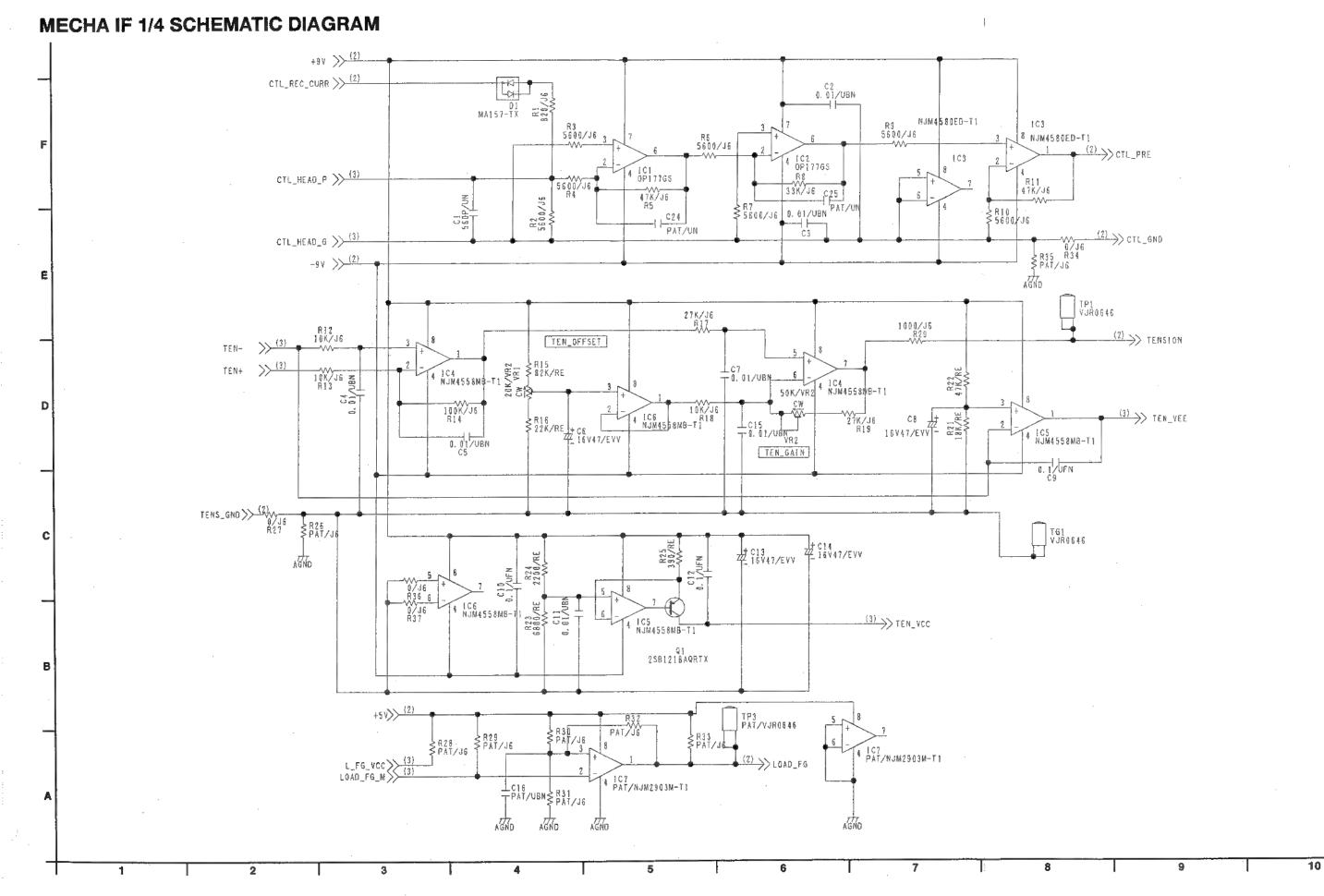


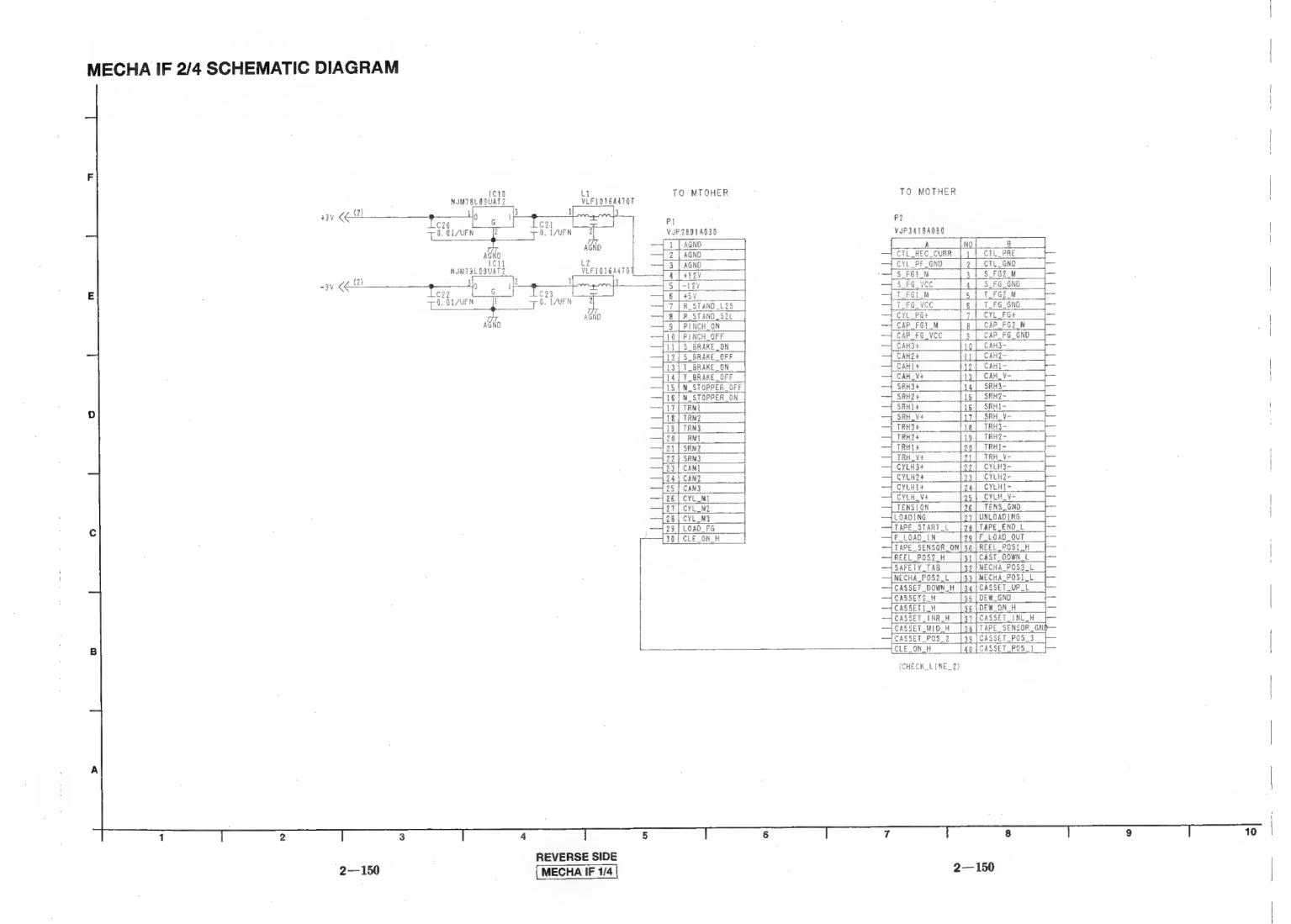
REVERSE SIDE V/S JACK 3/3

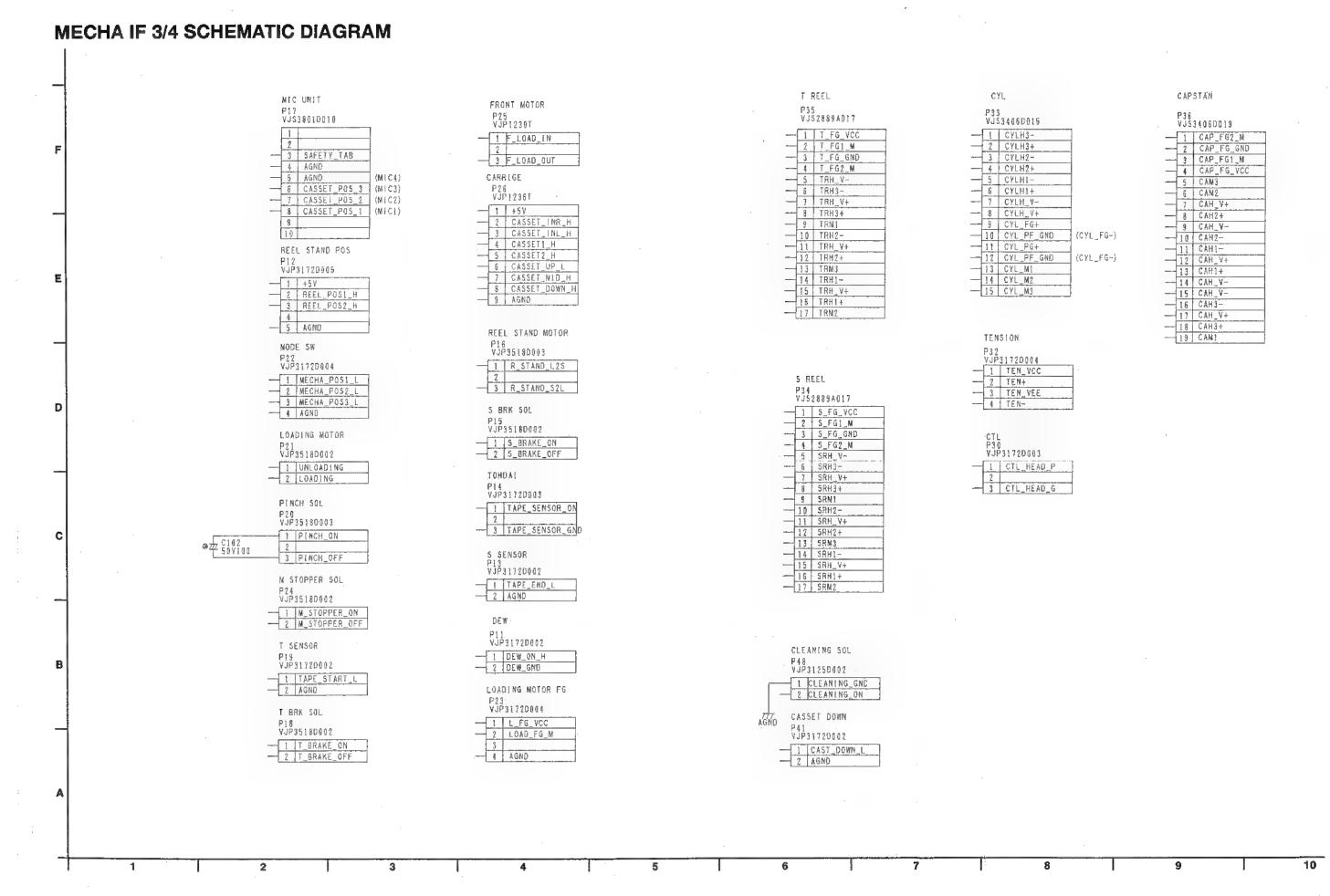
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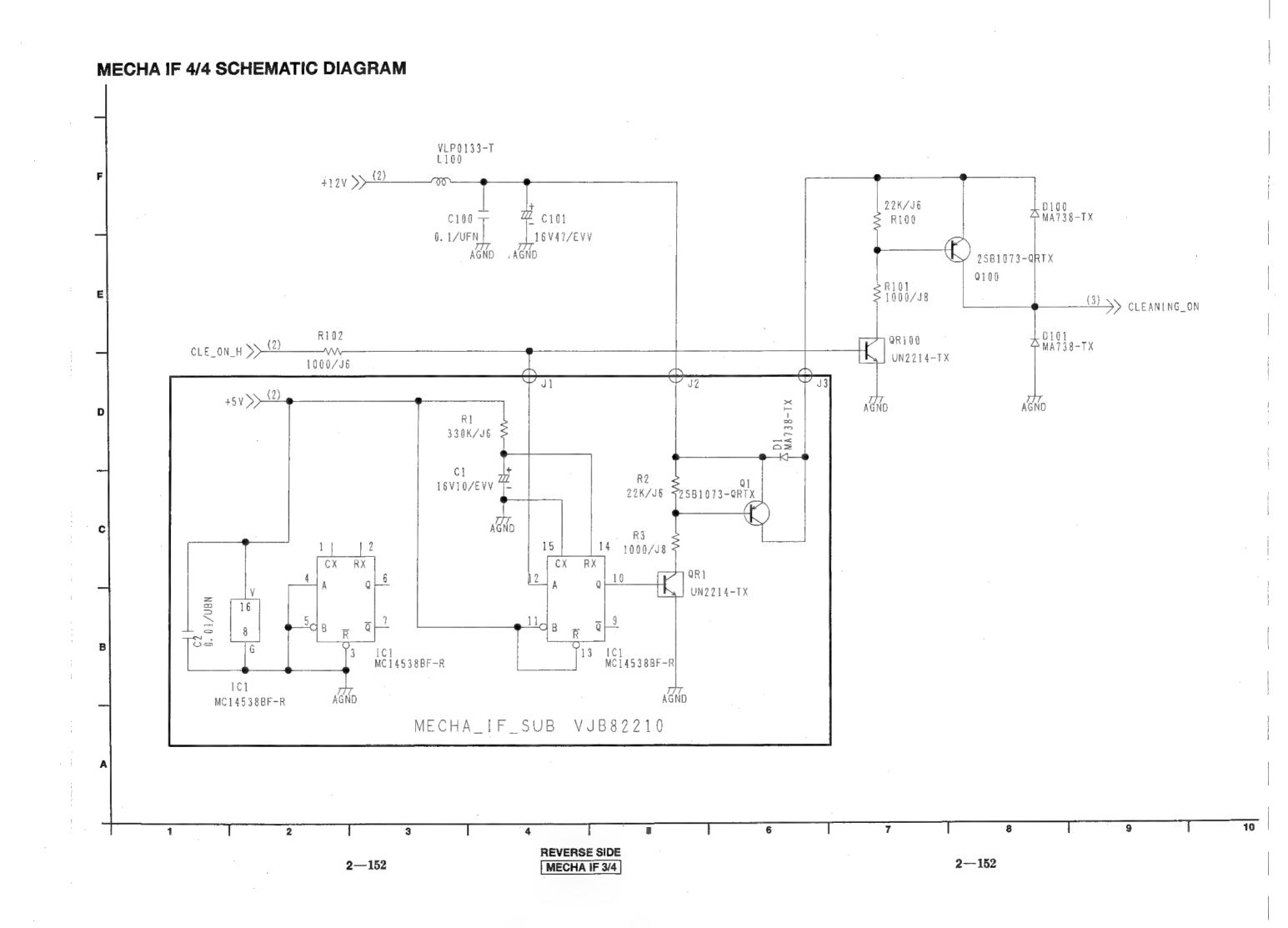


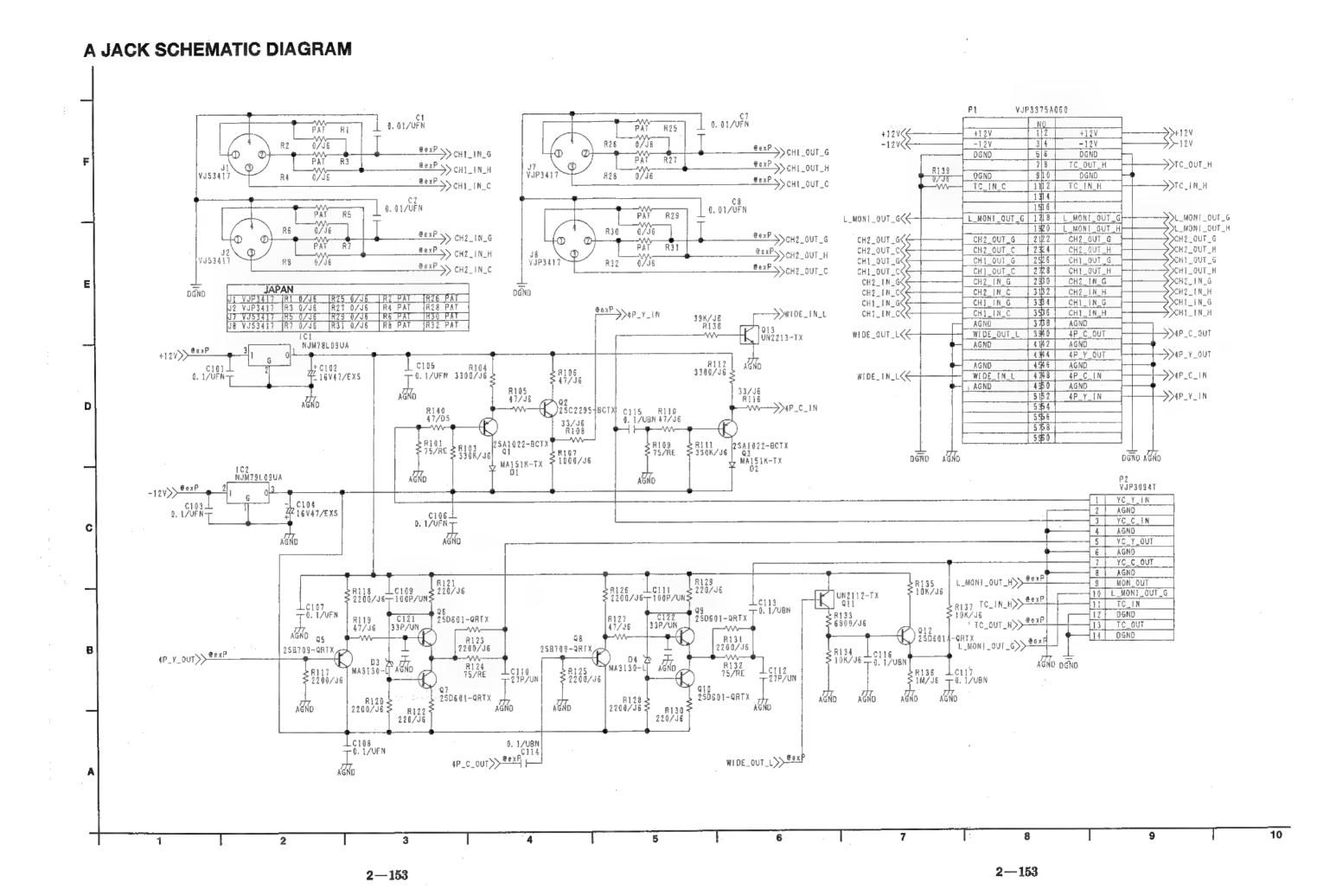


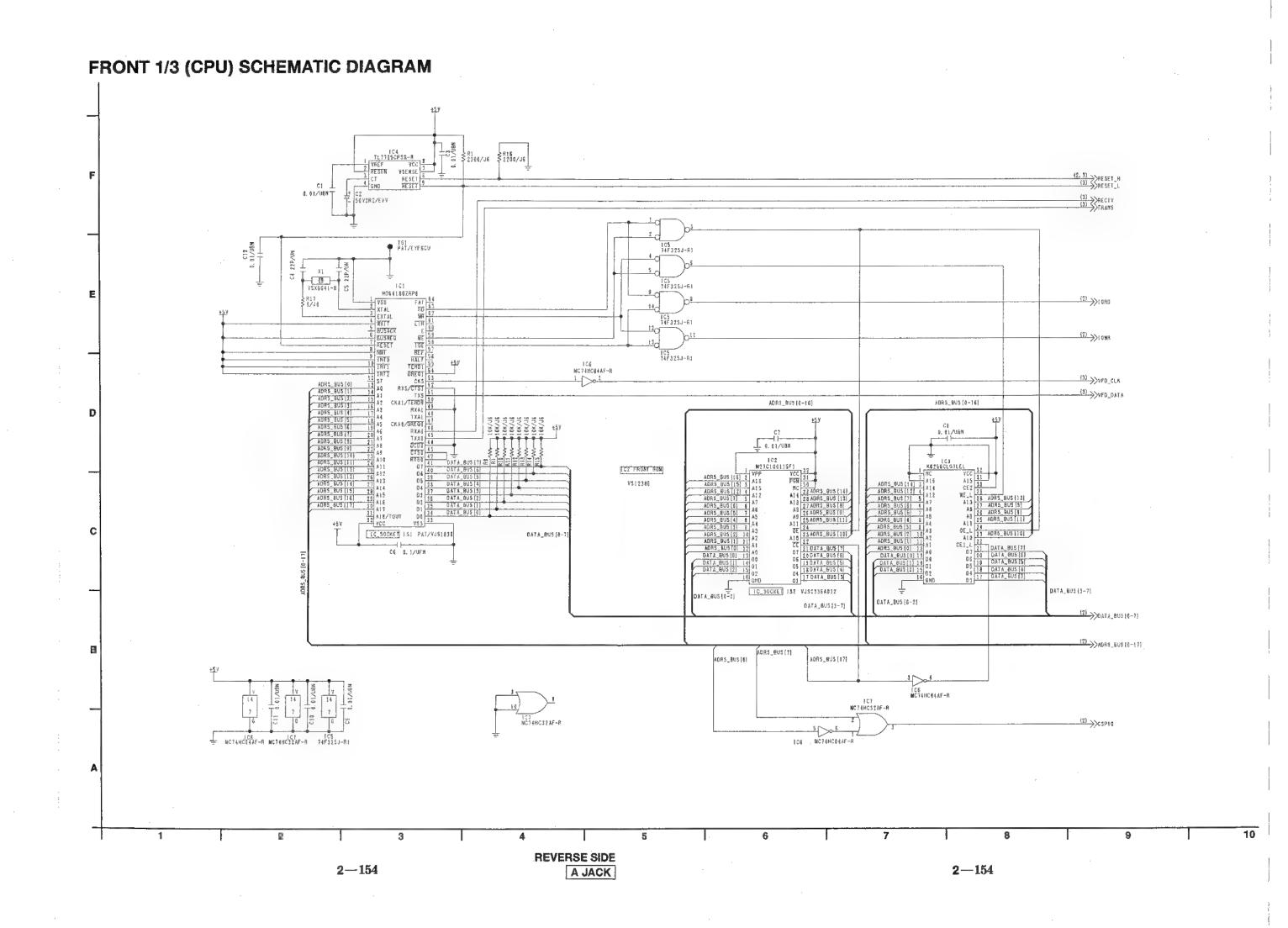


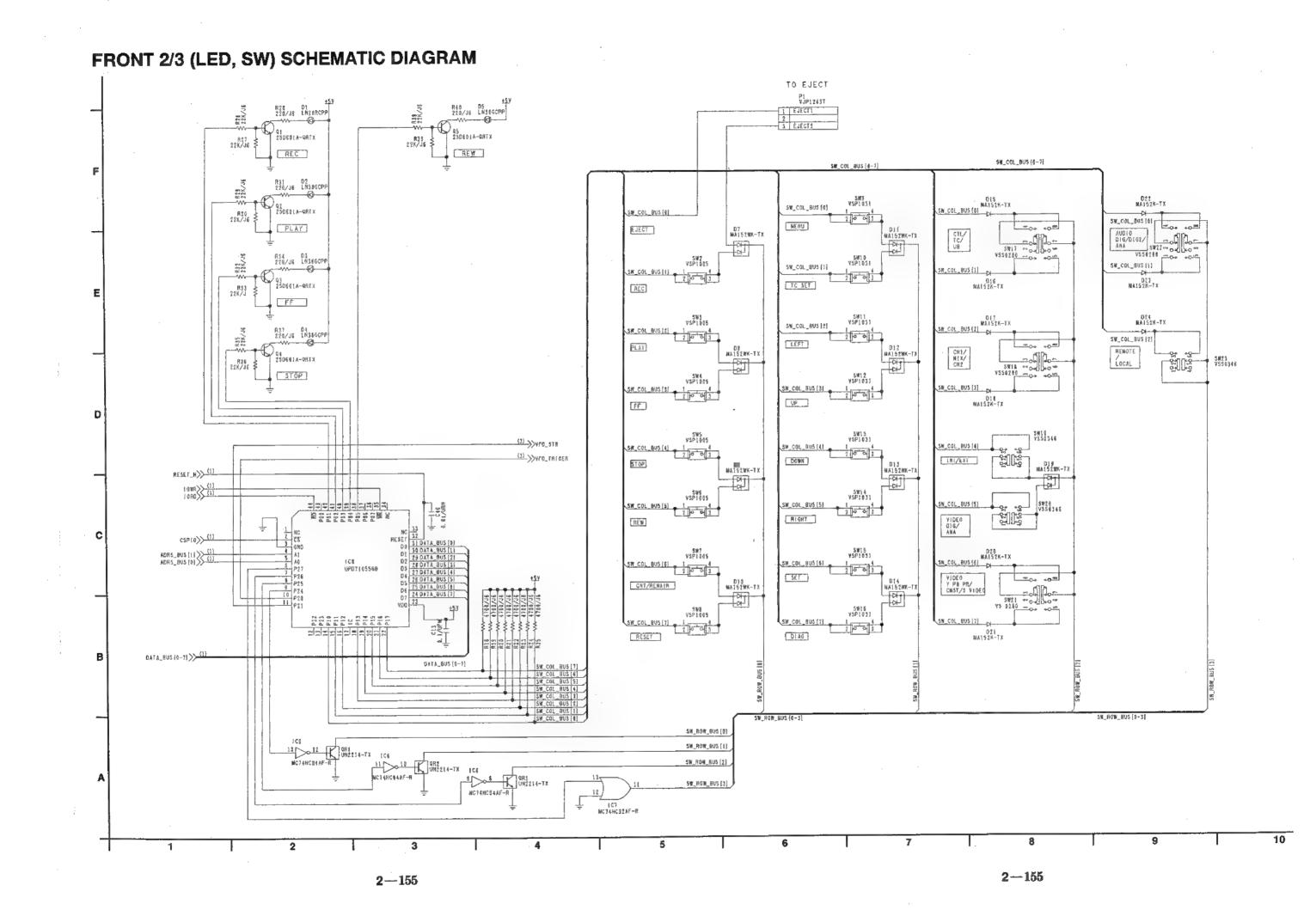


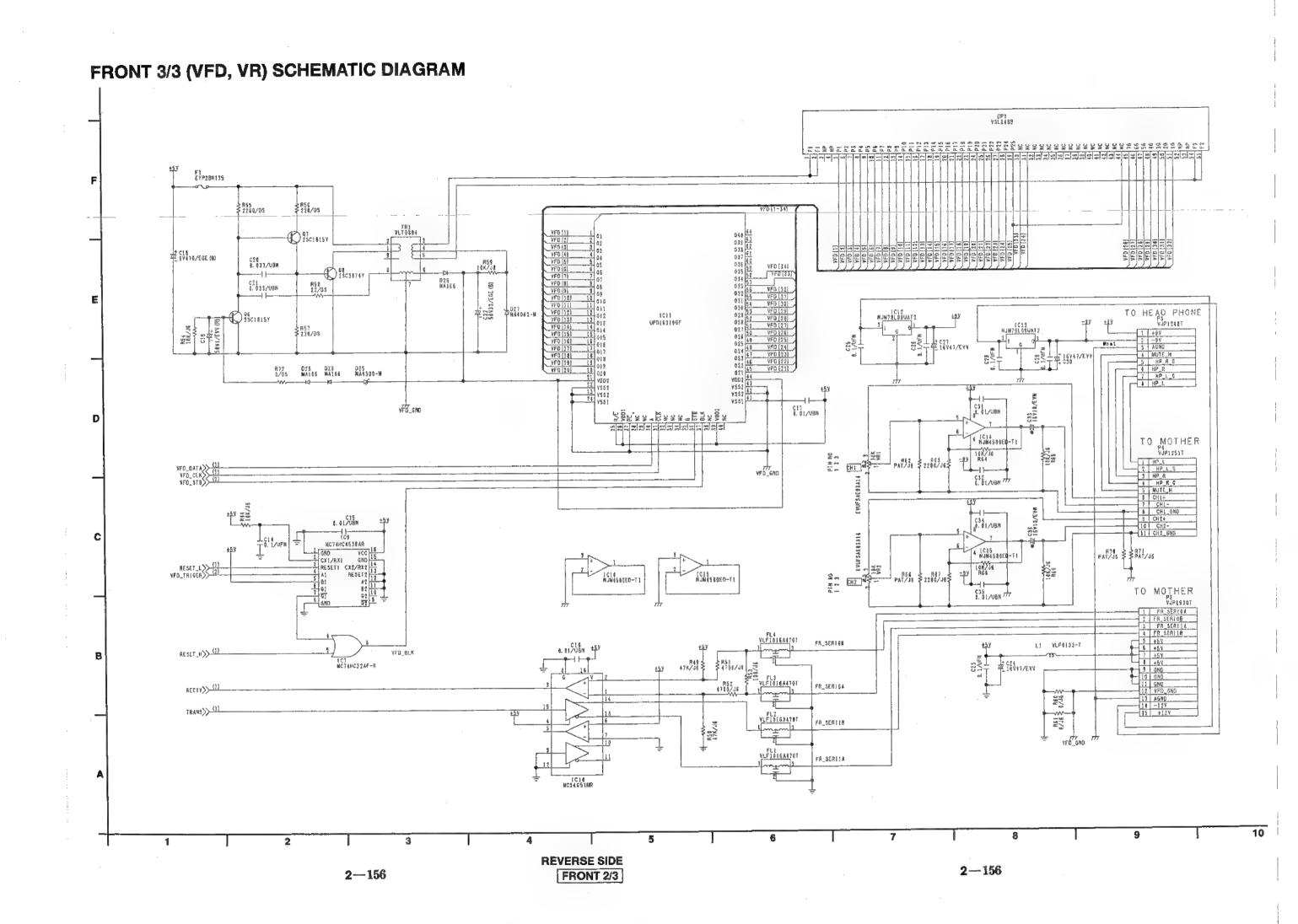


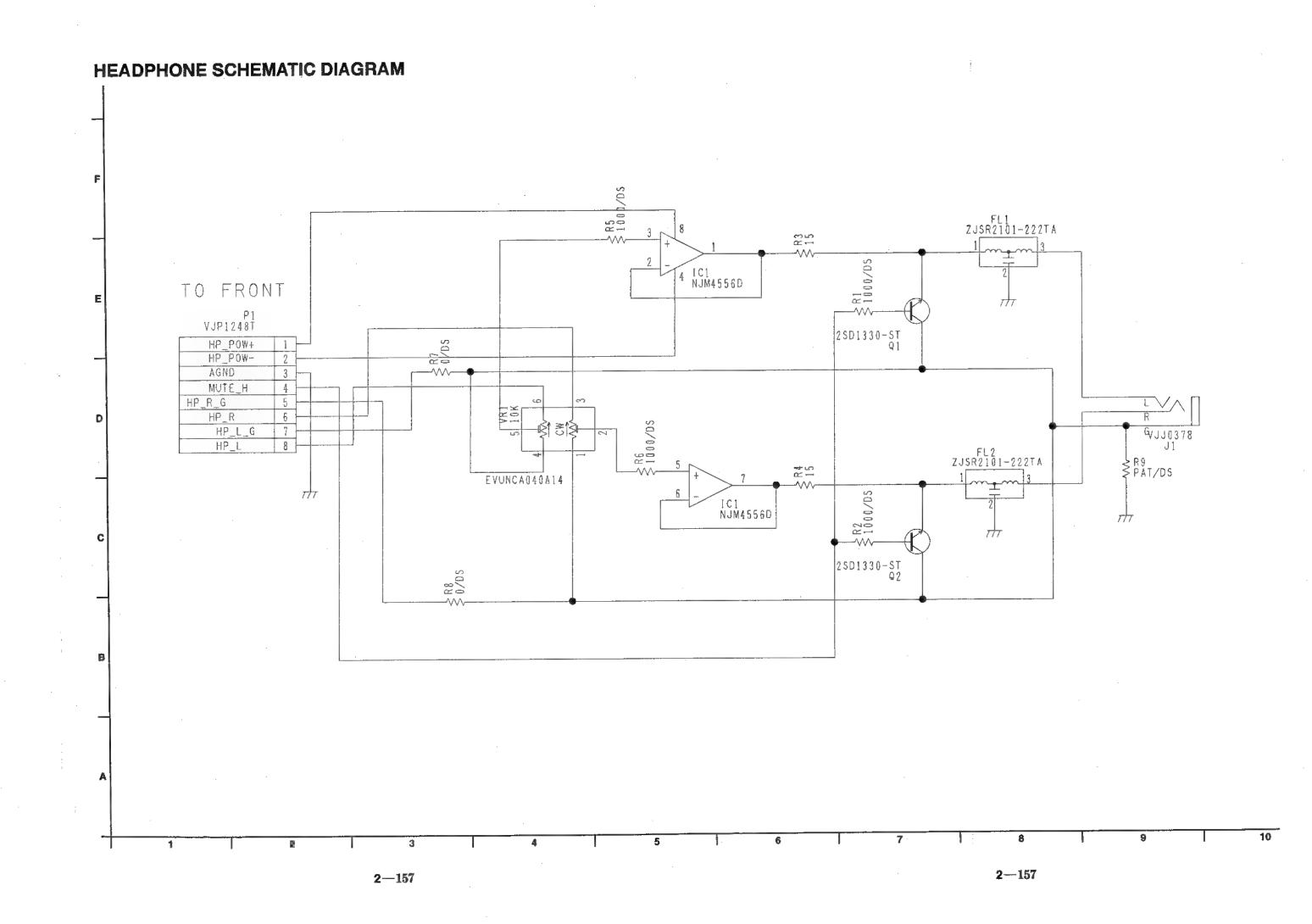


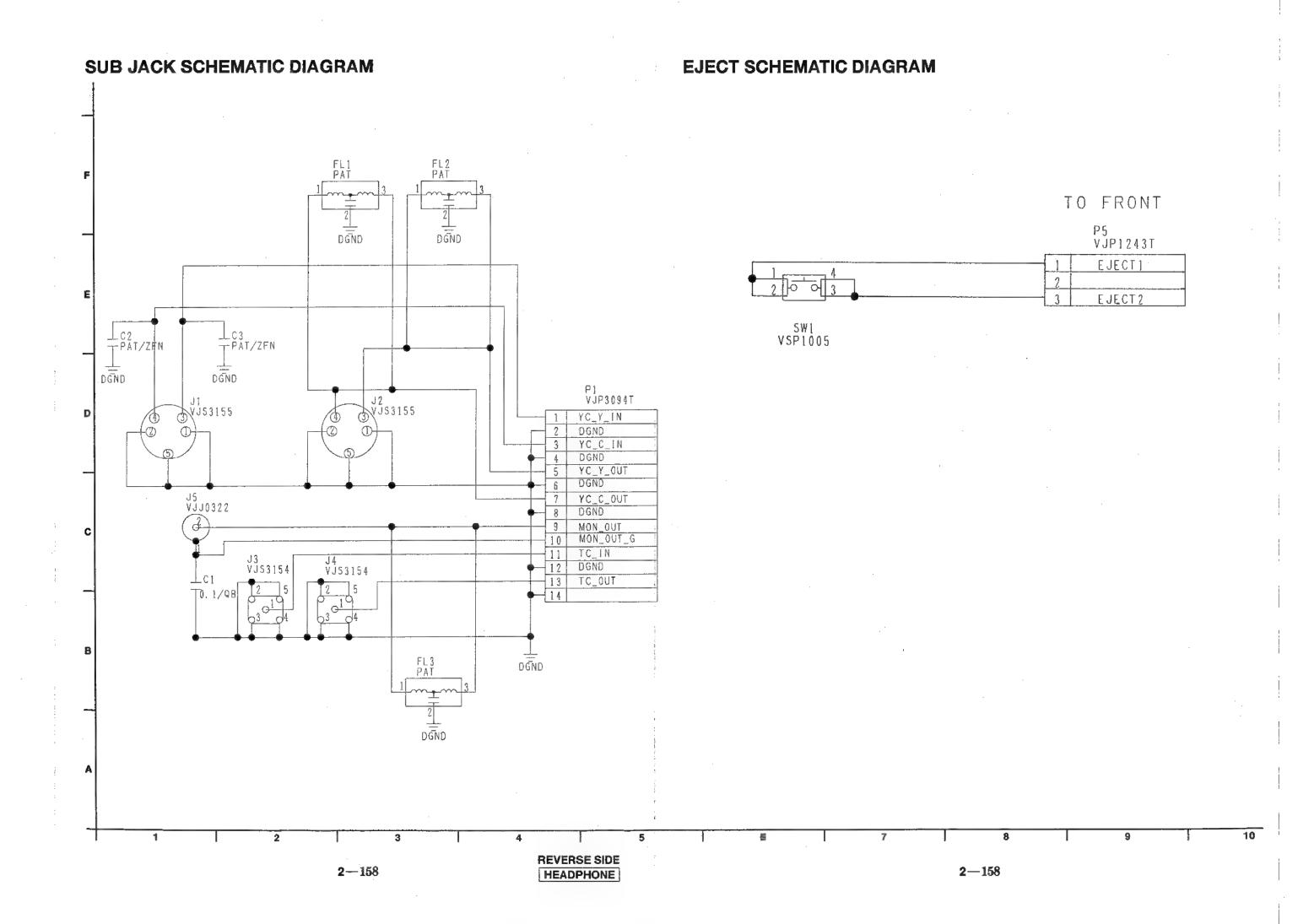










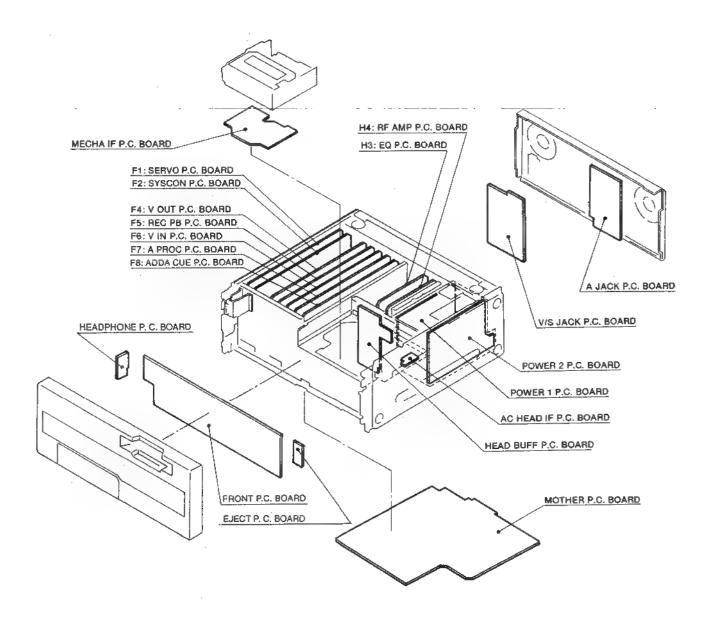


CIRCUIT BOARDS

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CIRCUIT BOARD LOCATION

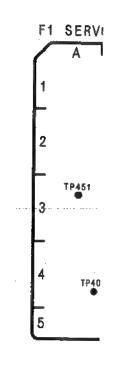


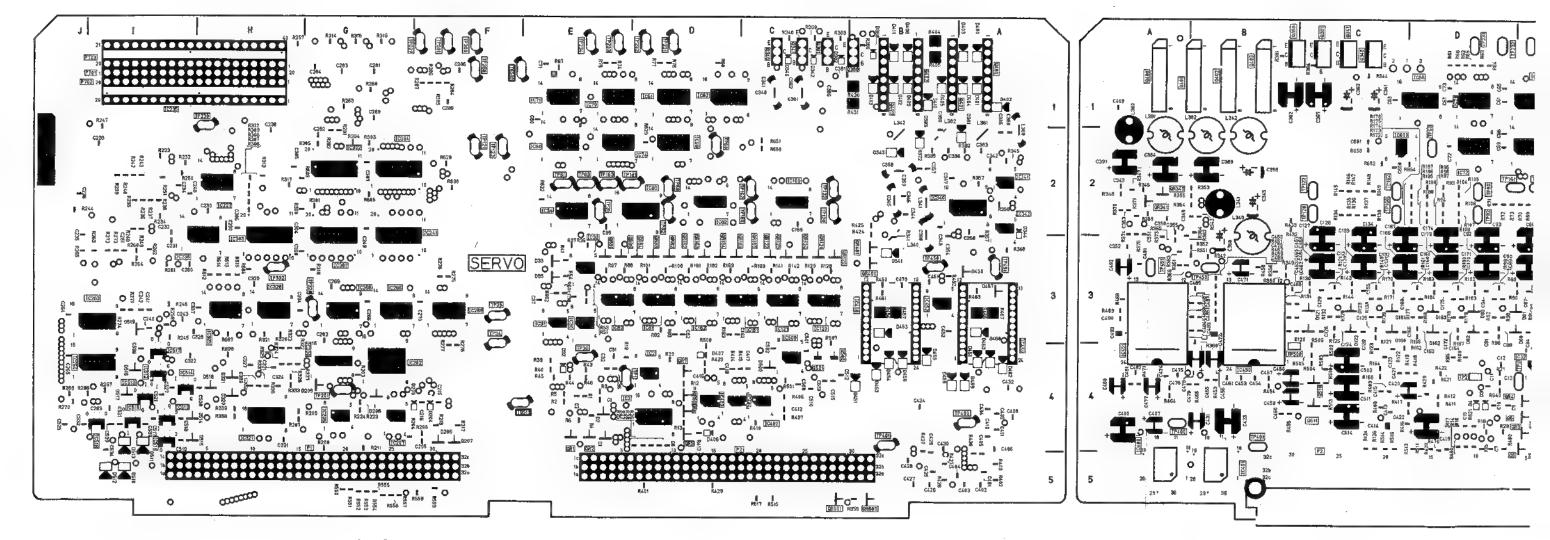
			FI S	SERVO				
Transistors			IC64	E-1	©	IC401	B-5	0
	= 4	_	1066	E-1	©	IC402	C-4	Ð
Q1	E-4	©	FC66	E-2	©	IC403	D-4	(Ē)
Q2	E-4	©	1C67	D-1	©	IC404	C-4	(Ē)
Q3	E-4	©	1C68	0-1	©.	IC450	B-3	
Q4	E-4	©	1069	D-2	Ð	IC451	A-4	
Q5	E-4	0	1G70	E-1	©	IC452	B-3	Ð
Q6	D-4	@	IC71	E-1	(f)	IC500	C-3	Ø
Q340	C-1		1C72	D-2	©	IC510	1-4	(Ē)
Q341	C-1		IC73	D-1	©	IC511	1-4	Ø
Q380	8-1		1C74	D-2	Ø	IC512	1-4	Ē
Q381	C-1			E-3	(E)	IC513	1-4	0
Q400	A-1_		1C80			1	1-4	-6
Q401	A-1		1081	D-3	©	IC514	1	
Q402	B-1		IC82	D-2	Ð	IC515	1-4	Ð
Q403	B-1		IC83	D-2	(f)	IC516	1-4	Ð
Q500	C-4	©	IC120	C-3	(f)	IC517	1-4	(E)
Q501	C-4	©	IC121	C-3	€	IG600	C-2	0
1	C-4	. ©	∮C160	C-2	(E) ·	Test Points		
Q610			IC161	C-3	Ð			
Q511	C-4	©	IC162	D-3	(F)	TP1	E-4	
Transistor-Res	latore		tC200	G-4	©	TP2	D-4	0
anaustyr/nos	.21010		1G201	G-4	©	TP30	E-4	
QR1	E-4	②	1C202	G-4	©	TP31	E-2	
QR2	€-4	(F)	1C203	F-4	©	TP32	E-3	0
QR3	E-4	Ö	IC204	0.2	Ô	TP33	£-3	0
QR4	D-4	©	(G205	G-4	Ď	TP34	F-3	-
QR5	D-4	Ē	1C207	G-4	Ó	TP35	F-3	
QR6	D-4	Œ.				1	D-2	
QR7	0.4	Œ)	1C230	1/1	0	TP60	i	
QR8	D-4	Ø	1C231	1-3	0	TP80	D-2	
			1C235	1-1	_	TP81	E-2	
QR30	E-3	©	IC236	1-3	ø	TP82	D-2	
QR81	E-3	®	IC237	H-2	©	TP83	E-2	
QR82	D-3	®	1C236	1-2	Ø	TP120	C-2	
QR83	D-3	®	1C239	1-2	©	TP121	F-2	
QR84	D-3	©	IC240	H-2	©	TP122	C-2	
QR86	E-3	(F)	IC241	F-3	©	TP123	F-2	
QR120	C-3	(f)	∤C260	H-3	0	TP160	C-2	
QR121	Ç-3	Ø.	1C261	J-3	0	TP161	D-2	
QR122	C-3	Ø	3G262	1-3	©	TP162	C-2	
QR123	C-3	ø	IG263	1-3	©	TP163	E-2	
QR124	C-3	Ē	IC264	J-4	Õ	TP200	G-4	@
QR160	C-3	②	IC265	F-3	©	TP201	G-4	_
QR161	C-3	Ø	1C266	G-3	Ē	TP202	G-4	©
QR162	D-3	Œ)	1C266	G-3	0	TP230	H-1	-
QR163	D-3	Œ.	IC267	G-3	©	TP230	D-1	
QR164	D-3	Œ.		F-3		TP232	D-1	
QR340	A-2	©	IC269		Ø		E-1	
QR341	A-2	©	IC280	F-1	0	TP233	J	
			IC281	F-1	0	TP234	E-1	
QR400	B-2	©	IC282	G-2	©	TP280	F-1	
QR401	B-3	©	1C300	H-2	0	TP300	F-1	
QR600	B-5	©	4G301	G-3	ø	TP301	F-1	
QR601	C-5	©	1C302	G-2	⑥	TP302	H-3	
Integrated At-	to side		4C303	H-3	Ð	TP320	G-3	
Integrated Circ	Julia -		IC304	G-2	(TP321	F-1	
IC1	E-4	©	₹C305	G-2	0	TP400	A-4	
IC2	D-4	Ð	1G320	H-4	©	TP401	B-4	
IC3	E-4	(Ē)	IG321	H-4	(f)	TP450	B-3	
IC30	E-3	e e	1C322	H-3	©	TP451	A-3	
4 8458550	E-3	Ø	1C323	H-4	©	TP500	B-4	©
1031	E-4	©)C324	H-3	ø	TG510	F-1	-
IC31			iC325	H-3	©	TG511	F-4	
IC32		æ		1 11.0	~	1500/11	1 7	
IC32 IC33	E-3	€			æ			_
IC32 IC33 IC34	E-3 E-2	€	IC326	H-3	©	Connectors		
IC32 IC33 IC34 IC35	E-3 E-2 E-3	©	1C326 1C340	H-3 B-2	Ø	<u> </u>	C.5	_
IC32 IC33 IC34	E-3 E-2	€	IC326	H-3		Connectors P1 P2	G-5 C-5	

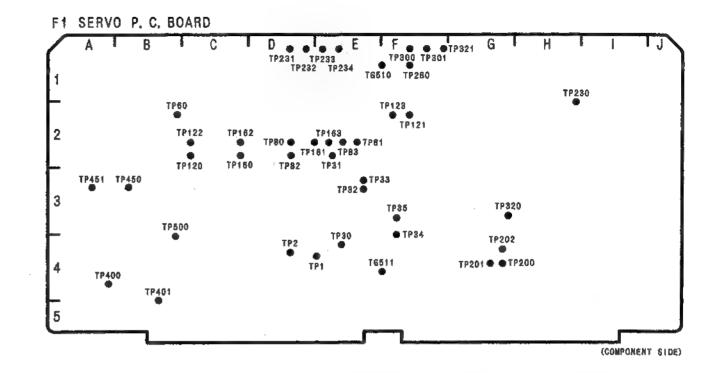
ADDRESS INFORMATION

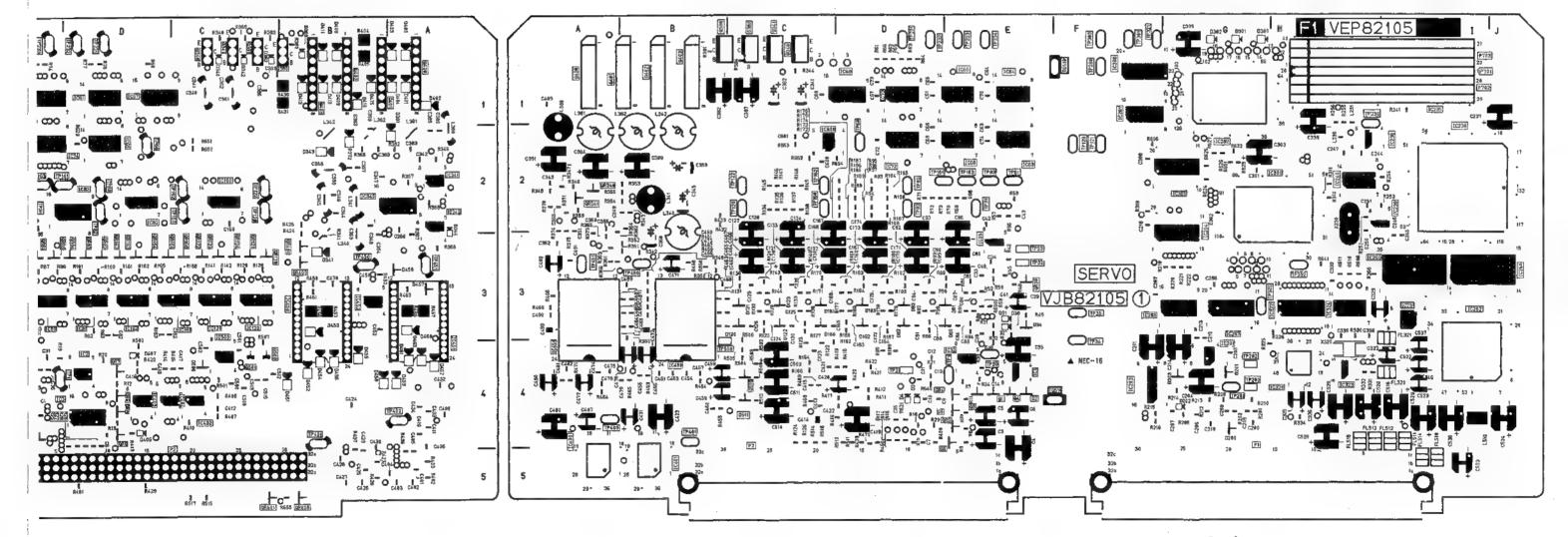
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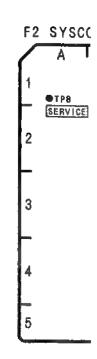






F2 SYSCON P.C. BOARD (VEP86146C: AJ-D650/D650P / VEP86146D: AJ-D640/D640P / VEP86146E: AJ-D650E / VEP86146F: AJ-D640E)

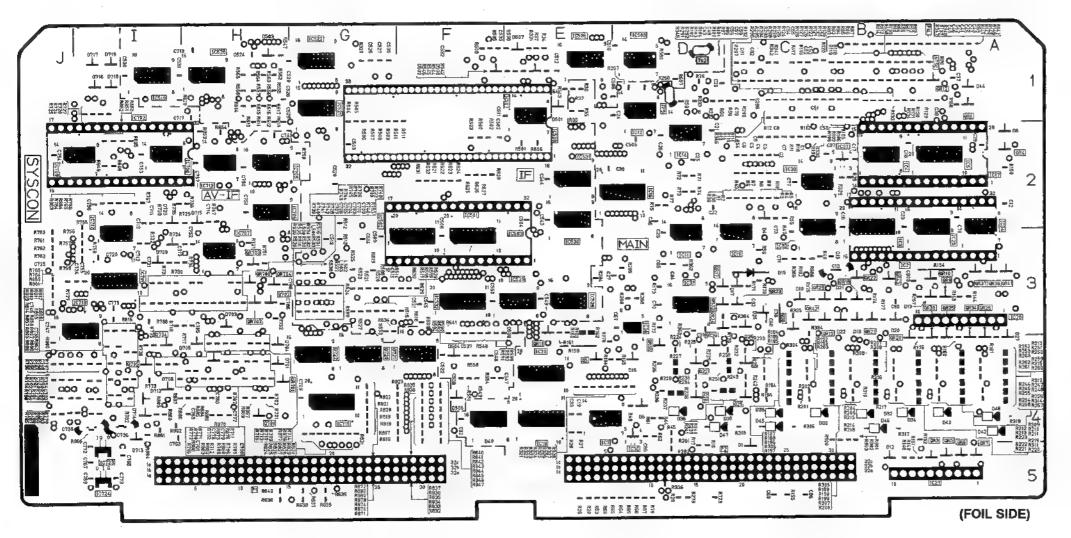
												F2 S1	SCON .													
Transistors			Q26	D-4	Ē	QR7	A-4	(f)	QFI32	B-4		IC7	E-4	(F)	IC35	C-1	©	IC531	E-2	Ð	IC724	1-5	Ð	TP702	J-5	0
C3	E-3	©	Q27	0-4	0	QR8 QR9	A-1 A-3	© ©	QR33 QR34	D-3 A-3	(E)	IC8 IC9	D-1 C-1	© ©	IC36 IC37	D-4 B-2	ø	IC532 IC701	F-3 H-3	(f) (c)	IC725 IC726	H-4 H-4	© ©	TG1 TG2	F-1 D-1	©
Q4	D-3	Č	Q28 Q29	C-4 C-4	© F	QR9 QR10	A-3 A-3	(f)	QR35	D-4	(Ē)	IC10	C-2	Ē	IC500	F-2	•	IC702	1-2		Test Points		-	TG701	H-4	©
Q6	E-4	(E)	Q30	A-4	Ö	QR11	E-4	(Ē)	QR36	D-3	Ē	IC11	D-3	(Ē)	IC501	F-2	(F)	IC703	1-1	0				Adjustment		\neg
Q6	C-4	0	Q31	A-4	©	QR12	A-1	Ð	QR37	A-3	Ð	IC12	D-2	(Ē)	IC502	G-2	Ð	IC704	H-2	©	TP†	8-2	©			
· Q7	C-4 B-4	0	Q32	A-4	©	Q#13	C-1	©	QA38	A-3	Œ	IC13 IC14	D-2	(C) (F)	IC503 IC504	F-3 E-2	©	IC705 IC706	G-2 G-2	(Ē)	TP2 TP3	E-5	© ©	VC1	D-3	©
09	B-4 C-4	© I	Q33 Q34	A-4	0	QR14 QR15	C-1	© ©	QR39 QR40	A-4 A-4	© ©	IC14 IC15	D-2 E-2	0	10505	D-1	0	IC707	H-3	(E)	TP4	E-4	6	Switch	٠.	i
Q10	C-5	Ö	Q34 Q35	B-4 A-4	0	QR16	B-1	©	QR41	A-3	Ē	IC16	D-2	ě	IC505	E-1	Ē	IC708	H-2	ě	TP5	B-2	Č	SW501	G-1	1
Q11	C-4	©	Q36	A-4	Č I	QR17	D-3	Ē	QR42	C-3	Ď	IC17	A-2		IC507	Ę-1	℗	IC709	J-2	(Ē)	TP6	B-2	0	Connectors		\neg
Q12	B-4	©	Q37	A-4	©	QR18	B-3	Ð	QR43	B-3	Ð	IC19	E-4	©	€C508	D-1	Ø	IC710	1-2	©	TP7	B-2	©			
Q13	C-4	©	Q38	C-3	(f)	QR19	C-3	Ø	QR70†	G-4	(E)	IÇ20	E-4	©	(C509	G-2	0	IC711	1-2	Ð	TP8 TP9	C-1	0	P1	Q-5 D-5	1
Q14 Q15	B-4 B-4	0	Q39	D-6	©	QR20	B-4	®	QR702	H-3	9	IC23 IC24	E-4 D-2	©	\$C510 IC514	G-3 G-3	٩	IC712 IC713	1-3 H-2	(f) (f)	TP10	D-1 B-1	©	12	D-3	}
Q15	B-4	ő	Q701	H-4	®	QR21 QR22	8-3 B-3	(P) (P)	QR703 QR704	H-3 H-3	(B)	IC25	A-3	0	IC514	G-4	©	IG714	G-4	ő	TP11	8-2	Č]
Q17	B-4	ğΙ	Q702 Q703	H-3 H-3	(E)	QR23	C-3	Ø	QR705	H-3	(F)	IC26	B-1	ě.	IC516	G-4	Ö	IC715	G-4	ě	TP12	B-2	Č	i		
Q18	B-4	Õ	Q704	H-3	Ē	QR24	D-3	ø			<u> </u>	IC27	B-5		IC517	F-1	©	IC716	F-4	©	TP500	F-2	0]		1
Q19	B-4	©	Q706	H-4	Ē	QR25	A-3	©	Integrated Circ	CUITS		IC28	A-3		JC523	F-1	©	IC717	G-4	©	TP501	F-2	©	į		1
Q20	B-4	©	Transistor-Rea	rietore		QR26	A-3	Ð	IC1	C-1	0	IC29	C-1	©]	IC524	E-1	0	IG718	1-3	(f)	TP502	G-2	0	{		
Q21	8-4	©				QR27	C-4	0	102	A-2		IC30	D-3	®	IC525	G-1	©	IC719 IC720	J-4	©	TP503 TP504	G-2 F-2	© ©			Ì
Q22 Q23	. D-4 D-4	Ø	QR3	A-4	©	QR28 QR29	C-4	©	tC3 IC4	B-3 B-2	e e	IC31 IC32	D-3 C-3	(P)	IC528	F-3 E-3	© ©	IC720	J-4 I-3	0	TP505	F-1	0]		i
Q24	C-4	ŏ١	QR4 QR5	A-2 A-4	® ℜ	QR29 QR30	C-3 B-3	©	IC5	8-2 A-2	Ē	1C33	C-2	B	IC529	E-3	Ē	IC722	1-3	©	TP506	F-1	Ö	Ì		i
Q25	C-4	œ	QR6	A-4	ø	QR31	B-4	© -	IC8	B-3	©_	1C34	A-2	©	IC530	E-3	Ď	IC723	1-5	Ď	TP701	J-5	Ö	<u> </u>		

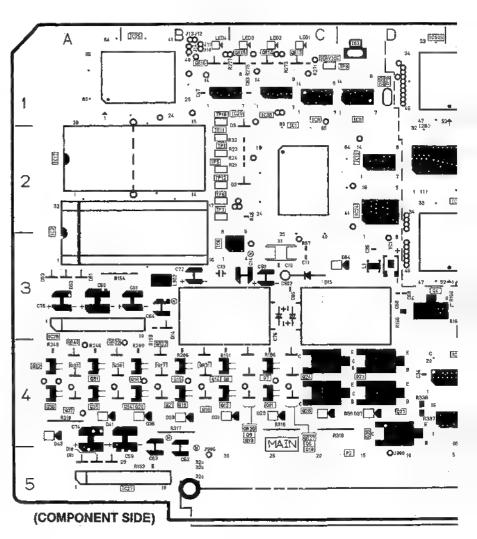


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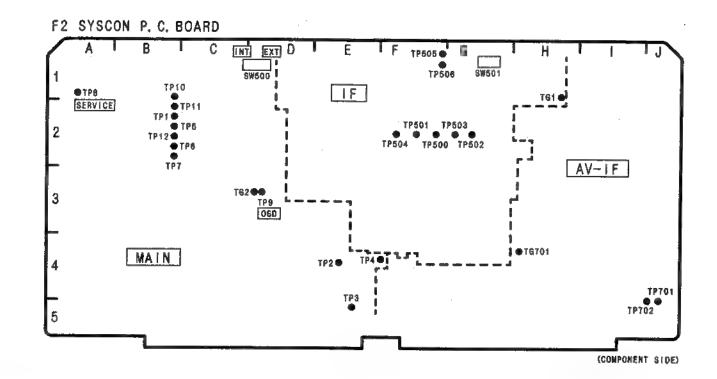
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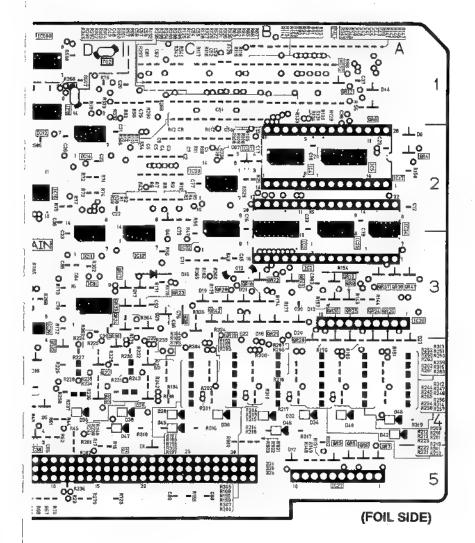


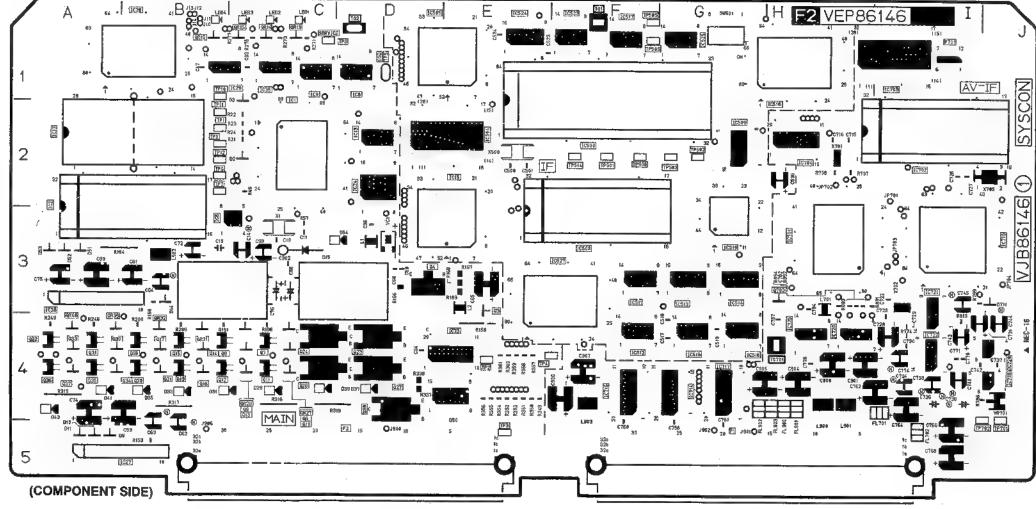
6146D: AJ-D640/D640P / VEP86146E: AJ-D650E / VEP86146F: AJ-D640E)

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		F2 \$1	SCON													
8-4	(C)	₹ C7	E-4	(F)	IC35	C-1	0	IC531	E-2	Ð	IC724	1-5	Ø	TP702	J-5	0
D-3	₿ 😥	IC8	D-1	0	IC36	D-4	Ð	IC532	F-3	Ø	IC725	H-4	(C)	TG1	F-1	Õ
A-3		IC9	C-1	©	JC37	8-2	Ð	JC701	H-3	0	IC726	H-4	©	TG2	D-1	_
D-4		IC10	C-2	(Ē)	₹C500	F-2		¥C702	1-2		Test Points			TG701	H-4	©
D-3	_	IC11	D-3	Ð	IC501	F-2	Ē	IC703	J-1	©	195t Points			Adjustment	1	
A-3		IC12	D-2	Ø	IC502	G-2	Ø	IC704	H-2	©	TP1	B-2	©	Adjustment		
A-3	-	IC13	D-2	0	IC503	F-3		IC705	G-2	Ð	TP2	€-4	0	VC1	D-3	©
A-4		IC14	D-5	®.	IC504	E-2	©	IG706	G-2	Ø	TP3	E-5	©	Switch		
A-4	_	IC15	€-2	©	IC505	D-1	©	IC707	H-3	ø	· TP4	E-4	©	- SHIGH		
A-3	_	IC16	D-2	Ø	IC506	E-1	Ð	IC708	H-2	Ø	TP5	B-2	(2)	SW501	G-1	
C-3		IC17	A-2	_	IC507	E-1	Ð	IC709	J-2	Ø	TP6	B-2	0	Connectors		
B-3		IC19	E-4	ø	IC508	D-1	ø	IC710	1-2	٥	TP7	B-2	0			
G-4	_	IC20	E-4	ø	#C509	G-2	0	10711	1-2	Ð	TP8	C-1	©	P1	G-5	
H-3	_	IC23	E-4	©	IC510	G-3	(C)	3C712	1-3	Ð	TP9	D-1	_	P2	D-5	
H-3	-	IC24	D-2	©	IC514	G-3	©	IC713	H-2	©	TP10	8-1	0			
H-3	_	IC26	A-3	© ©	IC515	G-4	©	IC714	G-4	©	TP11	B-2	0	l ·		
H-3	(f)	IC26	B-1	(C)	IC518	G-4	©	IC715	G-4	(Ē)	7P12	8-2	©			
. 9		IC27 IC28	B-5		IC517	F-1	©	IC716	F-4	©	TP500	F-2	0	ļ		
	-	IC29	A-3	4	IC523	F-1	0	IG717	G-4	© -	TP501	F-2	0			
C-1 A-2	© .	1 1C29	C-1	©	IC524	E-1	©	IC718	1-3	Ø	TP502	G-2	© .			
		1 1C30	0-3	(Ē)	IC525	G-1	©	IC719	J-4	0	TP503	G-2	0			
B-3		IC31	D-3	(Ē)	IC527	F-3	0	IC720	J-4	0	TP504	F-2	©			
8-2	_	1032	0.3	(f)	IC528	E-3	Ð	JC721	1-3	0	TP505	F-1	©			
A-2	_	1038 1034	C-2	ø O	IC529 IC530	É-3	Ð	IC722	1-3	0	TP506	F-1	©			
B-3	0	1034	A-2	0	E0030	E-3	€	1C723	1-6	Ð	TP701	J-5	0		1	



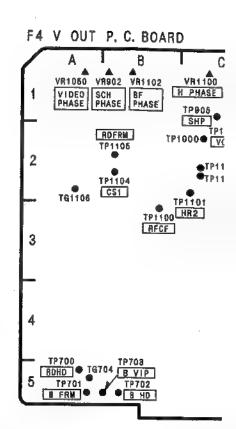
3-4





F4 V OUT P.C. BOARD (VEP83352A: AJ-D650/D640/D650P/D640P / VEP83352B: AJ-D650E/D640E)

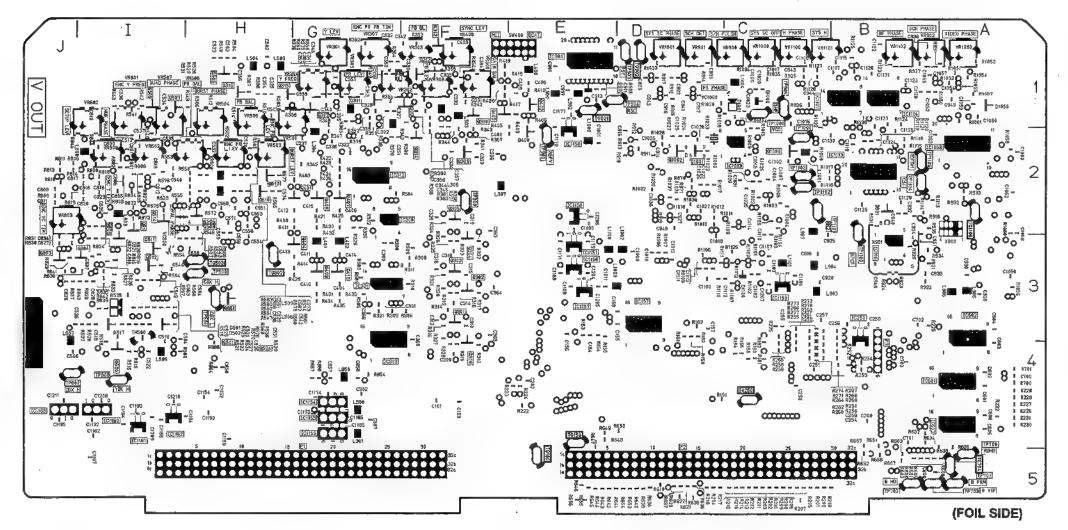
									F4 V	OUT									
Transistors		Q403	E-1 ©	Q802	J-3	© IC160	C-3 ©	IC602	A-4 Ø	IC1004	C-1 ©	IC1156	E-3 (f)	TP800	1-4	VR307	G-1	Connectors	
G200 G300 G301 G302 G302 G303 G304 G306 G306 G307 G308 G309 G309 G311	F-4 F-3 F-3 F-3 F-3 F-2 G-2 F-1 F-1 F-1 G-1	Q404 Q405 Q406 Q407 Q408 Q409 Q410 Q500 Q501 Q501 Q502 Q503 Q504 Q506	6.1.4.2.1.3.3.4.4.2.2.4.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1	Q803 Q804 Q805 Q805 Q806 Q807 Q850 Q851 Q1000 Q1001 Q1002 Transistor-Re-		(P)	E-4 © © © © © © © © © © © © © © © © © © ©	IC603 86700 1C800 1C801 1C802 1C804 1C805 1C800 1C900 1C902 1C904 1C908 1C909	A-4	IC1006 IC1015 IC1051 IC1053 IC1056 IC1056 IC1057 IC1058 IC1100 IC1102 IC1103 IC1106 IC1107	D-2	IC1157 IC1158 IC1159 IC1160 IC1161 IC1162 Test Points TG100 TG150 TG250 TG503 TG704	E-3 (F) E-2 (F) C-3 (F) H-4 (F) H-4 (F) E-5 E-4 C-4 H-3 A-5	TP801 TP900 TP903 TP904 TP905 TP1000 TP1001 TP1100 TP1101 TP1102 TP1103 TP1104 TP1105	J-4 E-2 D-1 D-1 C-1 C-2 C-1 B-2 C-2 C-2 C-2 B-2 B-2 B-2	VR400 VR500 VR501 VR502 VR503 VR504 VR505 VR506 VR507 VR800 VR801 VR802 VR802	F-1 H-2 H-1 H-2 H-1 H-1 H-1 H-1 H-2 I-1 I-2 I-1 I-2	P1 P2 P3	H-6 D-5 B-4
Q312 Q313	F-1 (F)	Q506 Q507	H-2 (F)	Integrated Circ	cuits	IC308 IC309	F-3 © G-1 ©	IC910 IC913	A-1 © A-2 ©	IC1108 IC1113	B-1 © B-2 (F)	TG802 TG901	H-3 E-1	Adjustments		VR900 VR901	C-1 D-1		
Q314 Q315 Q316 Q317 Q318 Q319 Q400	12 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Q508 Q509 Q510 Q511 Q512 Q613 Q514	H-1 ® H-2 ® H-2 ® I-3 ® I-3 ® H-2 ® I-2 ®	IC150 IC151 IC152 IC153 IC154 IC158 IC157	D-4 C-4 D-4 D-4 D-4 E-3 D-3	© IC310 © IC311 © IC312 © IC313 © IC500 © IC502 © IC503	G-1 (G) F-1 (G) G-2 (B) G-4 (D) H-2 (G) H-2 (G)	IC915 IC916 IC923 IC924 IC925 IC1000	0-2 © 0-2 B-3 © 0-3 © 0-2 © 0-3 © 0-3	IC1114 IC1115 IC1116 IC1150 IC1151 IC1152 IC1153	B-1 © © © E-2 © G-4	TG902 TG1106 TP300 TP500 TP501 TP700	D-1 A-2 F-2 H-3 H-3 A-5 A-5	VC500 VC1000 VR300 VR301 VR302 VR303 VR304	I-3 C-1 F-1 G-1 G-1 F-1 G-1	VR902 VR1000 VR1001 VR1050 VR1100 VR1102	B-1 C-1 D-1 A-1 C-1 B-1		and the state of t
Q401 Q402	G-2 (P) E-1 (P)	Q800 Q801	I-2 ©	IG158 IC159	C-4 D-3	© IC600 © IC601	B-5 © A-4 ©	IC1002 IC1003	G-2 © G-2 ©	IC1154 IC1155	G-4 G-4	TP702 TP703	8-5 8-5	VR305 VR306	G-1 F-1	Switches SW400	E-1	-	

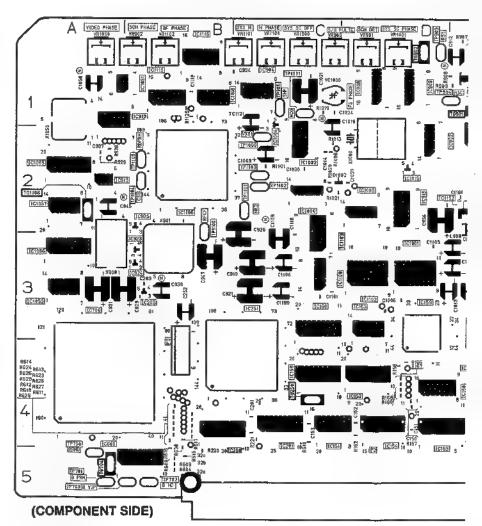


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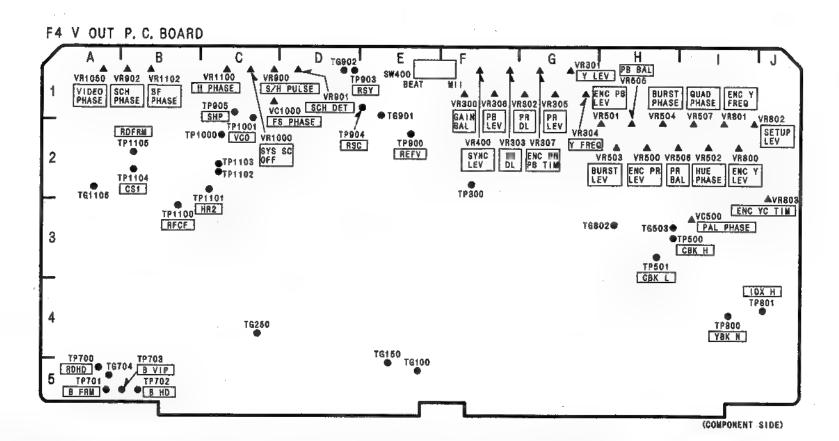
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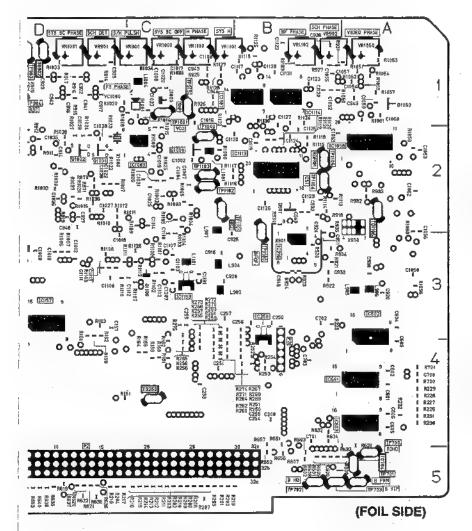


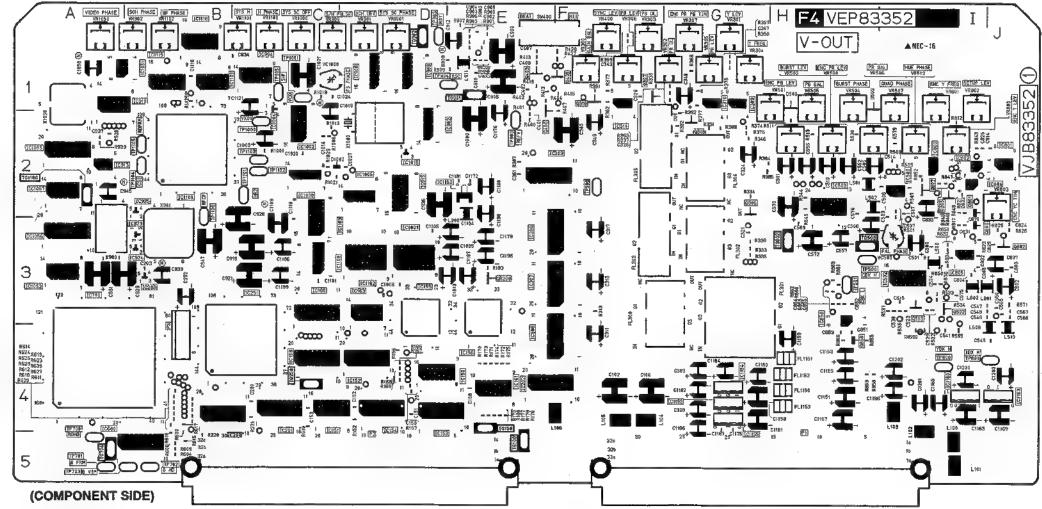
40P / VEP83352B: AJ-D650E/D640E)

F4 V	OUT										-	
0000000	IC1004 IC1006 IC1015 IC1051 IC1053 IC1056	C-1 D-2 D-2 8-1 A-3 A-2 A-3	00000000	IC1156 IC1157 IC1158 IC1159 IC1160 IC1161	E-3 E-3 E-2 C-3 I-4 I-4	9999	TP800 TP801 TP900 TP903 TP904 TP905 TP1000	I-4 J-4 E-2 D-1 D-1 C-1 C-2	VR307 VR400 VR500 VR501 VR502 VR503 VR504	G-1 F-1 H-2 H-1 I-2 H-2 H-1	P1 P2 P3	H-5 D-5 B-4
0000000	1C1057 IC1058 IC1100 IC1102 IC1103 IC1106 IC1107	A-2 C-3 D-3 C-3 B-2 B-1	@@@@@@	Test Points TG100 TG150 TG250 TG503 TG704	E-5 E-4 C-4 H-3 A-6		TP1001 TP1100 TP1101 TP1102 TP1103 TP1104 TP1105	C-1 B-2 C-2 C-2 C-2 B-2 B-2	VR505 VR506 VR507 VR800 VR801 VR802 VR803 VR900	H-1 H-2 I-1 I-2 I-1 I-1 J-2 C-1		
98888888888888888888888888888888888888	IC1108 IC1113 IC1114 IC1115 IC1216 IC1150 IC1151 IC1152 IC1153 IC1154 IC1155	B-1 2 3 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	<u> </u>	TG802 TG901 TG902 TG1106 TP300 TP500 TP501 TP700 TP701 TP702 TP702	H-3 E-1 D-1 A-2 F-2 H-3 A-5 A-5 B-6 B-5		Adjustments VC500 VC1000 VR300 VR301 VR302 VR303 VR304 VR306 VR306	I-3 C-1 F-1 G-1 G-1 F-1 G-1 F-1	VR901 VR902 VR1000 VR1001 VR1050 VR1100 VR1102 Switches SW400	D-1 B-1 C-1 D-1 A-1 C-1 8-1	; ;	



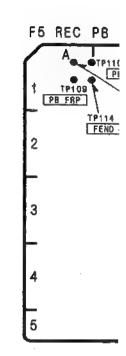
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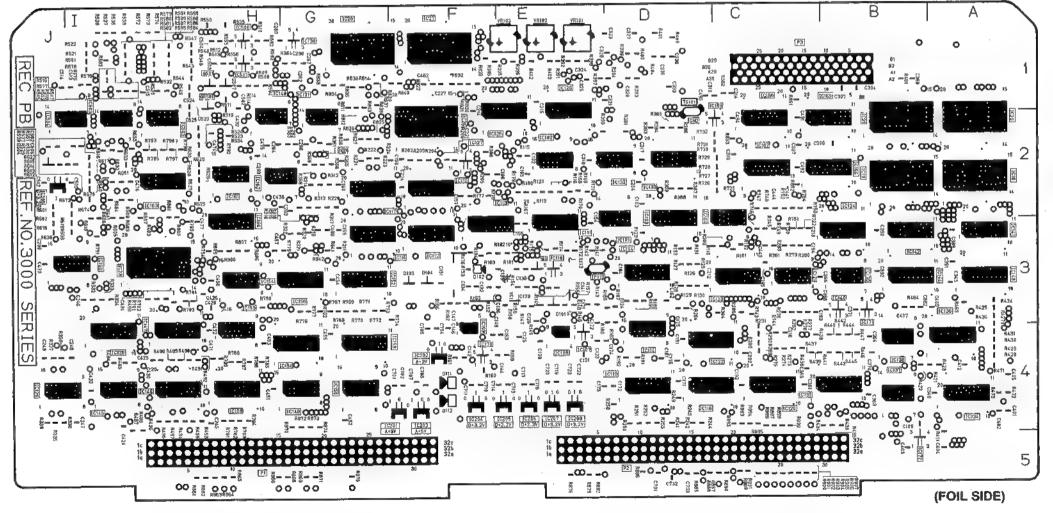
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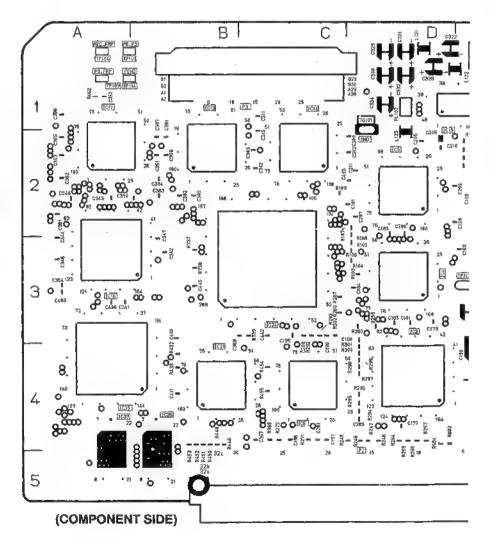
									F5 Ri	EC PB										
Transistors			IC3019	B-2	®	IC3114	D-4 F-2	(P) (D)	IC3140 IC3141	B-3	Ē	1C3167 1C3168	C-2	(P)	Test Points			VR3102 VR3103	E-1 E-1	
Q3001 Q3501	E-3 H-1	© ©	#C3020 #C3021 #C3022	A-2 B-2 H-3	ø ø	103115 103116 103117	G-3 G-2	(F)	IC3142 IC3143	A-3 B-3 A-3	(F)	1C3169 1C3171	C-2 8-3	Ó Ó	TG3101 TG3102	D-1 H5	0	Switches		
Transistor-Res	lator		IC3023	1-3	€	IC3118	D-4 D-4	Ē	IC3144 IC3145	B-3 I-4	Ē Ē	IC3172 IC3201	B-5 F-4	(E)	TG3501 TP3101	G-1 F-1	© ©	SW3101 SW3102	E-1	0
QR3501	H-1	Ē	IC3024 IC3025	C-3 G-4	0	IC3119	C-4	Ē	IC3146	1-4	Ē	#C3202	F-4	Ø	TP3102 TP3103	F-1 F-1	© ©	Connectors	E=1	
Integrated Circ	uits		1C3027 IC3028	F-1 G-1	® ®	1C3121 4C3122	C-3 D-4	© ©	IC3147 IC3148	I-4 H-2	Ð	IC3203 IC3204	F-4 F-4	e e	TP3104 TP3105	A-1 H-2	© ©	P3001	G-5	
IC3001	E-2	0	IC3030	G-3	0	103123	Ç-3 B-3	© ©	IC3149 IC3150	G-4 G-4	(F) (F)	103205 103206	€-4 E-4	Ø Ø	TP3106	H-2	٧	P3002 P3003	D-6 C-1	
IC3003 IC3004	D-4 D-3	© ©	IC3031 IC3035	B-4 A-4	0	103124 103125	E-1	Œ	IC3151	G-4	Ð)C3207	E-4	Ð	TP3108 TP3109	F-1 A-1	© ©	13000	0-1	i
IC3005 IC3006	D-2 C-4	0	IC3036 IC3037	8-4 A-4	©	IC3126	E-1 F-2	E E	IC3152 IC3153	G-3 G-4	© Ø	IC3208 IC3501	E-4 1-1	©	TP3110	A-1	Ö			
103007	F-2	©	IC3101	C-3	(f)	IC3128	1-4	(Ē)	IC3154	H-3	(Ē)	IC3501 IC3502	1-1 J-2	Ø	TP3111 TP3112	F-1 F-1	© ©			
IC3008 IC3009	F-1 H-4	0	IC3102 IC3103	E-2 D-3	(F)	IC3129	J-4 G-1	(F) (F)	IC3156 IC3157	G-3 H-3	©	IC3502	1-2	(£)	7P3113 7P3114	E-3 A-1	©			
IC3010 IC3011	(-4 G-2	0	IC3104 IC3105	D-3 D-3	(Ē)	IC3131 IC3132	G-2 D-2	(F) (F)	IC3158 IC3159	H-4 H-4	(Ē)	IC3504 IC3505	J-2 J-2	© ©	TP3501	G-1	0			
IC3012	G-1	©	IC3107	E-4	(P)	IC3133	D-2	Œ	IC3160	1-3	(F)	IC3507	H-1	(Ē)	TP3502 TP3601	G-1 G-1	0			
IC3013 IC3014	B-2 G-2	0	IC3108 IC3109	E-4 F-4	(F)	JC3134 IC3135	A-4 B-4	(Ē)	IC3161 IC3162	H-2 H-2	Ø Ø	IC3508 IC3601	H-1 ∔-2	(P) (©)	7P3602	G-1	O			
IC3015	D-1	©	IC3110	E-3 E-3	(F)	IC3136 IC3137	A-4 B-4	© ©	IC3163 IC3164	I-2 H-3	Ē	IC3601 IC3603	1-2.	(Ē)	Adjustments					
IC3016 IC3017	A-3 A-2	© ©	IC3111 IC3112	F-3	© ©	IC3137	8-4	Ē	IC3165	B-2	Ð	RAZOU3	J-~.	W)	VC3001	E-3				
IC3018	A-2	ø.	IC3113	F-3	Ē	IC3139	B-3	(F)	IC3166	B-2	®.		Į .		VR3101	E-1		<u> </u>		



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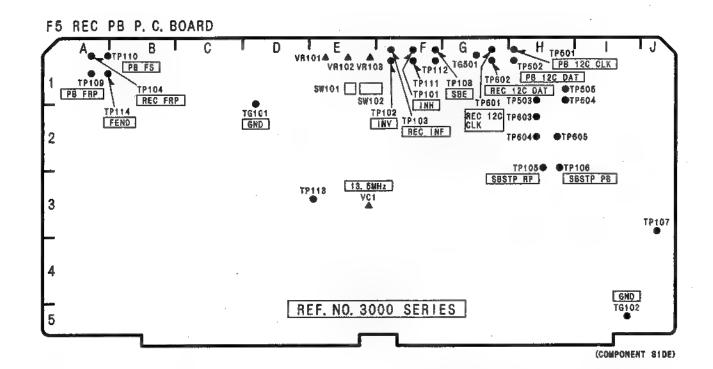
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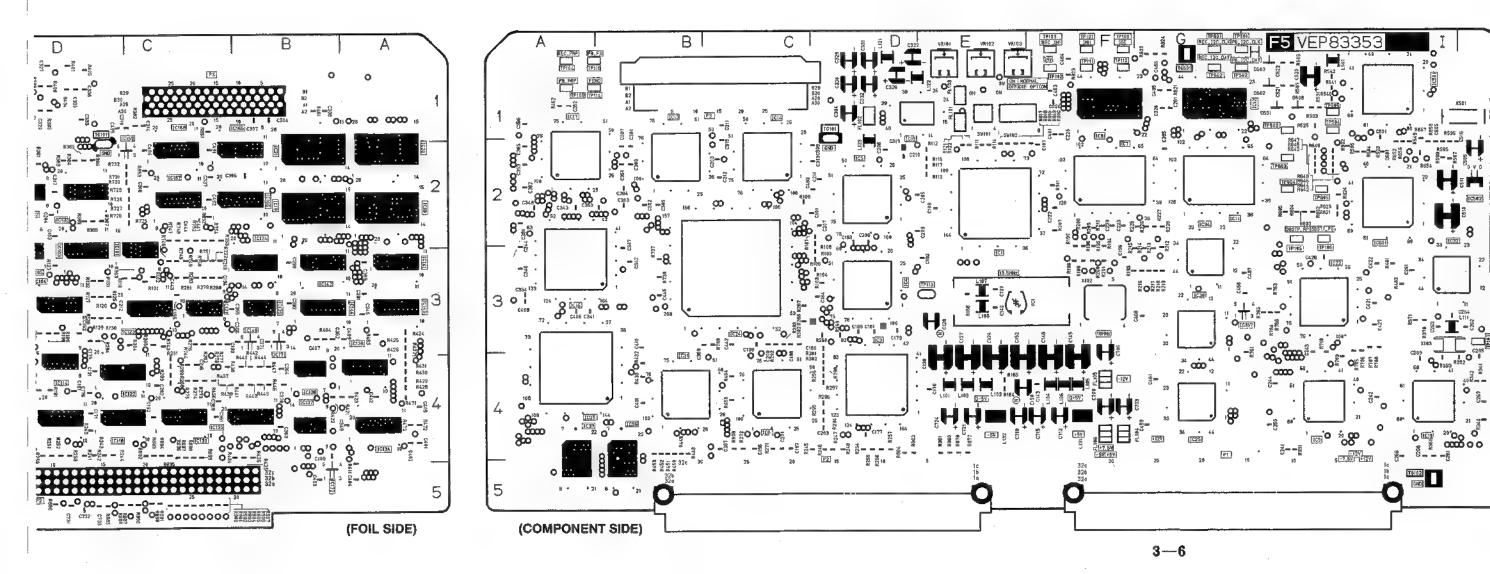




340P / VEP83353B: AJ-D650E/D640E)

				F5 Rt	EÇ PB			· ·							
)	IC3114 IC3115	D-4 F-2	e e	IC3140 IC3141	B-3 A-3	(F)	1C3167 IC3168	C-2 C-2	© ©	Test Points			VR3102 VR3103	E-1 E-1	
5	IC3116 IC3117	G-3 G-2	(Ē)	IC3142 IC3143	B-3 A-3	Ē Ē	IC3169 IC3171	C-2 B-3	ø ø	TG3101 TG3102	D-1 I-5	©	Switches		
5	IG3118 IG3119 IG3120	D-4 D-4 C-4	999	IC3144 IC3145 IC3146	B-3 F-4 F-4	© ©	IC3172 IC3201 IC3202	B-5 F-4 F-4	(F) (F) (F)	TG3501 TP3101 TP3102	G-1 F-1 F-1	(Q) (Q) (Q)	SW3101 SW3102	E-1 E-1	© ©
5	IC3120	C-3	®	IG3147	1-4	Ē	IC3203	F-4	Ē	TP3103	F-1	. ©	Connectors		
000000000000000000000000000000000000000	IC3122 IC3123 IC3124 IC3125 IC3126 IC3127 IC3128 IC3129 IC3130 IC3131 IC3132 IC3133 IC3134 IC3135 IC3136 IC3136 IC3137 IC3138	D-4 C-3 B-3 E-1 F-2 I-4 J-4 G-1 G-2 D-2 A-4 B-4 B-4 B-4	ଉ ଏ ର ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ	IC3148 IC3149 IC3150 IC3151 IC3152 IC3153 IC3154 IC3156 IC3157 IC3158 IC3159 IC3160 IC3161 IC3162 IC3163 IC3164 IC3166	H-2 G-4 G-4 G-3 G-3 H-3 H-4 H-4 H-2 H-2 H-2 H-3 B-2	ପ୍ର ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ	IC3204 IC3205 IC3206 IC3207 IC3208 IC3501 IC3502 IC3502 IC3503 IC3504 IC3504 IC3505 IC3507 IC3508 IC3607 IC3601 IC3601 IC3603	F-4 E-4 E-4 E-4 F-1 I-1 J-2 J-2 J-2 J-2 J-1 I+1 I+1	ର ଉପରର୍ଜ୍ଞର୍ଜ୍ଞର୍ଜ୍ଞର୍କ୍ତ	TP3104 TP3105 TP3106 TP3108 TP3109 TP3110 TP3111 TP3112 TP3113 TP3114 TP3501 TP3502 TP3602 Adjustments VC3001	A-1 H-2 H-2 F-1 A-1 A-1 F-1 E-3 A-1 G-1 G-1	00000 0000000	P3001 P3002 P3003	G-6 D-5 C-1	
ó	IC3139	B-3	Ē	IC3166	B-2	Ē	1			VR3101	E-1				





F6 V IN P.C. BOARD (VEP83355A: AJ-D650/D640/D650P/D640P / VEP83355B: AJ-D650E/D640E)

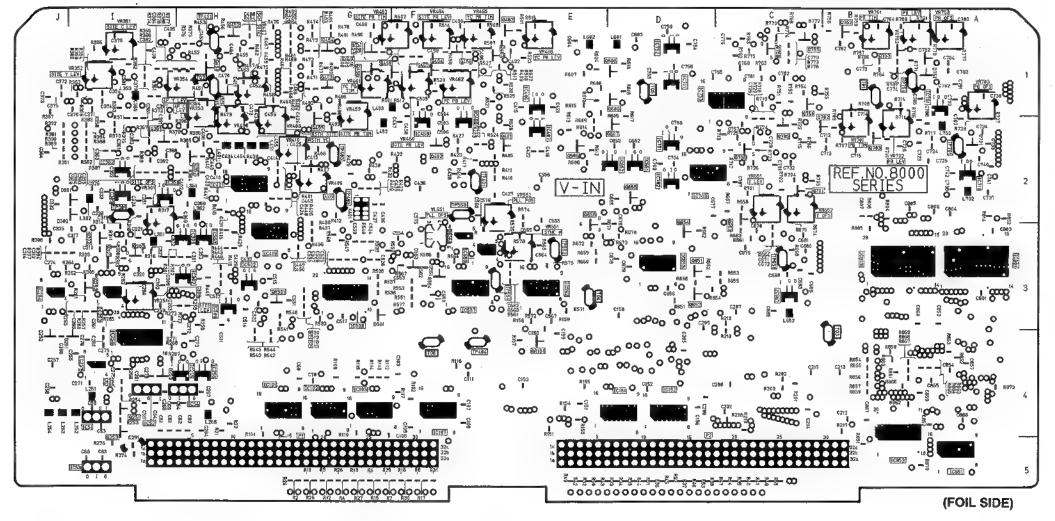
												F6	VIN]
Transistors			Q8552	F-3	©	Transistor-Res	istors		IC8204	B-4	0	IÇ8403	G-2	©	IC8567	F-3	©	IC8855	A-4	0	VR8351	1-1	Connectors	
00054	1.4	A	Q8553	F-3	©	QR8151	E-4	€	IC8205	C-4	©	IC8404	H-2	©	1C8651	D-2	ø	IC8856	A-4	©	VR8352	3-1	P8001	G-6
Q8251 Q8252	1-4 1-3	®	Q8554	F-3	0	QR8501		Ø	IC8210	C-4	0	IC8406	E-\$	Ð	1C8652	D-2	ø	IC8857	B-5	0	VR8353	H-1	P8002	D-5
Q8253	I-S	Ø	Q8601	E-1	©	Gribaut	נרוז		IC8251	H-3	©	IC8407	E-2	ø	IC8653	D-3	Ø	IC8858	A-5	C	VR8354	H-1	1 0002	
Q8301	I-3	œ	Q8602	€-2	©	Integrated Circ	uits		IC8252	1-3	ூ	IC8410	F-2	©	C8655	D-3	(0)	Test Points			VR8406	H-2		[
Q8302	1-3	®	Q8603	€-1	0				IC8254	J-3	®	IC8414	F-2	©	₹C8656	C-3	0				VR8407	H-1		[
Q8303	1-3	Ð	Q8604	€-2	©	IC8051	1-4		IC8256	H-4	Ð	IC8418	F-2	0	£C8660	C-3	0	TG8001	B-1		VR8408	H-2		
Q8351	F-2	Ø	Q8605	E-2	ø	IC8062	1-4	j	IC8256	H-4	Ø	IC8419	H-3	(Ē)	iC8661	C-3	Ð	TG8002	H-1		VR8409	G-2		
Q8352	H-2	Ó	Q8606	D-1	Ø	ICB053	I-5	-	IC8257	1-4	ூ	IC8423	G-2	©	3C8701	D-2	Ø	TG8003	B-4		VR8410	G-5		
Q8401	E-2	Ð	Q8607	0-2	Ð	1C8054	H-4		IC8258	1-4	Ð	IC8428	H-3	0	IC8702	D-2	Ð	TG8004	E-3		VR8459	G-2		
Q8451	H-1	(f)	Q8608	ี ฮ-เ	ø	IC8101		©	IC8259	H-4	©	IC8451	H-1	©	IC8703	D-2	Ø	TG8005	D-1		VR8460	F-1		
Q8452	H-1	Ð	Q8651	. 0-3	Ø	ICB102		(F)	(C8301	H-3	(E)	IC8452	G-1	©	IC8705	C-2	0	TG8006	F-4		VR8461	G-1		
			Q8652	D-3	Ø	\$C8103		©	iC8302	H-3	Ð	IC8453	G-1	0	IC8706	A-2	©	TP8301	1-2		VR8462	F-1		
Q8453	G-1	(F)	Q8653	Ç-3	Ð	ì¢8107		6	IC8303	1-3	©	IC8455	F-1	© :	IC8710	A-2	0	TP8401	F-2		VR8463	F-1		!
Q8454	G-1	(F)	Q8654	D-3	Ð	#C8109	F-4	0	IC8304	J-3	0	IC8456	F-1	0	IC8711	A-2	Ð	TP8403	G-2		VR8464	F-1		
Q8455	G-1	Ð	Q8655	D-2	ø	IC8110		©	IC8308	J-2	©	IC8459	F-2	Ð	IC8751	D-1	€	TP8404	F-4		VR8465	F-1		
Q8456	G-1	(Ē)	Q8656	€-3	Ð	IC8111		©	IC8309	1-5	Ð	IC8460	F-2	Ø	IC8752	D-1	€	TP8405	G-2		VR8466	E-1		
Q8457	G-1	Ē	Q8701	C-2	Ø	IC8151		(P)	IC8351	1-2	Ø	IC8501	H-3	(Ē)	IC8753	C-1	Ø	TP8451	H-1		VR8551	E-3	}	!
Q8458	IF-1	Ð	Q8702	B-2	Ð	IC8152	D-4	(f)	IC8352	1-2	Ð	IC8502	H-3	ø	IC8755	C-1	0	TP8551	F-3		VR8552	E-2	[
Q8459	F-1	ø	Q8703	B-2	⑥	iC8153	E-4	0	IC8353	H-3	®	IC8503	G-3	(Ē)	IC8756	B-1	©	TP8552	E-3		VR8651	C-2		
Q8460	F-1	©	Q8704	C-2	0	≇C8154		©	IC8354	I-2	©	IC8504	G-3	©	IC8760	A-1	0	TP8553	F-2		VR8652	C-5		
Q8461	F-1	©	Q8705	C-2	Ð	IC8155		0	IC8355	I-1 .	©	IC8507	H-3	©	IC8761	A-1	€	TP8651	C-3		VRB701	B-2		
Q8462	E-1	Ð	Q8706	C-2	⑥	IC8156	D-4	0	IC8356	1-2	©	IC8551	F-3	©	IC8801	A-3	Ē	TP8701	A-2		VR8702	B-2		
Q8463	F-1	Ð	Q8751	G-1	Ð	IC8161	€-4	©	IC8357	I-1	0	IC8552	F-3	0	IC8802	8-3	©	TP8751	B-1		VR8703	A-1		
Q8464	F-1	(Ē)	Q8752	B-1	Ð	IC8164	D-4	0	108358	H-1	(C)	IC8554	E-3	©	IC8803	B-3	Ð	Adjustments			VR8751	B-1	ŀ	
Q8465	F-1	©	Q8753	B-1	®	IC8165	E-4	0	108359	1-1	©	IC8557	F-3	Ē	1G8804	B-3	(C)	,			VR8752	B-1		
Q8466	E-1	0	Q8754	C-1	0	IC8201	C-4	©	2C8401	H-2	Ð	IC8560	F-3	(E)	IC8851	A-5	Ø	VL8551	F-2		VR8753	A-1		
Q8467	F-1	ø	Q8755	C-1	℗	IC8202	C-4	©	IC8402	G-2	©	IC8561	F-3	ø	3C8853	B-4	Ē	VR8251	1-3			[1	
Q8551	F-3	©	Q8756	C-1	Ð	IC8203	6-4	©	IC8402	H-2	0	IC8562	E-3	(Ē)	IC8854	B-4	©	VR8301	1-2					

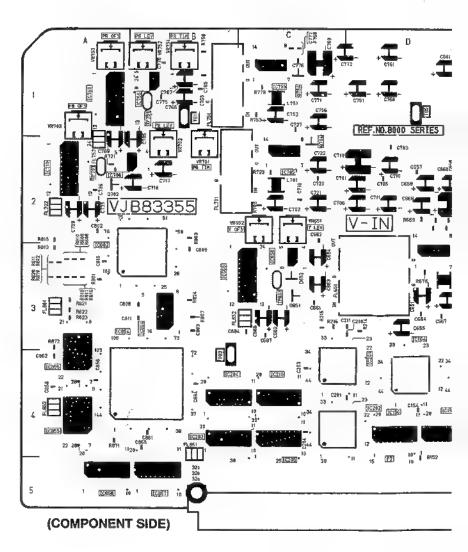
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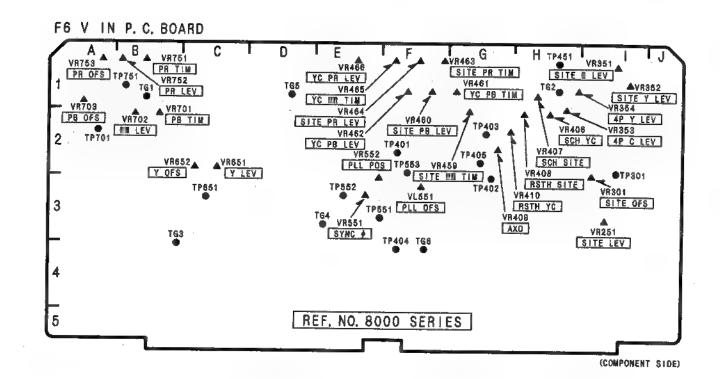
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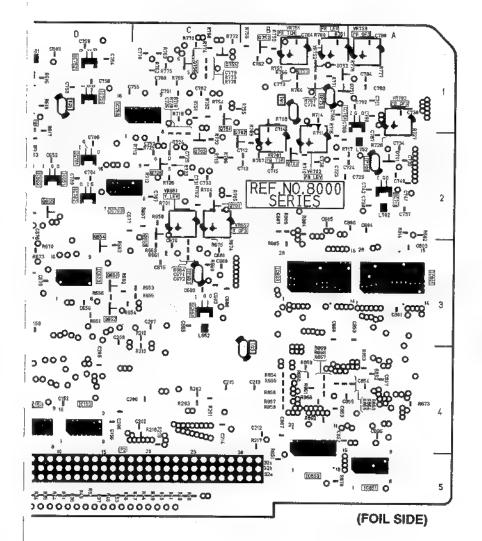


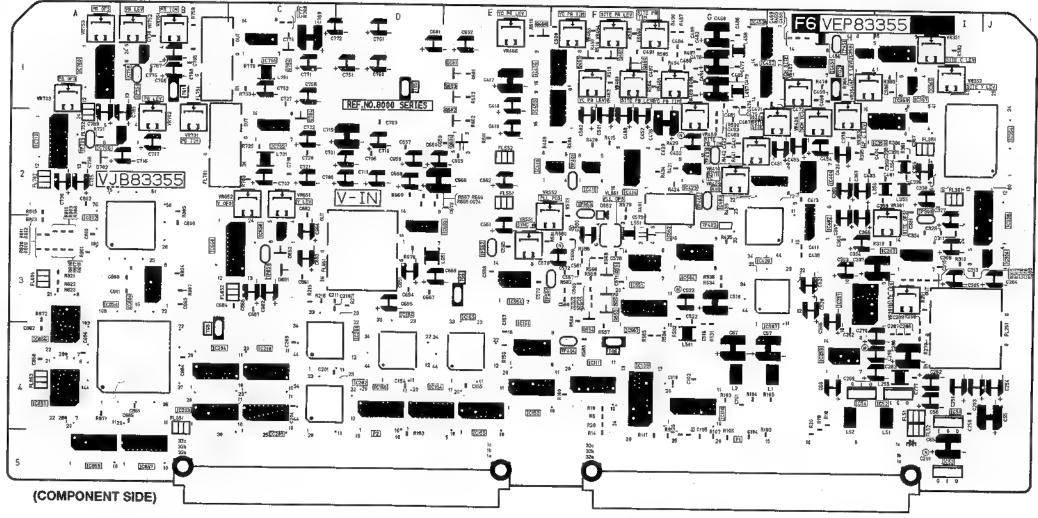
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			F6	VIN										•	
14	6-4	0	IC8403	G-2	0	IC8567	F-3	0	IC8855	A-4	©	VR8351	I-1	Connectors	
15	C-4	0	IC8404	H-2	©	IC8651	D-2	Œ	IC8856	A-4	(2)	VR8352	I-1		T
0	C-4	©	IC8406	E-1	ø	IC8652	D-2	®	IC8857	B-5	0	VR8353	H-1	P8001	G-5
1	H-3	©	IC8407	E-2	ø	IC8653	D-3	Ð	IC8858	A-5	©	VR8354	H-1	P8002	D-5
2	1-3	Ð	IC8410	F-2	©	IC8655	D-3	•	Test Points			VR8406	H-2		
ļ	J-3	Ē	IC8414	F-2	0	IC8656	C-3	0				VR8407	H-1	ļ	
5	H-4	Ð	IC8418	F-2	(0)	ICB660	C-3	Ø	TG8001	B-1		V∓8408	H-2	}	
5	H-4	Ø	JC8419	H-3	Ð	IC8661	C-3	Ð	TG8002	H-1		VR8409	G-2		
7	1-4	Ø	IC8423	G-2	©	IC8701	D-2	Ð	TG8003	B-4		VR8410	G-2		}
В	1-4	Ø	IC8428	H-3	C	iC8702	D-2	Ø	TG8004	E-3		VR8459	G-2		
9	H-4	0	IC8451	H-1	©	IC8703	D-2	Ø	TG8005	D-1		VR8460	F-1		
l	H-3	(Ē)	IC8452	G-1	©	IC8705	C-2	©	TG8006	F-4		VR8461	G-1		
2	H-3	Ð	IC8453	G-1	0	IC8708	A-2	0	TP8301	1-2		VR8462	F-1	1	
3	1-3	0	IC8455	F-1	©	IC8710	A-2	©	TP8401	F-2		VR8463	F-1	1	
1	J-3	©	IC8456	F-1	0	IC8711	A-2	Ð	TP8403	G-2		VF18464	F-1	}	
3	J-2	0	IC8459	F-2	Ð	JC8751	D-1	(Ē)	TP8404	F-4		VR8465	F-1		l
)	1-2	Ø	1C8460	F-2	Ø	IC8752	D-1	Ð	TP8405	G-2		VR8466	E-1		
1	1-2	Ø	IC8501	H-3	Ð	IC8753	C-1	(P)	TP8451	H-1		VR8561	£-3		1
2	1-2	Ð	IC8502	H-3	ø	IC8755	C-1	©	TP8551	F-3		VR8552	E-2		
3	H-3	Ð	IC8503	G-3	Œ	IC8756	8-1	©	TP8552	E-3		VR8651	C-2		
ļ.	I-2	0	IC8504	G-3	0	IC8760	A-1	©	TP8553	F-2		VR8652	C-2		
5	I-1	©	IC8507	H-3	٩	IC8761	A-1	Ø	TP8651	C-3		VR8701	B-2		
6	1-2	©	IC8551	F-3	©	IC8801	A-3	ø	TP8701	A-2		VR8702	B-2	1	
7	I-1	©	IC8552	F-3	Ø	IC8802	B-3	0	TP8751	B-1		VR8703	A-1		
8	H-1	0	€C8554	E-3	© :	fC8803	B-3	®	Adjustments			VR8751	B-1		ļ
3	I-1	©	1C8557	F-3	Ē	IC8804	B-3	©	Mujusarierits			VR8752	B-1		
1	H-2	®	IC8560	F-3	(F)	IC8851	A-5	(F)	VL8551	F-2		VR8753	A-1		
2	G-2	©	IC8561	F-3	®	IC8653	B-4	⊕	VR8251	I-3					
5	H-2	©	IC8562	E-3	Ð	IC8854	B-4	© i	VR8301	I-2					



3-7



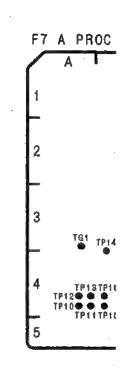


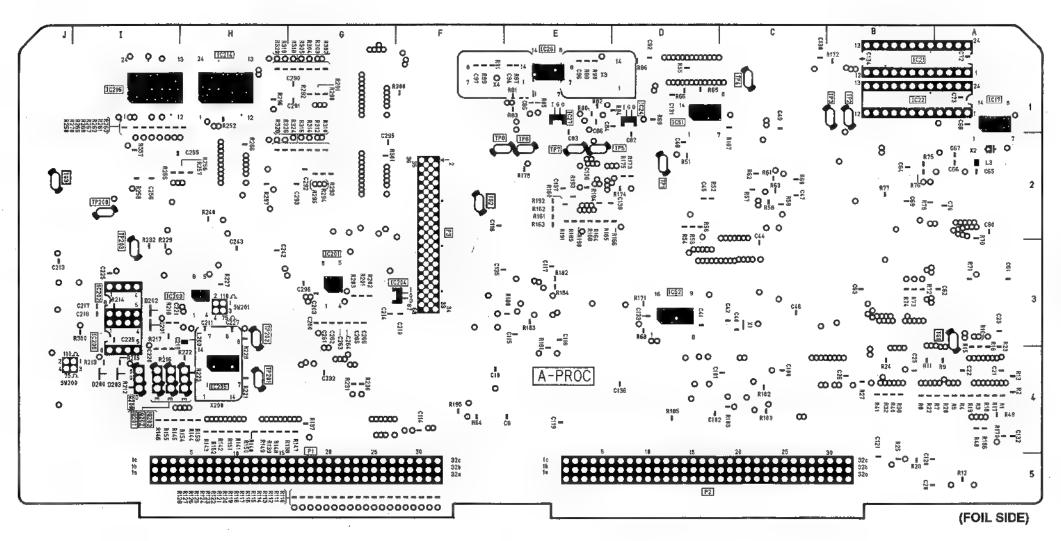
					F7 A	PROC					
Integrated Circ	:ults		IC24	D-1	Œ)	IC50	A-4	0	TP7	E-2	
IC1 IC2 IC4 IC5 IC6 IC7 IC8 IC9 IC10 IC11	A-4 B-4 A-5 A-3 B-4 B-5 C-3 D-3 C-3 C-2	0000000000000	IC25 IC26 IC27 IC28 IC30 IC31 IC34 IC35 IC36 IC37 IC38	E-1 E-1 E-1 C-4 D-4 G-4 G-4 H-4	000000000000000000000000000000000000000	IC51 IC52 IC53 IC54 IC56 IC56 IC57 IC58 Test Points	D-1 D-3 F-3 C-1 E-2 E-2 B-1 D-4	000000000	TP8 TP10 TP11 TP12 TP13 TP14 TP15 TP15 TP16 TP17 TP18 TP19 TP20	F-2 A-4 A-4 B-3 B-4 B-4 B-4 C-4	ଚଡ଼ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ
IC13	D-1	0	IC39 IC40	H-4	© ©	TG2	F-2				
IC14 IC15	B-3 A-2	© ©	IC40 IC41	F-4	0	TG3	J-2 0-2		Connectors	1	
IC16 IC17	A-3 A-1	6	IC42 IC43	F-3 E-4	Ö	TP2 TP3	B-1 B-1		P1 P2	G-6 D-5 F-2	
IC19 IC20	B-2 B-2	0	IC44 IC45	E-3 E-3	© ©	TP4 TP5	C-1 E-2		P3	F-2	
IC23	A-1	0	IC46	F-2	٥	TP6	E-2				

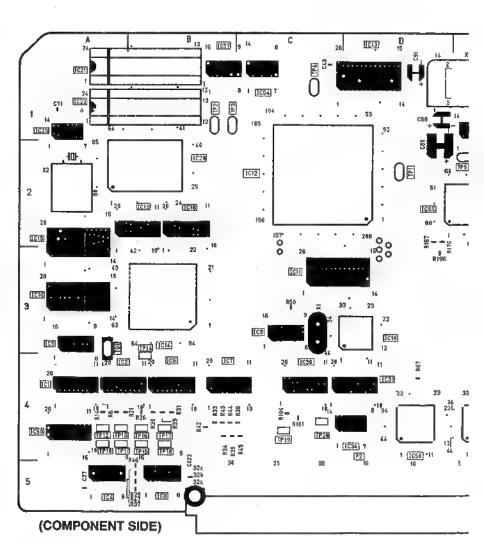
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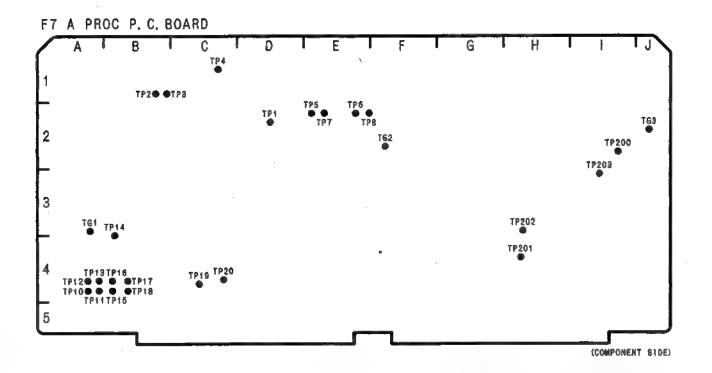


					F7 A	PROC					
Integrated Cl	1		IC24 IC25	D-1 E-1	©	IC50 IC51	A-4 D-1	© (£)	TP7 TP8	E-2 F-2	
IC1 IC2 IC4 IC5 IC6 IC7 IC8 IC9	A-4 B-4 A-5 A-3 B-4 B-4 B-5 C-3	ଉଡ୍≣ଚ୍ଚ୍ଚ୍ଚ	IC26 IC27 IC28 IC30 IC31 IC34 IC35	E-1 E-1 C-4 D-4 D-4 G-4 G-4	ଚଚଚଚ୍ଚ ବ ବ ବ ବ କ	IC52 IC53 IC54 IC56 IC56 IC67 IC58	D-3 F-3 C-1 E-2 E-2 8-1 D-4	00000000	TP10 TP11 TP12 TP13 TP14 TP15 TP16 TP17	A-4 A-4 A-4 B-3 B-4 B-4	9999
IC10 IC11 IC12	D-3 C-3 C-2	000	IC36 IC37 IC38 IC39	H-4 I-4	000	Test Points	A-3		TP18 TP19 TP2Ⅲ	B-4 C-4 C-4	0
IC13 IC14	D-1 B-3	0	1C40	1-4	0	TG2 TG3	F-2 J-2		Connectors		
IC15 IC16 IC17 IC19	A-2 A-3 A-1 B-2	999	IC41 IC42 IC43 IC44	F-4 F-3 E-4 E-3	0000	TP1 TP2 TP3 TP4	D-2 B-1 B-1 C-1	•	P1 P2 P3	G-5 D-5 F-2	
IC20	B-2	0	IC45	E-3	© ©	TP5	E-2				

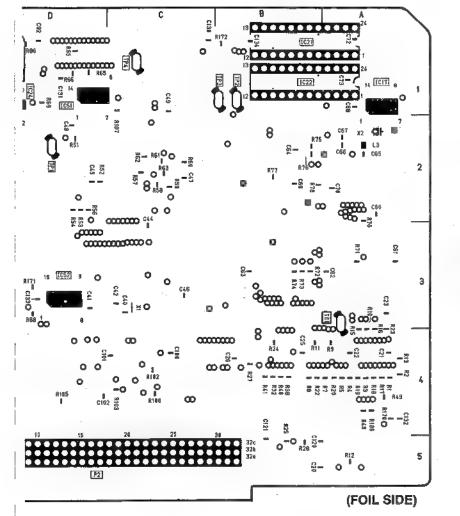
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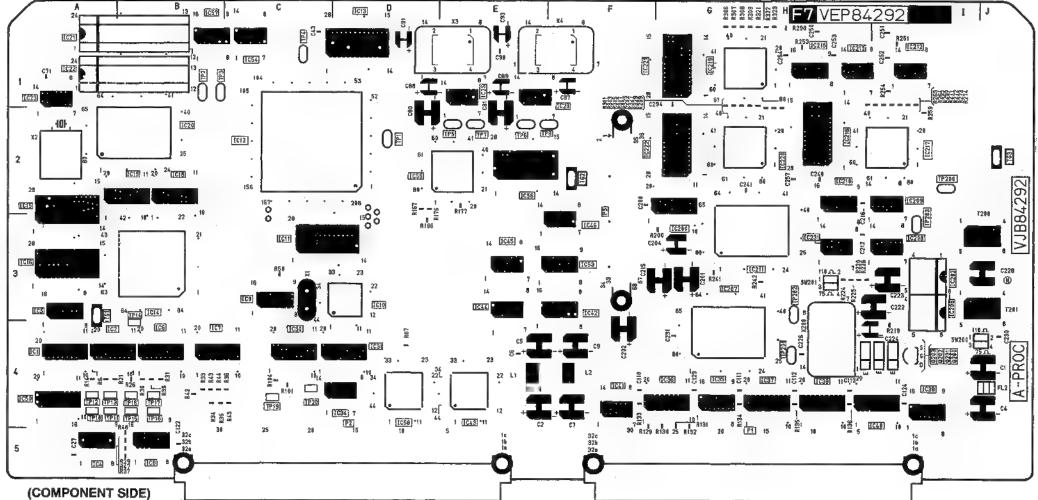
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F8 ADDA CUE P.C. BOARD (VEP84293A: AJ-D650/D640/D650P/D640P / VEP84293B: AJ-D650E/D640E)

	-						···· \				F8 /	ADDA				· · · ·						
Transistors		j	Q4342	E-3	0	Q4506	B-1 (Ē)	IÇ4006	J-3	©	IC4194	G-1	(Ē)	fC4386	C-2	©	Test Points		TP4502	B-3	Connectors	
Q4221 Q4222 Q4223 Q4224 Q4225 Q4226 Q4227 Q4228 Q4229 Q4230 Q4231 Q4281 Q4282	G-3 G-3 G-3 G-2 G-3 G-3 G-3 G-3 G-2 F-3	<u>ଜଡ଼ଉଉଉଉ</u>	Q4343 Q4344 Q4345 Q4346 Q4347 Q4348 Q4350 Q4381 Q4382 Q4383 Q4384 Q4384	E-3 E-2 D-2 D-3 E-3 D-3 B-3 C-3 B-3 C-3	(安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安安	Q4507 Q4508 Q4509 Q4510 Q4511 Q4512 Q4513 Transistor-Res QR4191 QR4341 QR4342	F-1 (P) E-4 (P) E-4 (P)	IC4008 IC4061 IC4062 IC4063 IC4064 IC4065 IC4066 IC4066 IC4068 IC4121 IC4122 IC4123 IC4124	I-3 I-4 I+3 I+3 I+3 I+3 I+3 I+2 I+2 I-1 J-2	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	IC4195 IC4221 IC4222 IC4223 IC4224 IC4225 IC4226 IC4281 IC4282 IC4283 IC4284 IC4341	E-1 G-4 G-4 G-2 G-2 G-3 G-2 F-2 E-2 F-3 E-2	ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ	(C4388 (C4389 (C4390) (C4391) (C4391) (C4461) (C4462) (C4463) (C4464) (C4464) (C4465) (C4466) (C4501) (C4551)	B-1 D-2 D-2 D-3 C-3 D-1 D-1 D-1 D-1 D-2 D-2 D-2 D-2	କ କଚ୍ଚତ୍ରକ୍ଷର ବ	TG4122 P TG4191 C TG4192 E TG4461 E TG4501 P TP4121 F TP4122 TP4123 F TP4124 TP4126 J J	-2 1-2 3-1 3-1 3-1 3-1 3-1 4-3 4-2 4-2 4-1 4-1 4-2 4-2	Adjustments VR4002 VR4062 VR4221 VR4281 VR4381 VR4382 VR4383 VR4384 VR4386 VR4501	H3 G-2 F-2 E-2 B-1 B-2 C-3 C-3 B-2 A-1	P4001 P4002	G-5 D-5
Q4283 Q4284 Q4285 Q4286 Q4287 Q4288 Q4289 Q4290 Q4341	F-3 F-3 F-2 E-3 F-3 F-3 F-2 E-3	GGGGGG	Q4396 Q4461 Q4462 Q4463 Q4501 Q4502 Q4503 Q4504 Q4505	G-3 D-1 D-1 D-1 A-2 A-2 A-2 B-2 A-1	\$\$\$\$\$\$\$\$\$\$\$	QR4381 QR4382 Integrated Circ IC4001 IC4002 IC4003 IC4004 IC4005	C3	KC4125 KC4126 KC4127 KC4128 KC4129 KC4131 KC4191 KC4192 KC4192	H-2 H-2 H-2 H-1 F-1 G-1 F-1	8898888	IC4342 IC4343 IC4344 IC4345 IC4381 IC4382 IC4383 IC4384 IC4384	E-2 E-1 E-2 B-1 C-1 C-1 C-2 B-2	<u>ଉଚ୍ଚତ୍ର ଜନ୍ୟ ବ୍ରତ୍</u>	IC4552 IC4552 IC4553 IC4554 IC4555 IC4556 IC4557 IC4558	G-4 F-4 G-4 E-4 F-4 D-4	<u> </u>	TP4192 C TP4193 E TP4221 C TP4281 F TP4381 C TP4382 C TP4383 C	5-1 3-1 5-2 3-2 5-2 3-1 3-3 3-3	Switchas SW4001 SW4061 SW4381 SW4382	I-4 IH-4 B-1 © C-1 ©		

F8 ADDA

A
VR501

1

CUE
BIA

2

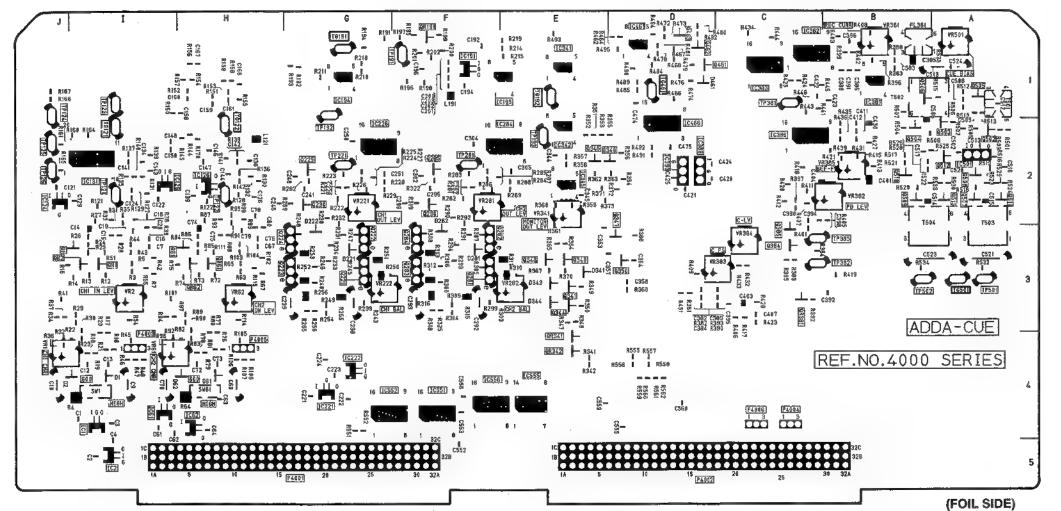
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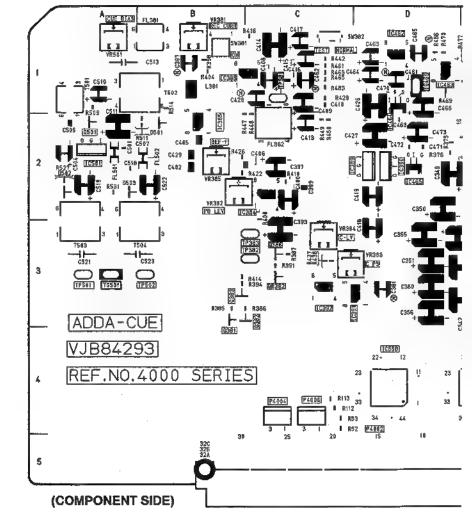
TP501
TG501

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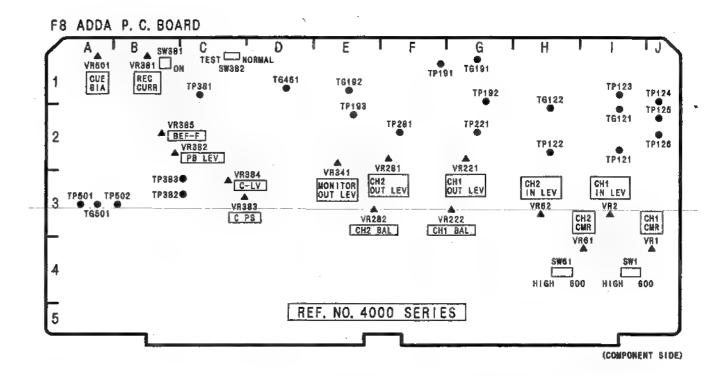
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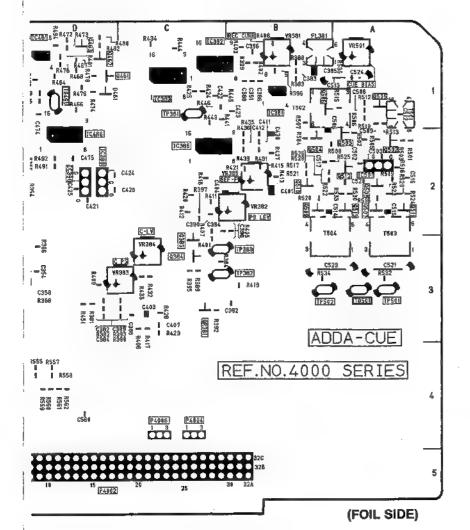


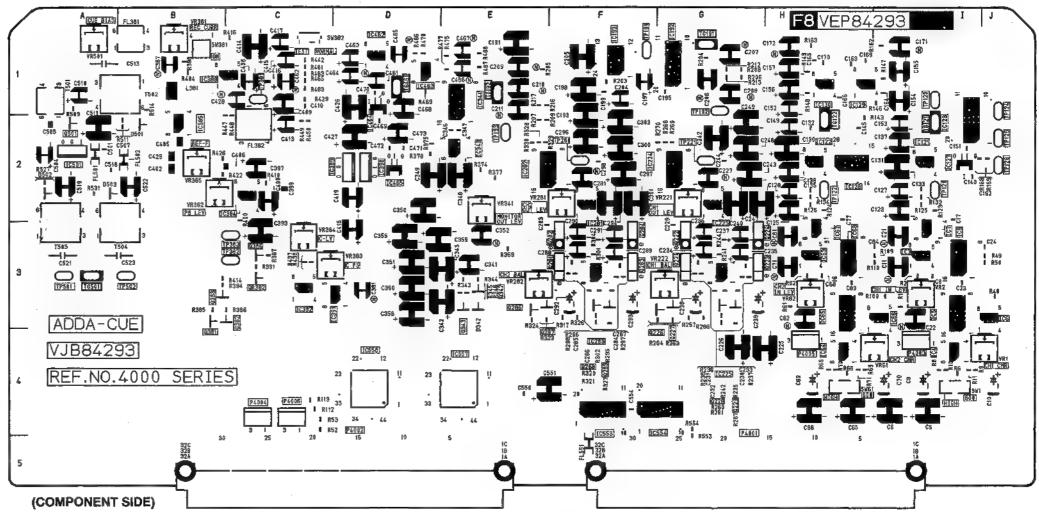


//D640P / VEP84293B: AJ-D650E/D640E)

		F8 /	ADDA								
IC4006	J-3 (©	IC4194	G-1 🕐	IC4386		Test Points		TP4502	B-3	Connectors	
IC4008 IC4061 IC4062 IC4063 IC4064 IC4065 IC4066 IC4121 IC4122 IC4123 IC4124 IC4125 IC4126 IC4126	2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	IC4195 IC4221 IC4222 IC4223 IC4224 IC4225 IC4226 IC4281 IC4283 IC4284 IC4341 IC4342 IC4343 IC4344 IC4345	E-1	IC4388 IC4399 IC4390 IC4391 IC4392 IC4461 IC4462 IC4463 IC4464 IC4466 IC4466 IC4501 IC4551 IC4553 IC4554	D-2 D-3 G-3 G-1 D-1 D-1 D-2 G-2 F-4 G-4 F-4	TG4121 TG4122 TG4191 TG4192 TG4461 TG4501 TP4120 TP4120 TP4120 TP4120 TP4124 TP4125 TP4126 TP4126 TP4191 TP4192 TP4193 TP4193 TP4221	I-2 I+2 G-1 D-1 A-3 I-2 I-1 J-1 J-2 J-2 F-1 G-1 E-2 G-2	Adjustments VR4002 VR4062 VR4062 VR4281 VR4281 VR4381 VR4382 VR4382 VR4384 VR4385 VR4501 Switches SW4001	I-3 H-3 G-2 F-2 B-1 B-2 C-3 G-3 G-3 H-2 A-1	P4001 P4002	G-5 D-5
IC4128 IC4129 IC4131 IC4191	H-1 (C) I-2 (P) F-1 (P)	IC4381 IC4382 IC4383	8-1 (P) C-1 (P) C-1 (P)	IC4555 L IC4566 IC4557 IC4558	E-4 (E-4 (TP4281 TP4381 TP4382	F-2 C-1 C-3	SW4381 SW4382	H-4 B-1 C-1	0 0	
IC4192 IC4193	G-1 © F-1 ©	IC4384 IC4385	C-2 © 5-2 ©			TP4383 TP4501	C-3 A-3				

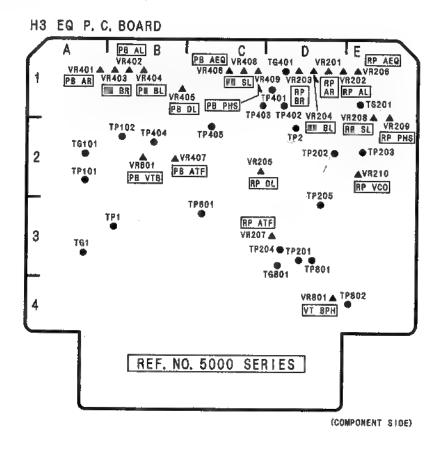






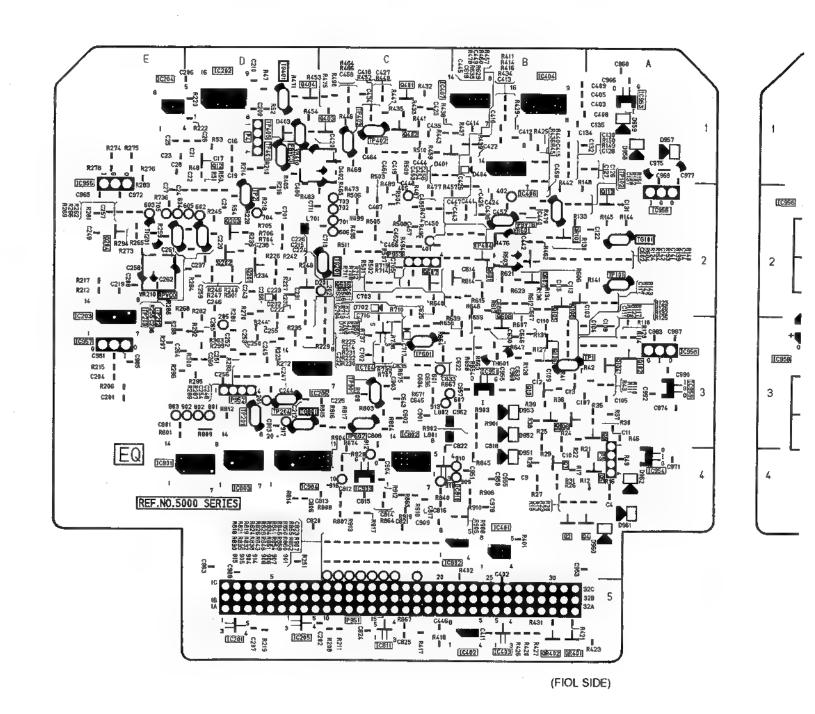
					3 400					
Transistors Q5001 Q5002 Q5003 Q5003 Q5006 Q5006 Q5006 Q5007 Q5008 Q6009 Q5010 Q5011 Q5012 Q6103 Q5104 Q5105 Q5108	A-4 © B-4 ① B-4 ② A-4 ② B-3 ① B-3 ① B-3 ② B-3 ② A-3 ② B-3 ③ B-3 ③ B-3 ③ B-3 ③ B-3 ③	Q5603 Q5606 Q5607 Q5608 Translstor-Re QR5101 QR5401 QR5402 Integrated Cl IC5101 IC5102 IC5202 IC5203 IC5204 IC5205 IC5206	B-2 ① B-5 ⑥ B-5 ⑥ B-5 ⑥ B-7 ⑥ B-7 ⑥ B-1 ⑥	IC5411 IC5601 IC5602 IC5602 IC5703 IC5704 IC5704 IC5801 IC5803 IC5805 IC5806 IC5806 IC5807 IC5808 IC5808 IC5808 IC5808 IC5808 IC5811 IC5811 IC5811	C2 © B2 © B3 © © C3 © © C2 © © C3 © C3 © C4 © C4 © C4 C4 B4 C4 C5 © C5 © C3 © C5 © C5 © C5 © C5 © C5 ©	Test Points TP1 TP2 TP101 TP102 TP201 TP202 TP203 TP204 TP401 TP402 TP403 TP404 TP406 TP601 TP602 TP801 TP802 TP801 TP802 TP801	B-3 D-2 A-2 B-2 D-3 D-2 5-2 D-3 D-1 C-1 C-1 C-1 C-1 C-3 C-3 C-3 C-3 C-3	VR5210 VR5401 VR5402 VR5403 VR5404 VR5406 VR5409 VR5410 VR5410 VR5411 VR5601 VR5801 Switch	E-2 A-1 B-1 8-1 C-1 B-2 C-1 C-1 D-1 C-2 B-2 D-1 D-3	0 0 0 0 0 0 0 0 0
Q5101 Q5102 Q5103 Q5104 Q5105	A-3 © A-3 © B-3 © A-3 © B-2 ®	IC5201 IC5202 IC5203 IC5204	D-5 ① D-1 ① E-3 ① E-1 ①	IC5806 IC5809 IC5810 IC5811	C-4 © C-4 © B-4 ① C-5 ①	TP406 TP601 TP602 TP601 TP602	D-1 C-3 C-3 D-3 © £-4 ©	Switch SW5801		
Q5402 Q5403 Q5404 Q5601 Q5602	C-1 © C-1 © 0-1 © B-2 © B-2 ©	IC5406 IC5407 IC5408 IC5409 IC5410	8-2 © C-1 © C-2 © C-1 © C-1 ©	1C5958 1C5959	A-3 A-3 Ø	VR204 VR206 VR207 VR208 VR209	D-1 © E-1 © D-3 © E-1 ©			

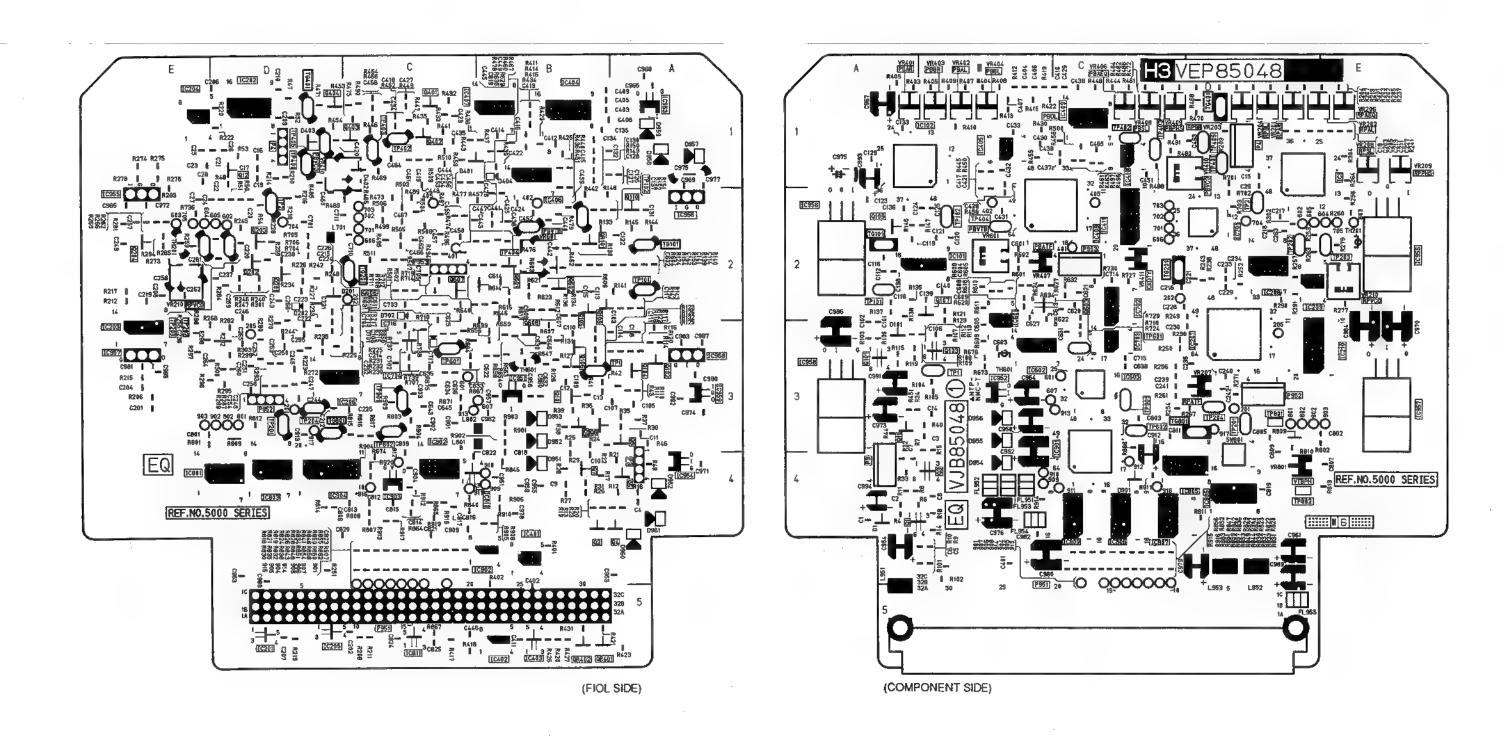
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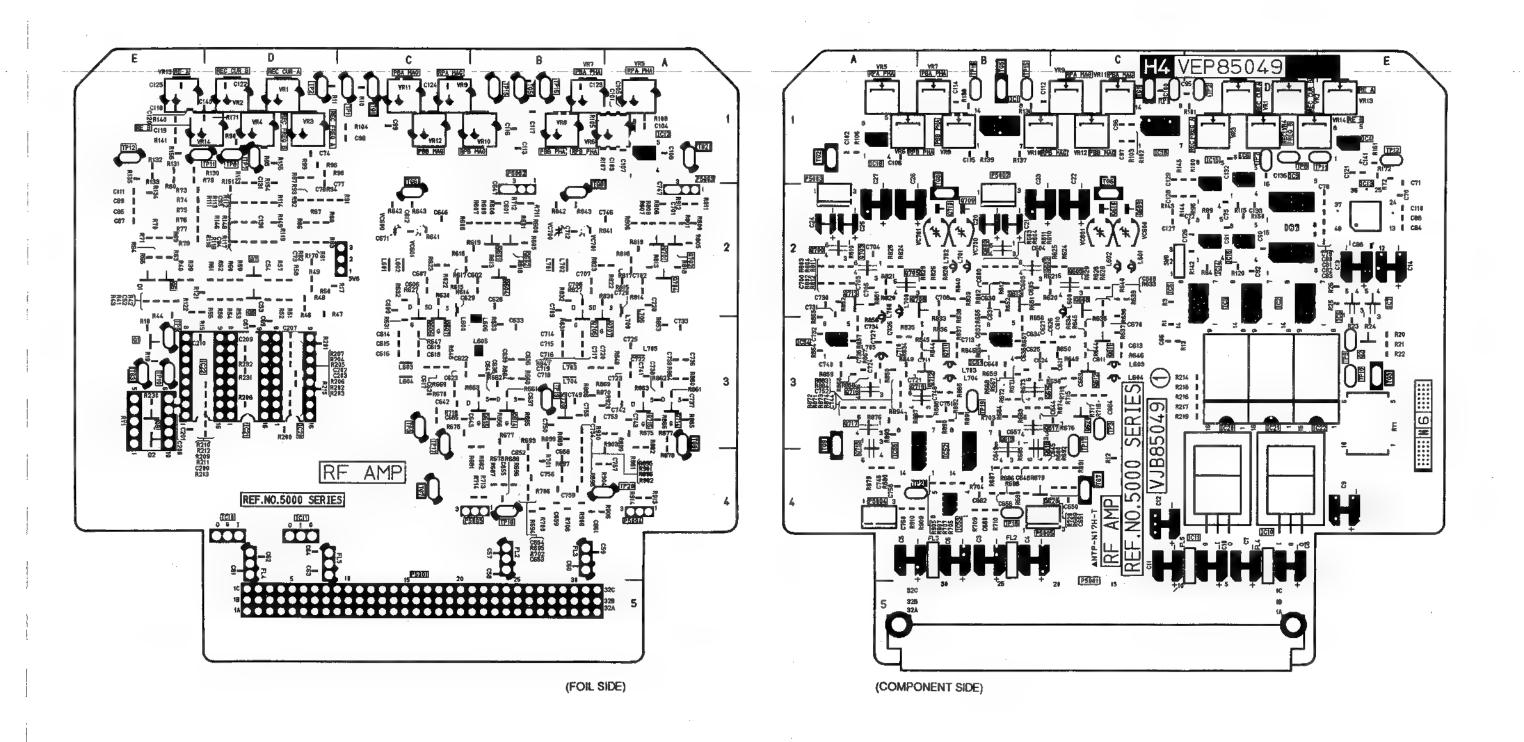


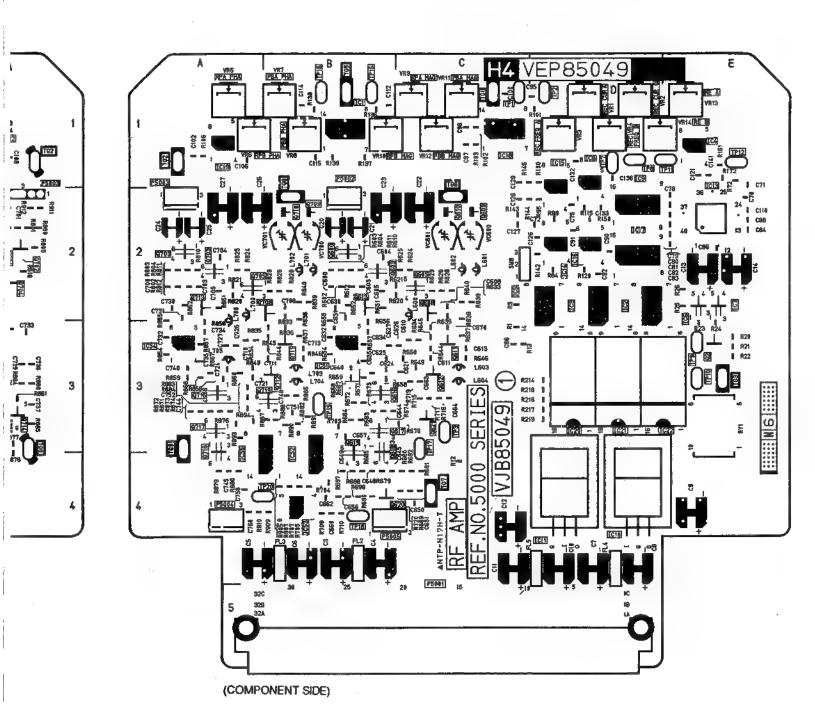
3 - 10

H3 EQ P.C. BOARD (VEP85048A)







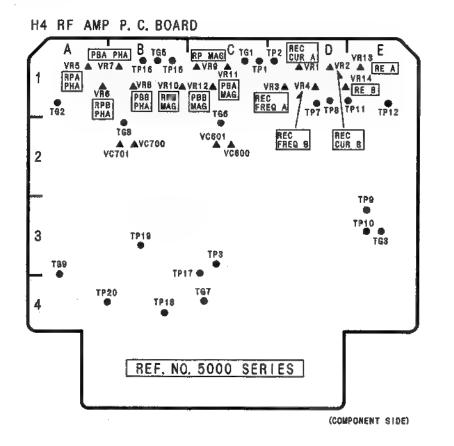


			H4 R	FAMP			
Transistors		Q5704	A-2 Ø	₹C5016	D-2 ©	TG5007	C-4
Q5001	E-3 🕑	Q5705	A-2 ©	‡C5017	D-2 ©	TG5008	B-2
Q5002	E-3 ©	Q5706	B-3 ①	IC5018	C-1 ©	TG5009	A-3
Q5005	E-2 €	Q5707	A-3 🛈	IC5019	A-1 ©	Adjustments	
Q5006	D-5 ®	Q5708	B-2 ©	IC5020	D-3		
Q5007	D-2 Ø	Q5709	B-2 ©	IC5021	D-3	VC5600	C-2 ©
Q5008	E-3 (f)	Q6710	B-2 ©	1C5022	E-3	VC5801	C-2 ©
Q5600	B-2 ©	Q5711	8-3 ©	IC5051	B-3 ©	VC5700	B-2
Q5601	B-2 (i)	Q5712	B-3 ©	IC5052	B-4 ©	VC5701	B-2 ©
Q5602	B-2 ©	Q5713	A-2 ©	IC5053	B-4 ©	VR5001	D-1
Q5803	8-2 ©	Q5714	A-3 ①	IC5054	A-3 ©	VR5002	D-1
Q5604	8-2 ©	Q5715	A-3 ①	IC5055	A-4 ©	VR5003	D-1
Q5605	C-2 ©	Q5716	A-3 ©	Test Points		™ VR5004	D-1
Q5606	C-3 (f)	Q5717	A-3 ©	1991 FURITS		VR5006	A-1
Q5607	C3 ®	Q5718	A-3 ©	TP1	C-1	VR5006	A-1
Q5608	C-2 ©	Q5719	B-3 ©	TP2	D-1	VR5007	B-1
Q5809	C-2 ©	Q5720	B-3 ©	TP3	C-3	VR5008	B-1
Q5810	C-5 @	Integrated Cir	nulla.	TP7	D-1	VR5009	C-1
Q5611	C3 ©	MICOGRATOR CIP	CHIE	TP8	D-1	VR5010	B-1
Q5612	C-3 (C)	IC5001	B-1 ©	TP9	E-3	V85011	C-1
Q5613	B-2 ©	IC5002	D-2 ©	TP†0	E-3	VR5012	C-1
		1C5003	D-2 ©	TP11	D-f	VR5013	E-1
Q5614	B-3 ®	IC5004	E-1 (C)	TP12	E-1	VR5014	E-1
Q5615	B-3 🕑	IC5005	C-2 ©	TP15	B-1	Cultab	
Q5616	8-3 ©	EC5006	E-2 ©	TP16	B-1	Switch	
Q5617	C-3 ©	IC5007	E-2 ©	TP17	G-3	SW5008	C-2
Q5618	B-3 ©	IC5008	D-1 ©	TP18	B-4	-	
Q5619	B-3 ©	IC5009	D-1 ©	TP19	B-3	Connectors	
Q5620	B-4 ©	1C5010	D-4	TP20	A-4	P5001	¢-4
Q5621	C-3 ©	3C5011	D-4	TG5001	G-1	P5002	B-1
Q5700	A-2 ©	#C5012	A-1 Ø	TG5002	A-1	P5003	A-1
Q5701	A-2 ①	IC5013	E-1 ©	TG5003	E-3	P5004	A-i
Q5702	A-2 ①	IC5014	D-2 ©	TG5005	B-1	P6005	B-4
Q5703	A-2 ©	IC5015	D-1 ©	TG5006	C-1		

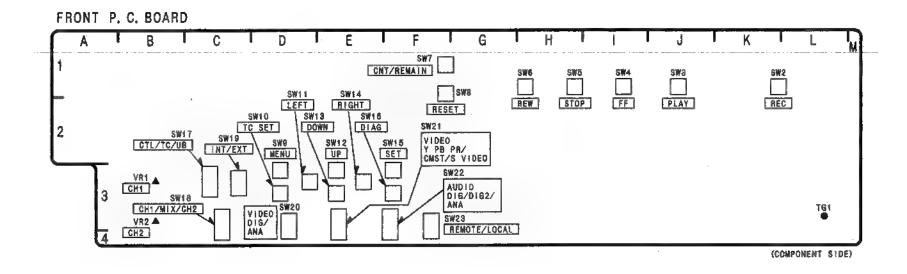
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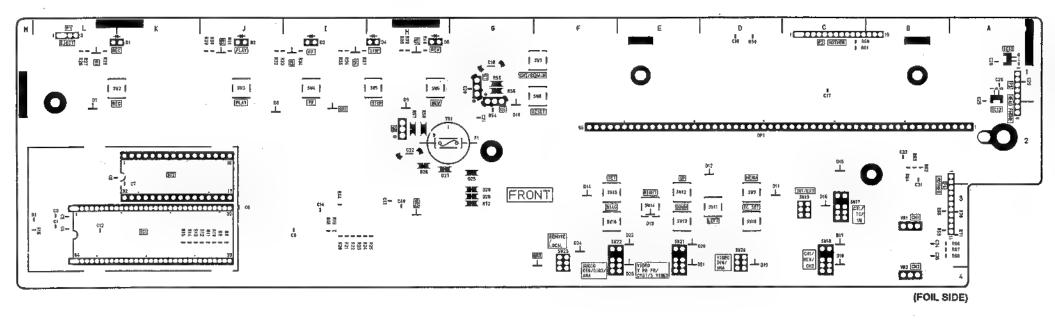


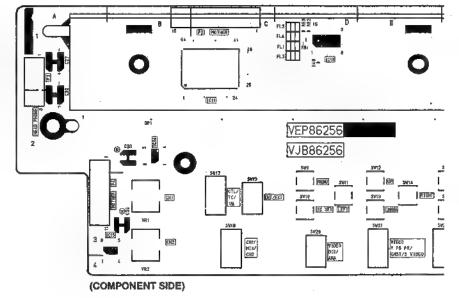
		FAC	ONT	
Translatora			Adjustments	
Q1	L-t	(F)	VFI:	B-3 .
Q2	J-1	Ð	VP2	B-4
Q3	I-1	Ø	Switches	
Q4	I-1	Ð		
Q5	H-1	Ð	SW2	K-3
Q6	G-1		SW3	J-1
Q7	G-1		SW4	I-1
Q8	H-5		SW5	H-1
Transistor-Re	sistors		SW6	H-1
	T-		SW7	F-1
QR1	1-2	Ø,	SW8	F-1
QR2		(B)	- SW9	- D-3
QR3	F-3	ூ	SW10	0-3
Integrated Cir	cults		SW11	D-3
	_		SW12	E-3
IC1	K-3		SW13	E-3
IC2	K-2		SW14	E-3
IC2			SW15	F-3
IC3	L-2	© ©	SW16	F-3
IC4	L-3	(C)	SW17	C-2
IC5	J-3	0	SW18	C-3
IC6	1-3	©	\$W19	C-3
IC7	J-3	(C)	SW20	D-3
IC8	H-3	© ©	SW21	E-3
IC9	1-3	© :	\$W22	F-3
IC10	D-1	0	SW23	F-3
IC11	C-1	(C)	Connectors	
IC12	A-1	Ð	P1	T
IC13	A-1	Ø		L-1
1014	8-2	(C)	P2	G-1 A-1
IC15	8-3	(E)	P3	A-1 A-3
Test Point			F*	A-3
TG1	L-3	@		

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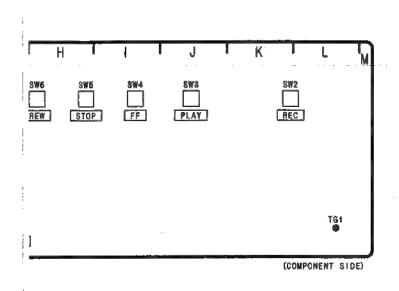
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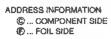


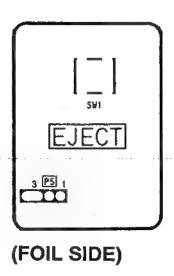


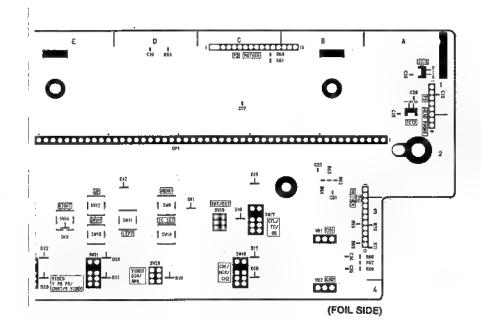
EJECT P.C. BOARD (VEP80A09A)

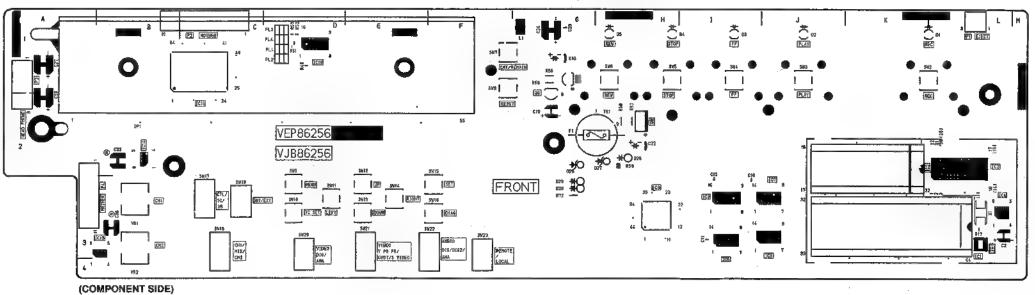


		FRO	ONT	
ransistors			Adjustments	
Q1	L-1	ூ	VR1	B-3
Q2	J-1	Ð	VR2	Ì B-4
Q3	1-1	8888	Switches	
Q4	I-1	®.	*****	1
Q6 '	H-t	Ø	SW2	K-1
Q6	G-1		SW3	J-1
Q7	G-1		SW4	I-1
38	H-2		SW5	H-1
nsistor-Res	intore		SW6	H-1
	10101-0		SW7	F-1
QR1	I-2	Ð	SW8	F-1
QR2	-H-3	Ð	SW9	D-3
QR3	F-3	⅌	SW10	D-3
egrated Circ	ults.		SW11	D-3
			\$W12	E-3
IC1	K-3		SW13	E-3
IC2	K-2		SW14	E-3
IC2			SW15	F-3
IC3	L-2	(2)	SW16	F-3
IC4	L-3	0	\$W17	C-2
IC5	J-3	0	SW18	C-3
IC6	1-3	0	SW19	C-3
IC7	J-3	©	SW20	D-3
IC8	H-3	©	SW21	E-3
IC9	1-3	0	SW22	F-3
IC10	D-1	0	SW23	F-3
IC11	C-1	©	Connectors	
IC12	A-1	Ø		_
IC13	A-1	(F)	P1	L-1
IC14	B-2	ନ ଉଚ୍ଚ ବ୍ର ବ୍ର ବ୍ର ବ୍ର ବ୍ର ବ୍ର ବ୍ର	P2	C-1
C15	B-3	©	P3	A-1
est Point			P4	A-3
FG1	L-3	©		

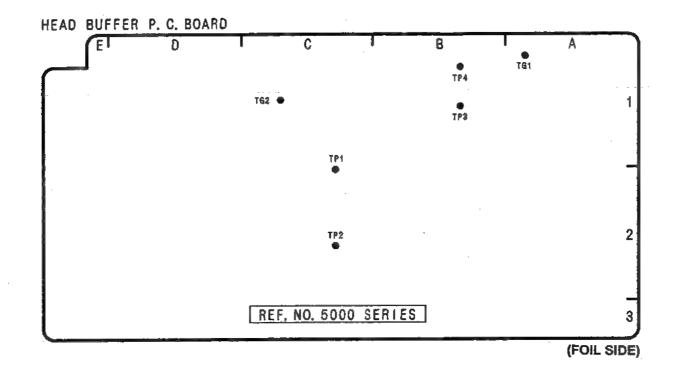








HEAD BUFFER P.C. BOARD (VEP85151A)

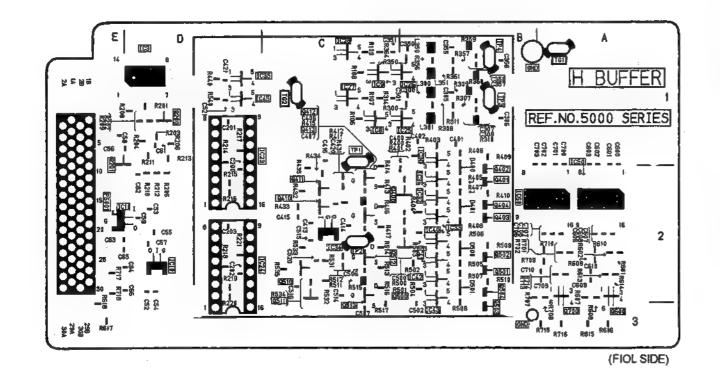


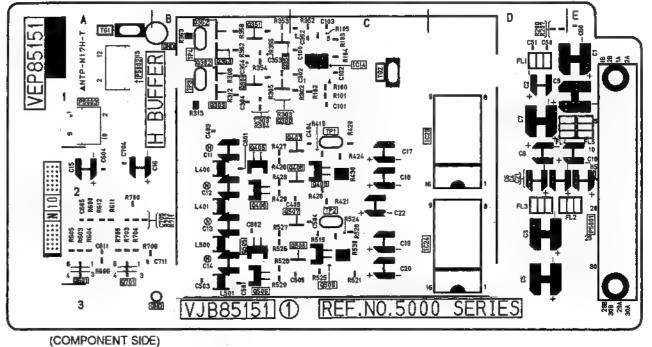
HEAD BUFFER					
Transistors		integrated Circ	cuite		
Q5200	D-1 🕞	1C5003	D-1 🕞		
Q5201	E-1 🛈	1C5008	C-1 €		
Q5300	B-1 ©	∮C5009	C-1 ®		
Q5301	.B-1 ©	€C5010	D-2 ©		
Q5302	B-1 ©	₹C5011	E-2 🕝		
Q5303	B-1 ◎	JC5014	C-1 ©		
Q8350 .	B-1 ©	IC5023	D-1		
Q5351	B-1 ©	FC5024	0-2		
Q5352	B ⋅1 ©	IC5025	B-1 (r)		
Q6353	B-1 ©	IC5026	B-1 €		
Q5400	C-2 🖲	IC5027	C-1 ①		
Q5401	B-2 ®	1C5028	C-1 🕑		
Q5402	- · · B-2 ◆	IC5030	8-1 🕑		
Q5403	B-2 🗈	IC5031	B-2 ®		
Q5404	B-2 🕤	105032	B-2 🕦		
Q5405	B-2 ©	IC5033	B-2 🕖		
Q5406	B-2 ©	IC5034	C-2 (F)		
Q5407	C-1 ©	IC5035	Ð-1 €		
Q5408	C-2 ©	IC5040	8-2 Ø		
Q5409	C-2 ©	IC5041	8-2 D		
Q5410	C-2 ©	IC5042	B-2 🕦		
Q5411	C-2 ①	105043	B-3 Ø		
Q5412	C-1 (F)	IC5045	D-1 🕦		
Q5413	C-1 (f)	IC5050	A-2 🐑		
Q5500	C-5 D	IC5080	B-2 €		
Q5501	B-2 🗊	Test Points			
Q5502	B-2 🕑	V			
Q5503	B-3 ①	TP: .	C-1		
Q5504	B-2 🕑	TP2	C-2		
Q5505	B-2 ©	TP3	8-1		
Q5506	B-3 ©	TP4	B-1		
Q5507	C-2 ©	TG5001	A-1		
Q5508	C-2 ©	TG5002	G-1		
Q5509	C-2 ©	Connectors			
Q5510	C-2 ®				
Q5511	C-2 ®	P5001	E-2		
Q5512	C-2 ①	P5002	A-1 ©		
Q5513	C-3 (D	P5003	A-1 ©		
Q5600	A-3 ①				
Q5601	A-2 ©				
Q5700	A-3 Ø				
Q5701	A-2 ©		1		

ADDRESS INFORMATION

⑤... COMPONENT SIDE

⑥... FOIL SIDE





3-13

POWER 1 P.C. BOARD (VEP81074A: AJ-D650/D640/D650P/D640P / VEP81074B: AJ-D650E/D640E)

JAPAN ONLY

▲ 警告



AC100Vの加わっている活電部(充電部、活電部)に 直接触れないでください。

■ 感電ややけどの可能性があります。

① 🕂 警告

▲ 印の部品は安全上重要な部品です。 交換するときは、安全および性能維持の ため必ず指定の部品をご使用ください。

- ② 内は充電部です。AC 100V が加わっておりますので点検、修理のときは感電しないよう充分ご注意ください。
- ③ 部品交換時には、電源プラグをぬいてから行ってください。
- ④ 一次側(充電部)の電圧・波形は、一次側アースを基準に測 定してください。
- ⑤ 部品品番は、部品価格表で確認の上交換ください。

CAUTION

THE MARK INDICATES THE PRIMARY CIRCUIT TO DISTINGUISH THE PRIMARY FROM THE SECONDARY CIRCUIT.

PAY ATTENTION NOT TO RECEIVE AN ELECTRIC SHOCK DURING REPAIR AND SERVICE OF THE PRODUCTS.

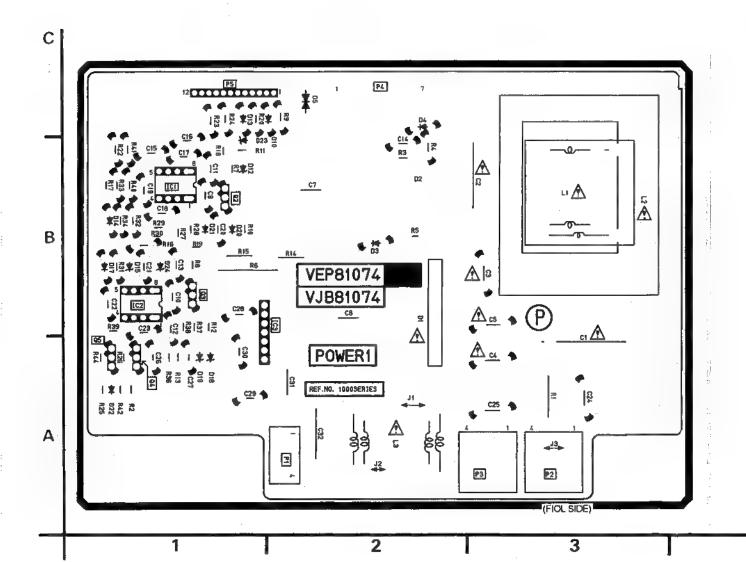
IMPORTANT SAFETY NOTICE:

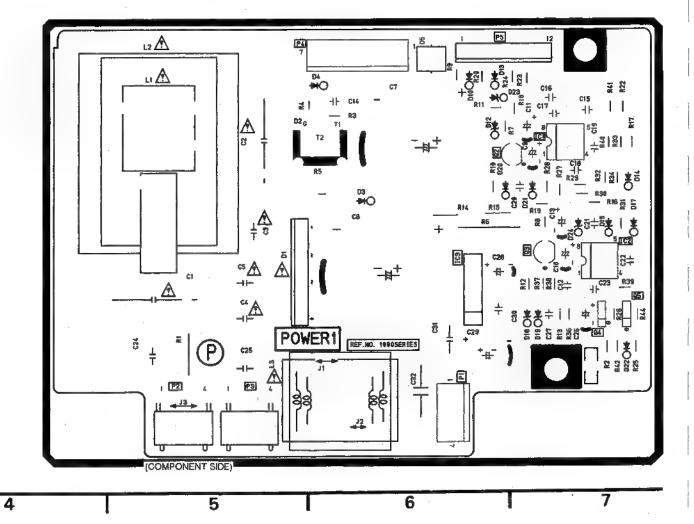
COMPONENTS IDENTIFIED WITH THE MARK A HAVE THE SPECIAL CHARACTERISTICS FOR SAFETY.

WHEN REPLACING ANY OF THESE COMPONENTS, USE ONLY THE SAME TYPE.

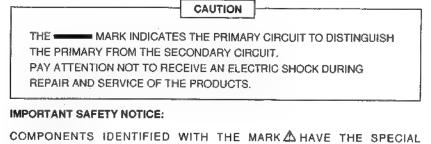
POWER 1	
Transistors	
Q2	\$-6, B -1
Q3	B-7, B-1
Q4	A-7, A-1
Q5	A-7, A-1
Integrated Circ	uits
IC1	B-7, B-1
. IG2	B-7, B-1
IC3	B-6, B-1
Connectors	
P1	A-6, A-2
P2	A-5, A-3
P3	A-5, A-3
P4	C-5, C-2
P6	C-6, C-1

ADDRESS INFORMATION

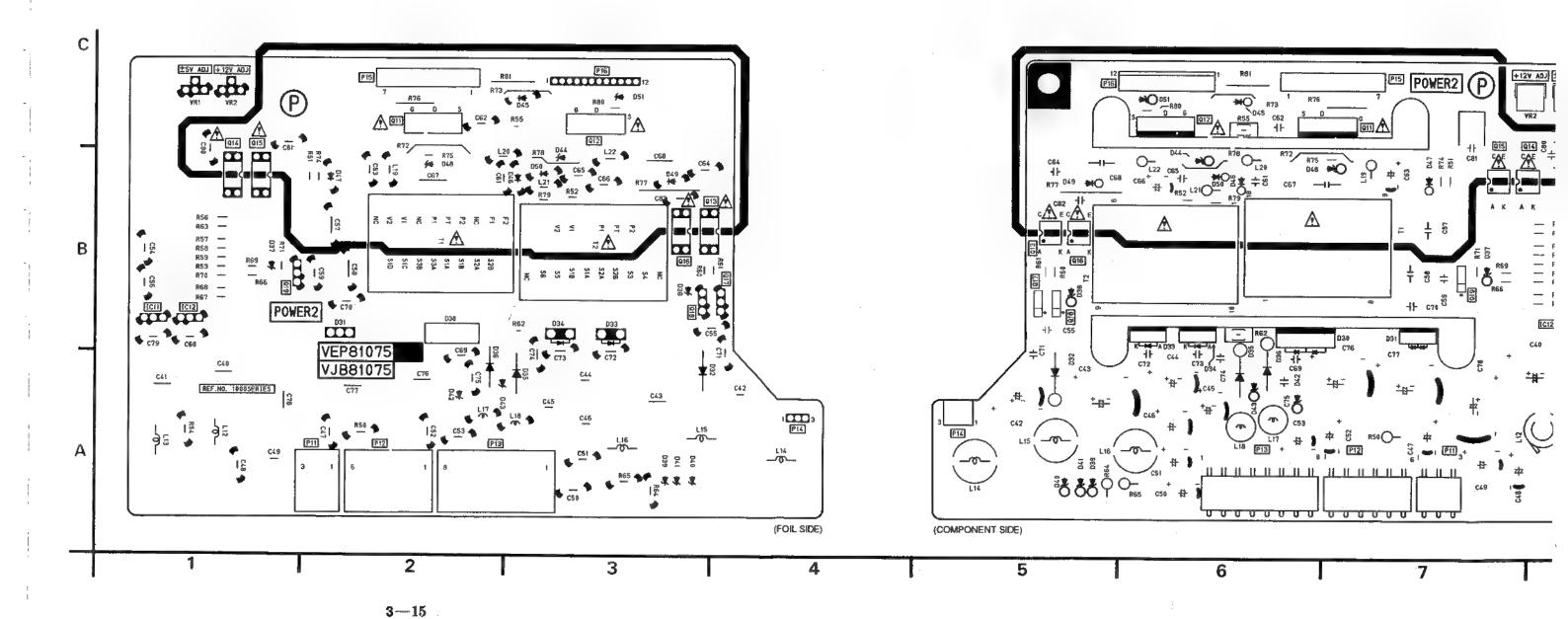




POWER 2 P.C. BOARD (VEP81075A: AJ-D650/D640/D650P/D640P / VEP81075B: AJ-D650E/D640E)



CHARACTERISTICS FOR SAFETY.
WHEN REPLACING ANY OF THESE COMPONENTS, USE ONLY THE SAME TYPE.



40P / VEP81075B: AJ-D650E/D640E)

要な部品です。 よび性能維持の 更用ください。 ておりますの 意ください。 てください。 スを基準に測

CAUTION

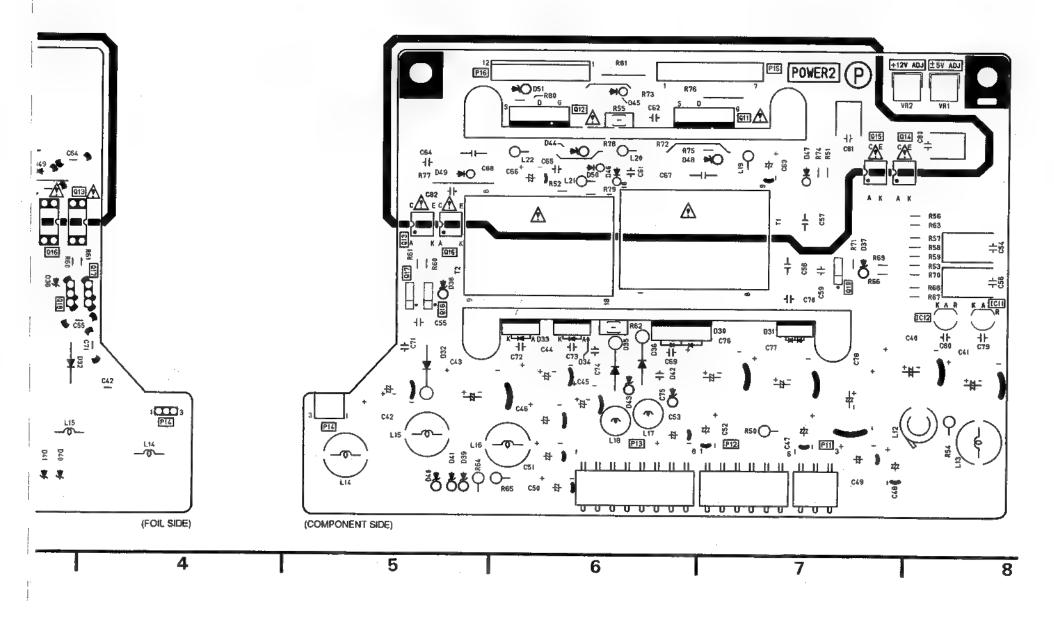
THE PRIMARY FROM THE SECONDARY CIRCUIT TO DISTINGUISH THE PRIMARY FROM THE SECONDARY CIRCUIT.

PAY ATTENTION NOT TO RECEIVE AN ELECTRIC SHOCK DURING REPAIR AND SERVICE OF THE PRODUCTS.

IMPORTANT SAFETY NOTICE:

COMPONENTS IDENTIFIED WITH THE MARK Δ HAVE THE SPECIAL CHARACTERISTICS FOR SAFETY.

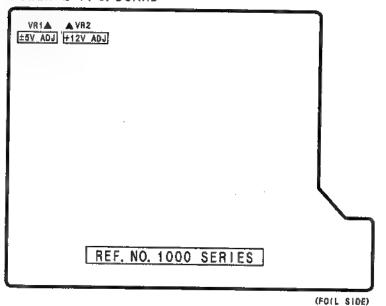
WHEN REPLACING ANY OF THESE COMPONENTS, USE ONLY THE SAME TYPE.

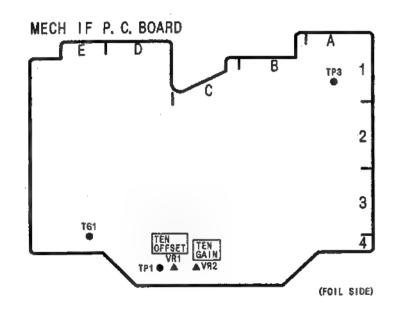


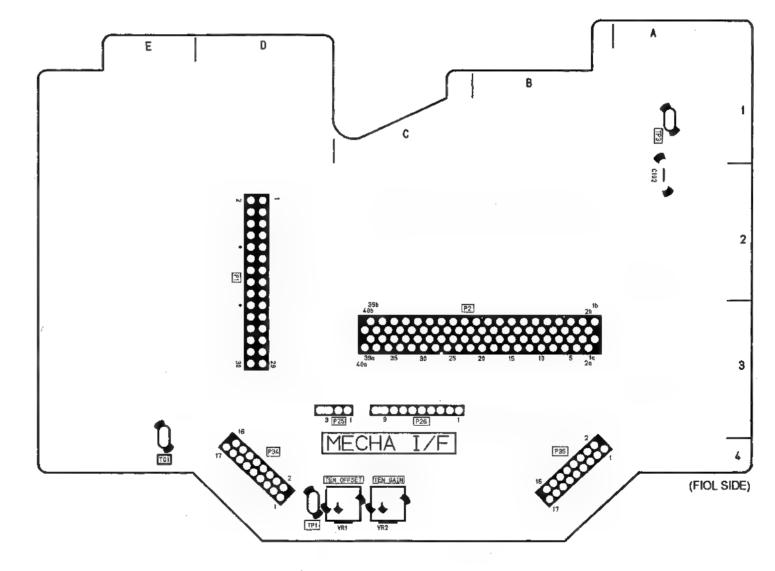
POWER 2				
Transletors				
Q11	C-7, C-2			
Q12	C-6, C-3			
Q13	B-6, B-3			
Q14	C-8, B-1			
Q15	C-7, B-1			
Q16	B-6, B-3			
Q18	B-5, B-3			
Q19	8-7, B-1			
Integrated Circuits				
IC11	B-8, 8-1			
IC12	B-8, B-1			
Adjustments				
VR1	C-8, C-1			
VR2	C-7, C-1			
Connectors				
Pt1	A-7, A-2			
P12	A-7, A-2			
P13	A-6, A-2			
P14	A-5, A-4			

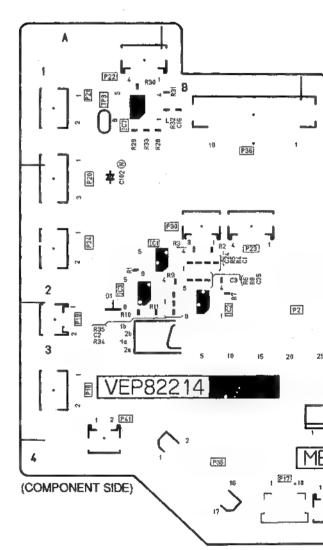
ADDRESS INFORMATION

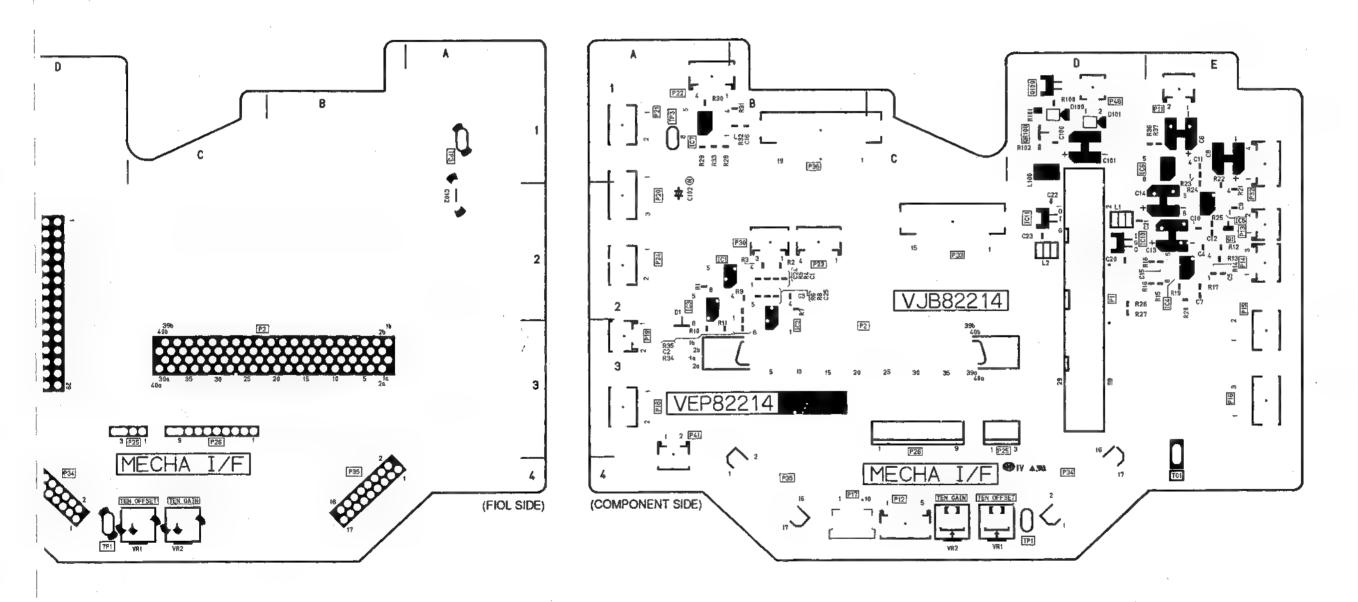
POWER 2 P. C. BOARD











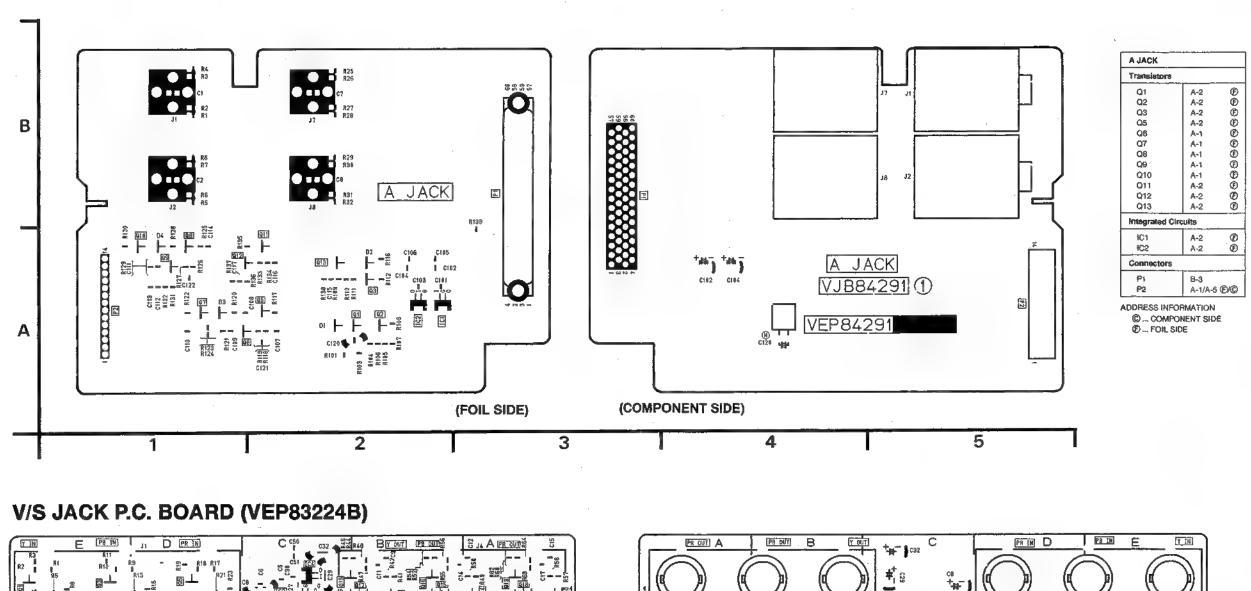
MECH VF		
Transistors		
Q1	E-2	0
Q100	D-1	©
Transistor-Res	istor	
QR100	D-1	©
Integrated Circ	ults	
IC1	A-2	©
IC2	B-2	0
IC3	A-2	0
IC4	€-2	0
IC5	E-2	(a)
IC6	Ð-1	Ö
IC10	D-2	Ö
1C11	D-5	©
Test Points		
TP1	D-4	
TP3	A-1	
TG1	E-4	
Adjustments		
VRt	C-4	
VR2	Ç-4	
Connectors		
. P1	D-2	
P1 P2	C-3	
P1 P2 P11	C-3 E-1	©
P1 P2	C-3 E-1 C-4	0
P1 P2 P11 P12 P13	C-3 E-1	0
P1 P2 P11 P12 P13 P14	C-3 E-1 C-4 E-2 E-2	0
P1 P2 P11 P12 P13	C-3 E-1 C-4 E-2 E-2 E-2	999
P1 P2 P11 P12 P13 P14 P15 P16	C-3 E-1 C-4 E-2 E-2 E-3	00000
P1 P2 P11 P12 P13 P14 P15	C3 E-1 C-4 E-2 E-2 E-3 E-3 B-4	000000
P1 P2 P11 P12 P13 P14 P15 P16	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3	0000000
P1 P2 P11 P12 P13 P14 P15 P16 P17	C3 E-1 C-4 E-2 E-2 E-3 E-3 B-4	000000
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-3 A-2	0000000
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-2 A-1	0000000
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-2 A-1 A-1	90999999
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-2 A-1 B-2	ତ ବ୍ରତ୍ତତ୍ୱତ୍ୱତ୍ତତ
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-2 A-1 A-1 B-2 A-2	90999999
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25	G-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-2 A-1 B-2 C-3	ତ ବ୍ରତ୍ତତ୍ୱତ୍ୱତ୍ତତ
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-2 A-1 B-2 A-2 C-3 C-3	ତ ବ୍ରତ୍ତତ୍ୱତ୍ୱତ୍ତତ
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26 P30	C-3 E-1 C-4 E-2 E-2 E-3 A-3 A-3 A-2 A-1 A-1 B-2 A-2 C-3 B-2	ଉତ୍ତ ବ୍ରହ୍ମ ବ୍ରହ
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26 P30 P32	G3 E-1 G-2 E-2 E-3 B-3 A-3 A-3 A-1 B-2 A-1 B-2 G-3 G-3 G-3 G-3 G-3 G-3 G-3 G-3 G-3 G-3	ଓ ଉତ୍ତ ବୃତ୍ତତ୍ୱର୍ଗତ
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26 P30 P32 P33	G3 E-1 G-2 E-2 E-2 E-3 B-4 A-3 A-3 A-1 B-2 C-3 B-2 C-3 B-2 C-3 B-2 C-3 B-2 C-3 B-2 C-3 B-2 C-3 B-2 C-3 B-2 B-2 B-2 B-2 B-2 B-3 B-4 B-2 B-2 B-2 B-2 B-2 B-3 B-3 B-3 B-3 B-3 B-3 B-3 B-3 B-3 B-3	ଉତ୍ତ ବ୍ରହ୍ମ ବ୍ରହ
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26 P30 P30 P312 P33 P34	C-3 E-1 C-4 E-2 E-2 E-2 E-3 B-4 A-3 A-2 A-1 B-2 A-1 B-2 C-3 C-3 C-3 B-2 E-2 D-4	ଓ ଉତ୍ତ ବୃତ୍ତତ୍ୱର୍ଗତ
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26 P30 P312 P33 P34 P35	C-3 E-1 C-4 E-2 E-2 E-3 A-3 A-2 A-1 A-1 B-2 A-2 C-3 B-2 C-3 B-2 D-4 B-4	00 0000000000000000000000000000000000
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26 P30 P31 P32 P33 P34 P35	C-3 E-1 C-4 E-2 E-2 E-3 B-4 A-3 A-2 A-1 B-2 C-3 B-4 C-2 D-4 B-1	0 00 00 0000000000000000000000000000000
P1 P2 P11 P12 P13 P14 P15 P16 P17 P18 P19 P20 P21 P22 P23 P24 P25 P26 P30 P312 P33 P34 P35	C-3 E-1 C-4 E-2 E-2 E-3 A-3 A-2 A-1 A-1 B-2 A-2 C-3 B-2 C-3 B-2 D-4 B-4	00 0000000000000000000000000000000000

ADDRESS INFORMATION

© ... COMPONENT SIDE

Ø ... FOIL SIDE

A JACK P.C. BOARD (VEP84291A: AJ-D650P/D640P/D650E/D640E / VEP84291B: AJ-D650/D640)



(FIOL SIDE)

3 - 17

(COMPONENT SIDE)

Hanaiatora		
Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8 Q9 Q10 Q11 Q12 Q13 Q14 Q15 Q16 Q17 Q18 Q19 Q20 Q21 Q22 Q23 Q24 Q25 Q26 Q27 Q26 Q27 Q26 Q27 Q26 Q27 Q26 Q202 Q202 Q202 Q203 Q204 Q205 Q206 Q207 Q206 Q207 Q208 Q209 Q209 Q209 Q209	E-1 E-1 D-1 D-1 D-1 E-2 D-2 D-2 D-2 D-1 B-1 A-1 B-1 A-1 A-1 B-2 C-2 B-2 B-2 B-2 B-2 C-2 B-2 B-2 C-2 B-2 B-2 B-1 C-1 C-1 B-1 C-1 B-1 C-1 B-1 C-1 B-1 B-1 C-1 B-1 B-1 B-1 B-1 B-1 B-1 B-1 B-1 B-1 B	ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ ଓ
Q210	-C-3	(
Q211	C-2	Ø
Q212	C-2	Ø
Integrated Circ		
IC1 IC2 IC3 IC4 IC5 IC6 IC201 IC202 IC203 IC205 IC206 IC206 IC206 IC207 IC208 IC209 IC210 Switches SW1	C1 C1 C2 C2 D4 E3 E3 E3 E3 E3 E3 E3 E3 E3 E3 E3 E3 E3	00000000000000000
Connector	0-2	_
P1	A-3	_
F)	A-3	

V/S JACK

Transistors

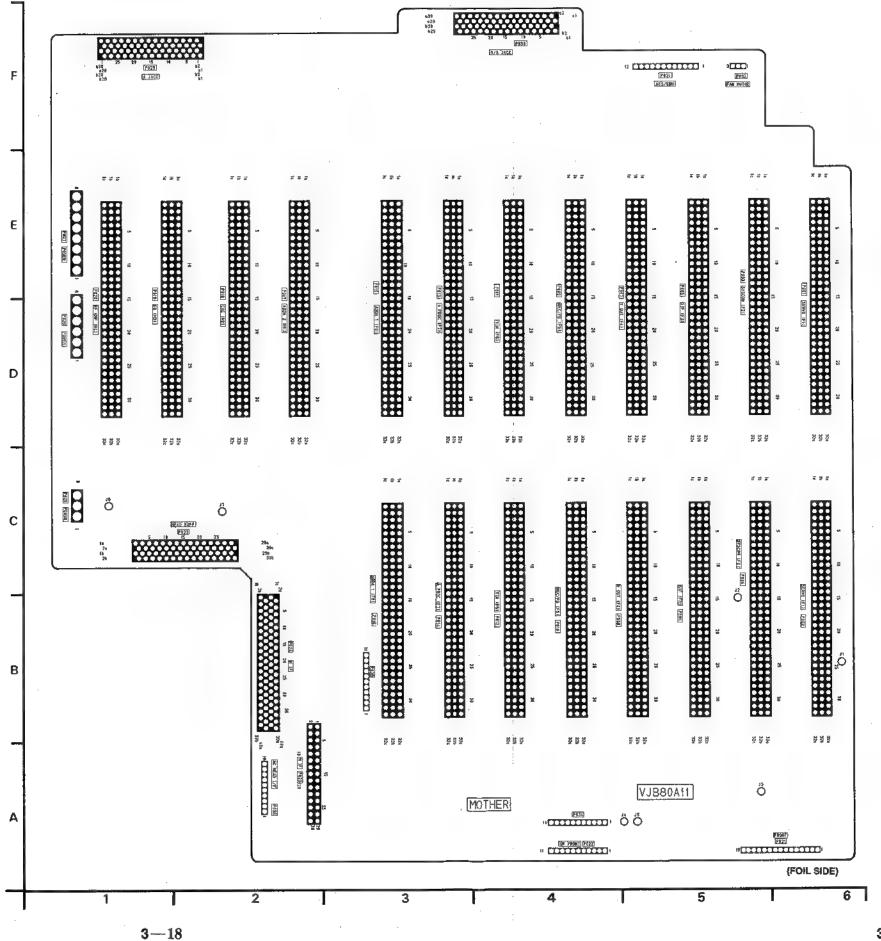
ADDRESS INFORMATION

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3 - 17

MOTHER P.C. BOARD (VEP80A11A)

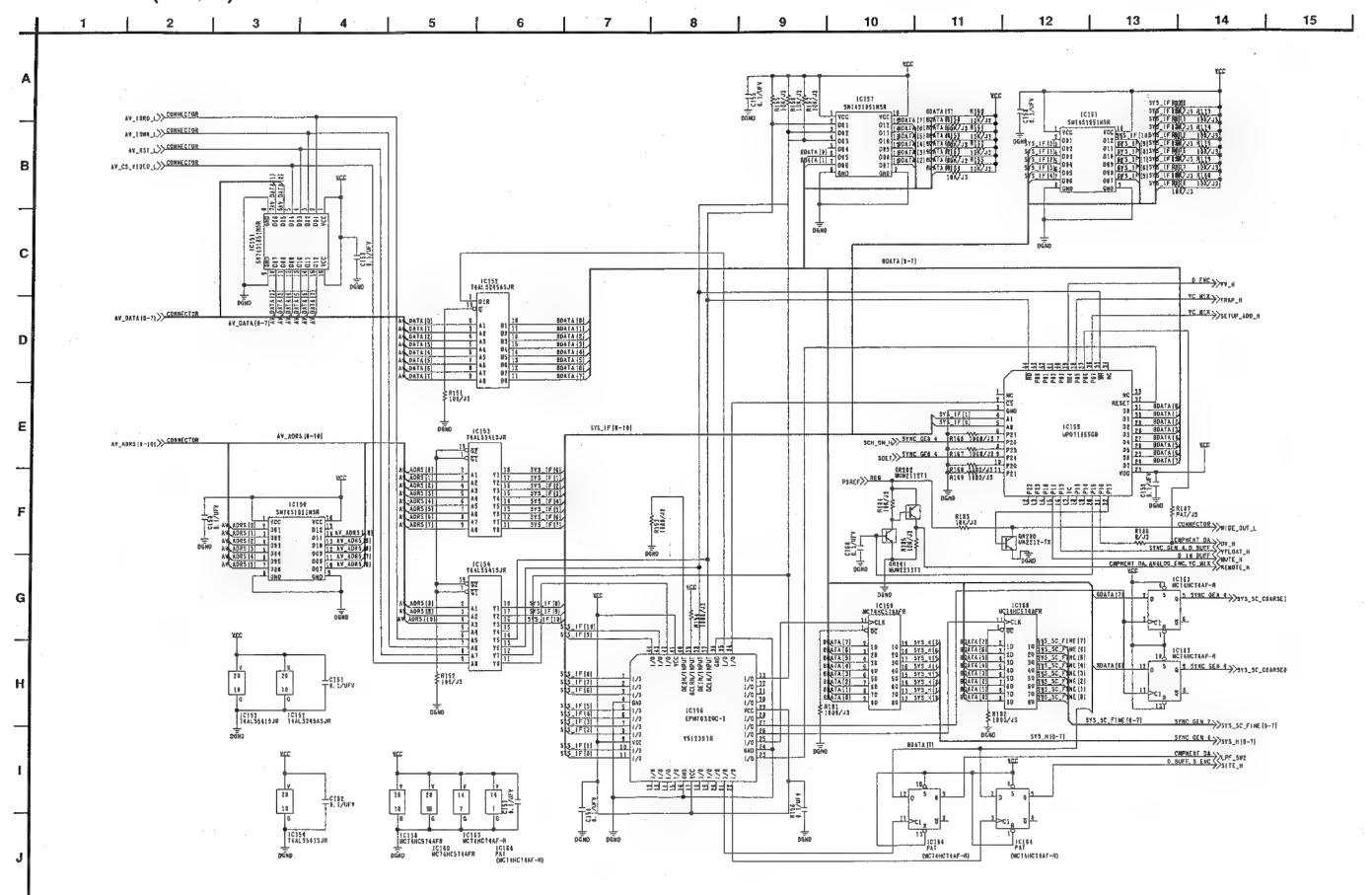


MOTHER	
Connectors	
P010	C-4
P011	E-4
P012	C-4
P013	E-3
P014	C-3
P015	E-3
P016	C-3
P019	E-1
P020	E-1
P02f	A-6
P022	A-4
P023	A-2
P024	B-2
P025	C-1
P026	D-1
P027	E-1
P029	F-1
P030	F-4
P031	F-5
P032	F-5
P033	C-2
P036	A-2
P001	E-6
P002	C-6
P003	E-6
P004	C-5
P005	Ë-5
P006	C-6
P007	E-5
P008	C-5
P009	E-4
P009	E-4

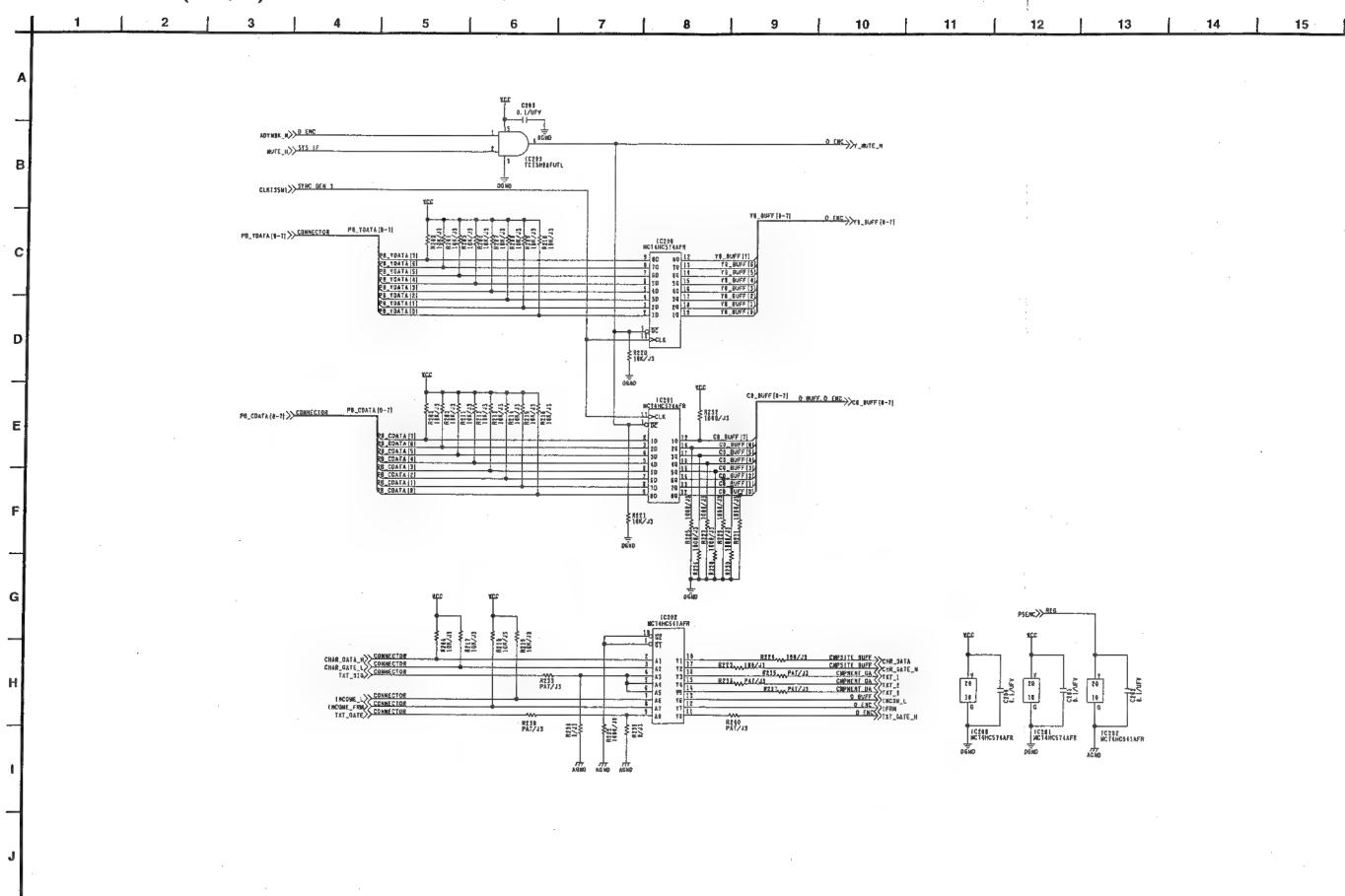
ADDRESS INFORMATION

VOUT CONNECTR (F4 1/16) SCHEMATIC DIAGRAM 13 14 15 12 10 11 9 PIZY TO BEG WLPOISS-T MISAC LO BEC AND ATENDERS PBV 10 REG VLP4|31-7 HEV TO REG SH VLP9 12 - 1 YC_OUT_M (0-9) >> FROM 0 OUT BUFF YC_0UT_N [0-9] YC_GUT_P(F-B) >> FROM G GUT BUFF 10,00T_P[6-9] DEND PS A1614248678 PL VJP34518696 P1 YJP34548856 P2 VJP14548498 P| YJP14548016 | VD14568098 | 16 SUT N 101 | 18 SUT N 101 | 18 SUT N 101 | 19 SUT N 101 | 18 SUT V.P36548008 1C SMW 2C YC OVI P111 3C YC OVI N121 4C YC OVI P141 5C YC OVI P131 6C YC OVI P131 18 GND 18 YC OUT MIST 18 YC OUT PIST 18 YC OUT PIST 18 5MD 26 5124 38 -124 48 57.54 -58 -1.5V 1C GNQ 2C +12V 3C -12V 4C +1.5V AC ONL N [8] YC QUT PIB) YC QUT MIST YC QUT NIST 1A GAD 2A +12Y AC ONE (1) D AA 112Y SA 11.5Y SA 11.5 34 -12V FB YC OUT NIET 11C F_BUFF (FROM D_GUT_SUFF) 11C REF_HST_N (FROM D_GUT_SUFF) 11C REF_HST_N (FROM D_GUT_SUFF) 11C COMP_SYNC_L (FROM D_GUT_SUFF) 11C (FROM D_OUT_BUFF) 128 YOR | 1 118 YOR | 7 14A 15A H SYNC L 16A 17A 18A 19A (12C DATA) 28A 118 SET_UP 188 SYS H PHASES 118 SYS SC CONSEZ 218 TIC CLO PB COATA [6] PB COATA [5] PB COATA [6] PB COATA [6] PB YDATA [4] PB YDATA [4] ZIA C_SYNC PB CDATA(2) PB CDATA(3) PB CDATA(4) PB CDATA(4) PB CDATA(4) PB CDATA(4) 218 AY CS YIOSO 1 (5) 218 AY ADR3 (8) 218 AY ADR3 (8) 218 AY ADR3 (9) 218 AY ADR3 (15) 218 AY ADR3 (17) 218 AY ADR3 (17) 218 AY OATA (11) 218 AY OATA (11) 218 AY OATA (11) 219 AY OATA (11) ZIA 22A PE CDATA (IL ZIA PE CDATA (IL ZIA PE YDATA (IL ZIA PE YDATA (IL ZIA PE YDATA (IL ZIA RE FRM ZIA RUT FRM JOAL AK ADRS [5] AK ADRS [5] AK DATAITI AK DATAISI AK BATAISI >>-30A 31A 32A GND 34H 31B 32B 6MD 31C -59 32C GHO WLP6131-T LLG5 PS_CDATA [4-1] (10 D IN BUFF) AY .AUR\$ (0-10) AV_ADRS [8-14] (10. SHEET? 28_Y0ATA (0-1) AV_DATA (0-1) < TO SHEETE AY_DATA [8-1] P8_YDAFA (8-1) < 10 0 18 BUFF C156 18427/EW C181 6.17/EV TG100 TJRSE48

V OUT SYS IF (F4 2/16) SCHEMATIC DIAGRAM



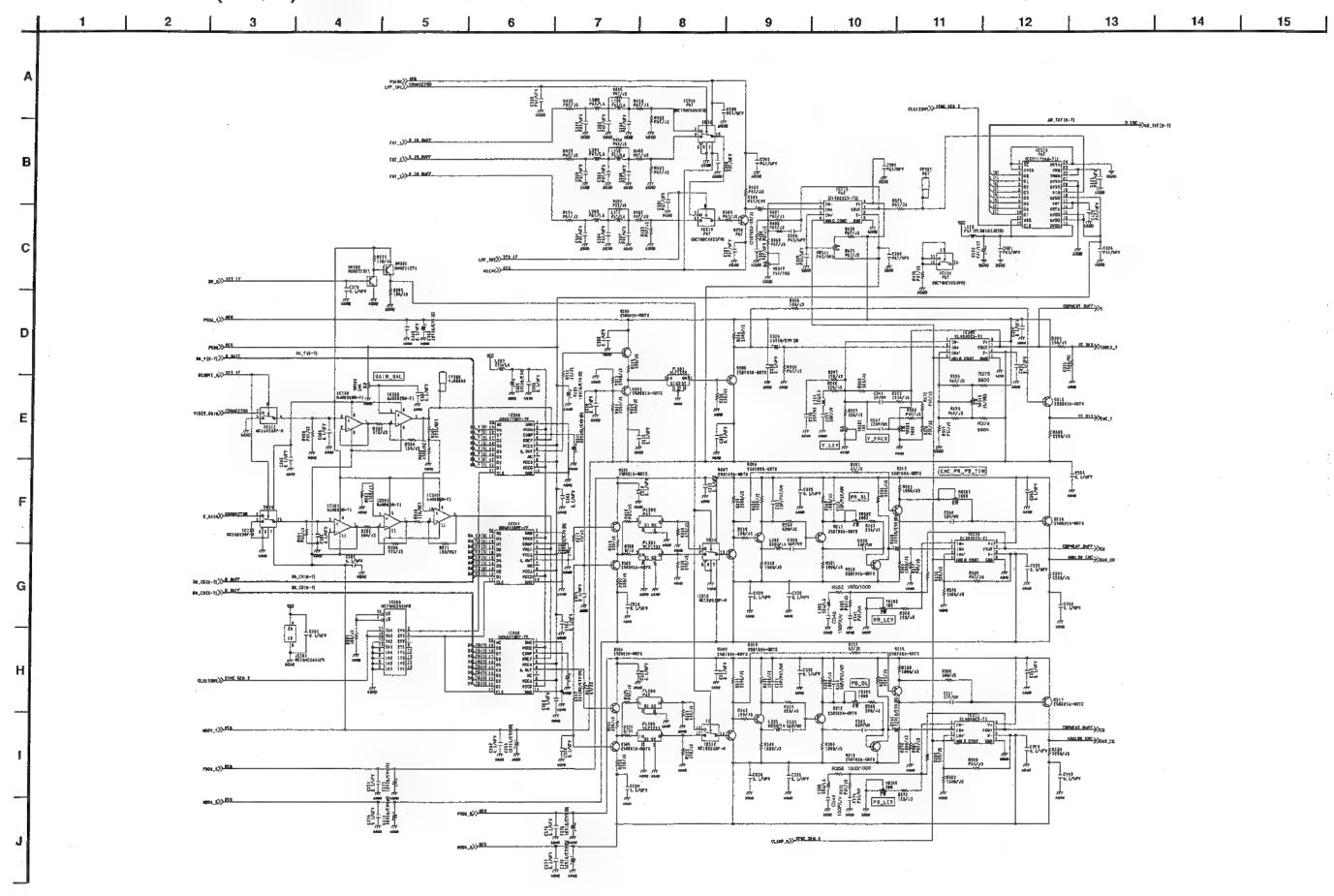
V OUT D IN BUFF (F4 3/16) SCHEMATIC DIAGRAM



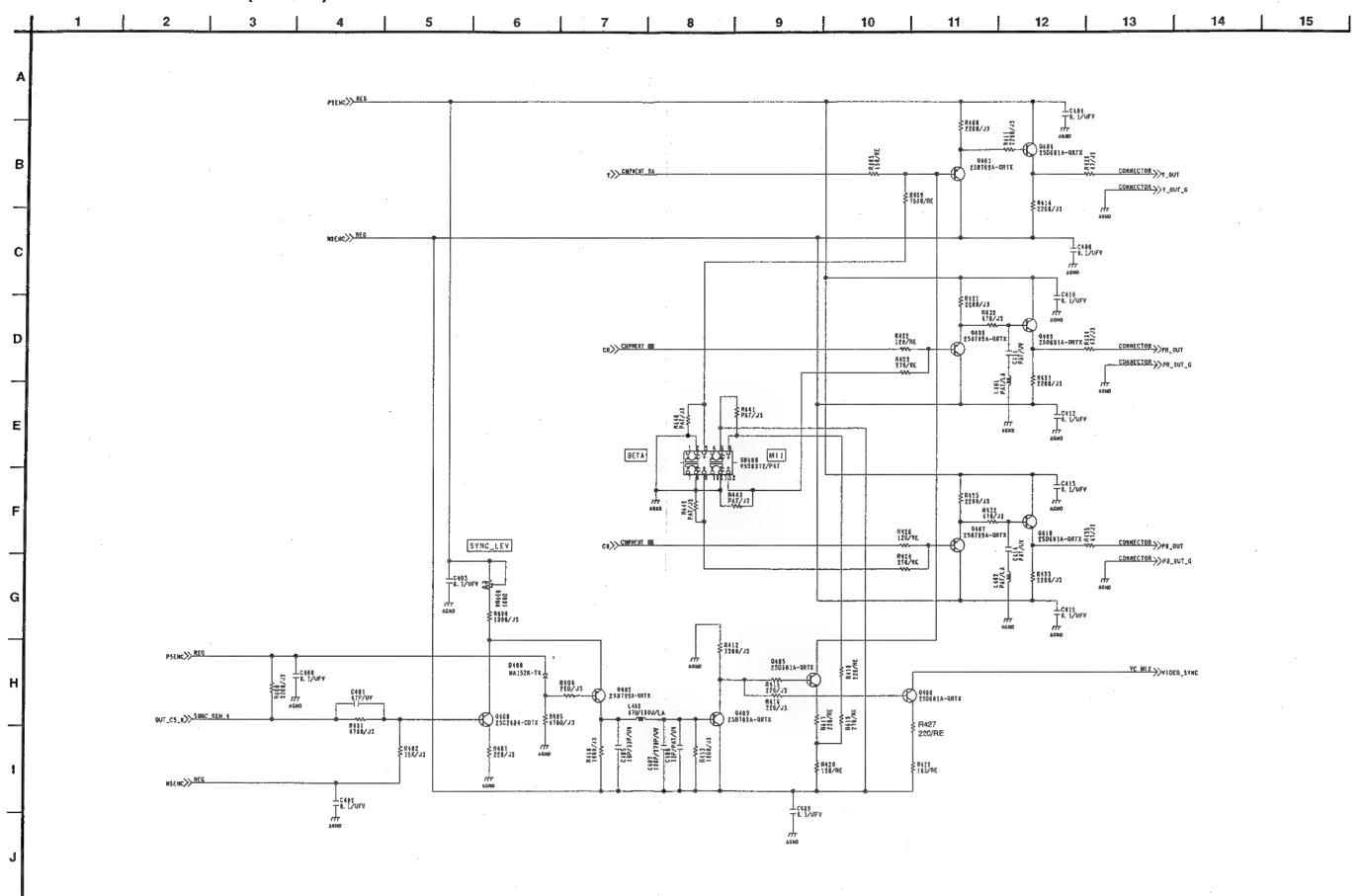
2-48

2-48

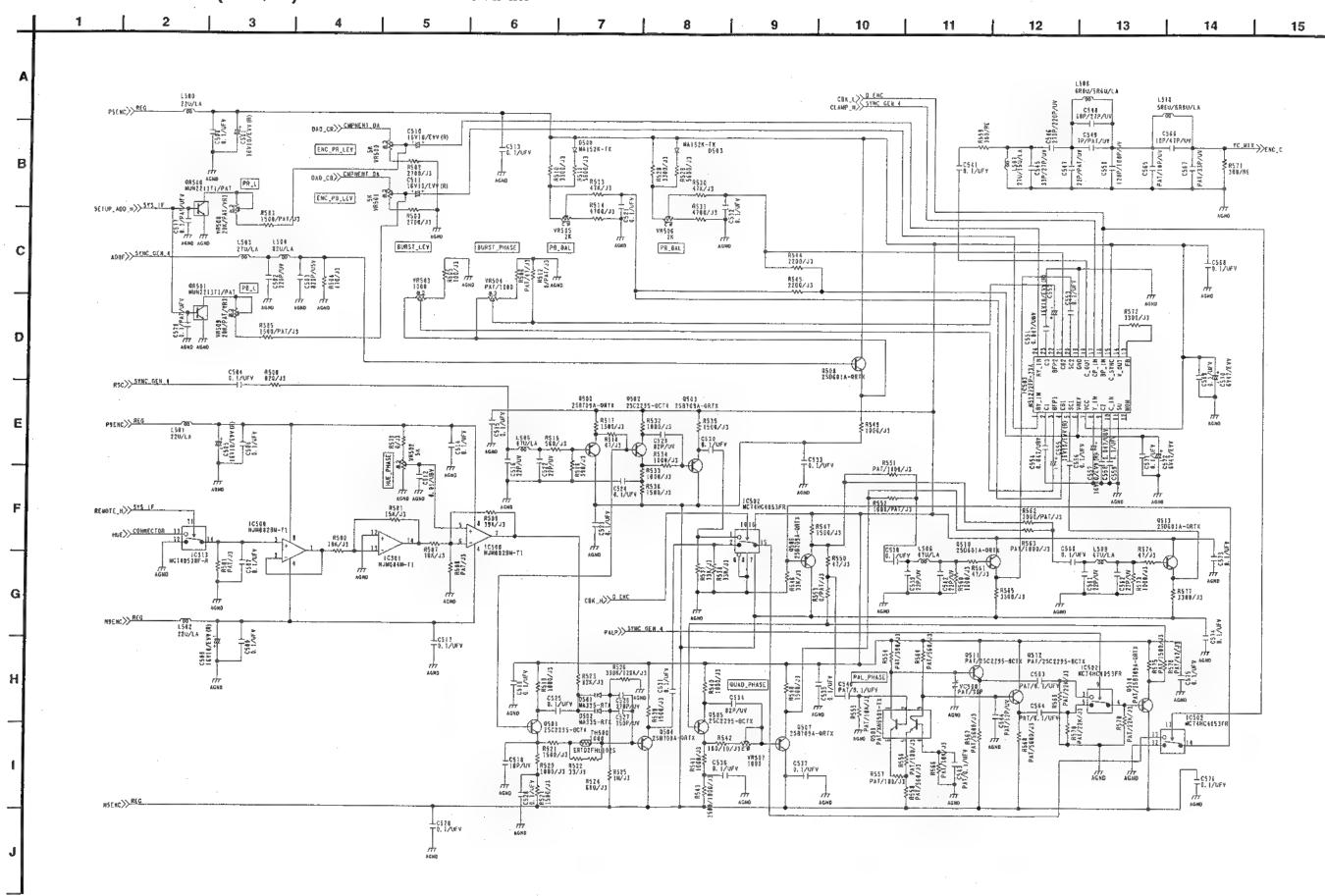
V OUT CMPNENT DA (F4 5/16) SCHEMATIC DIAGRAM



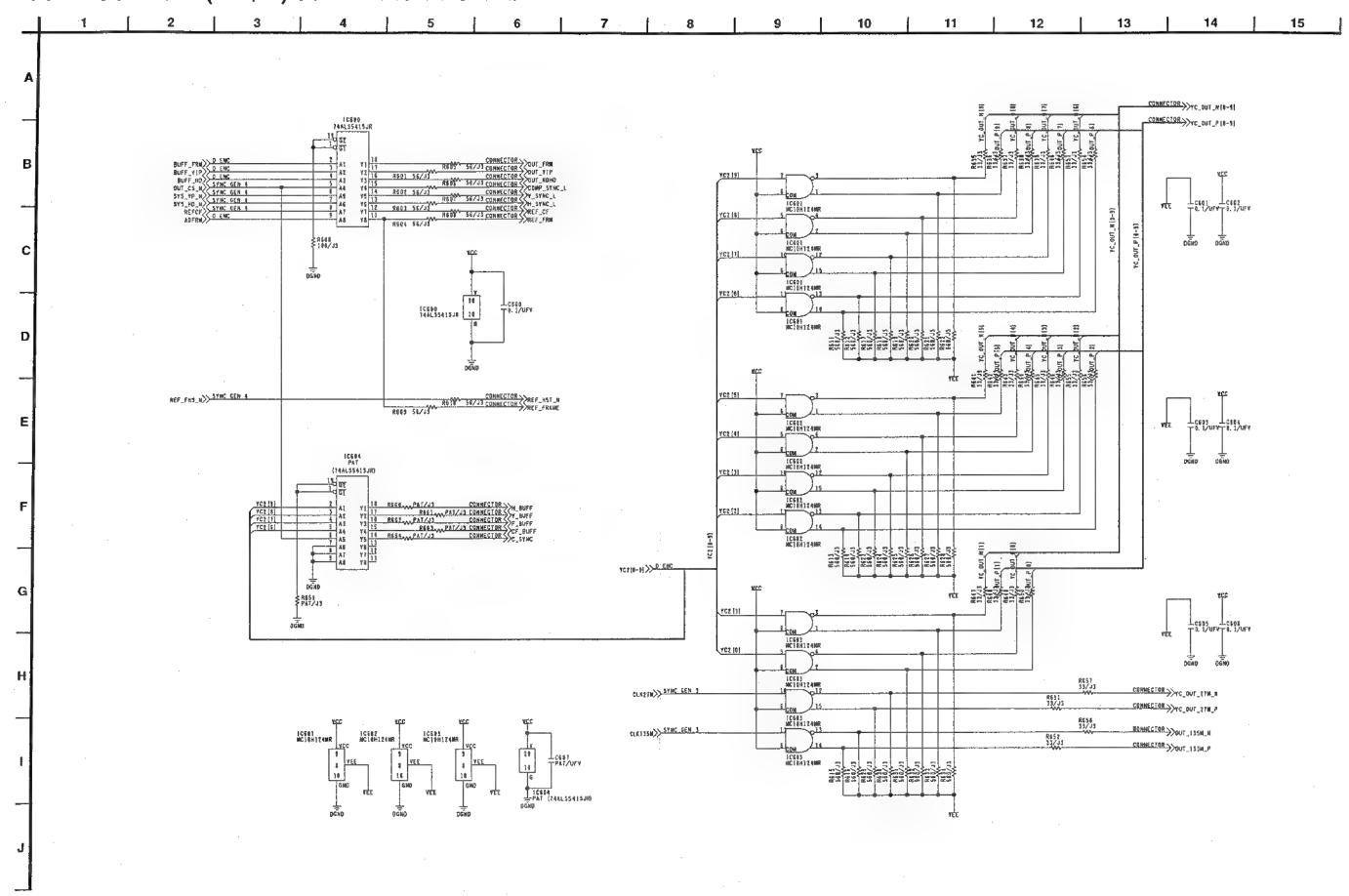
V OUT CMPNENT BUFF (F4 6/16) SCHEMATIC DIAGRAM



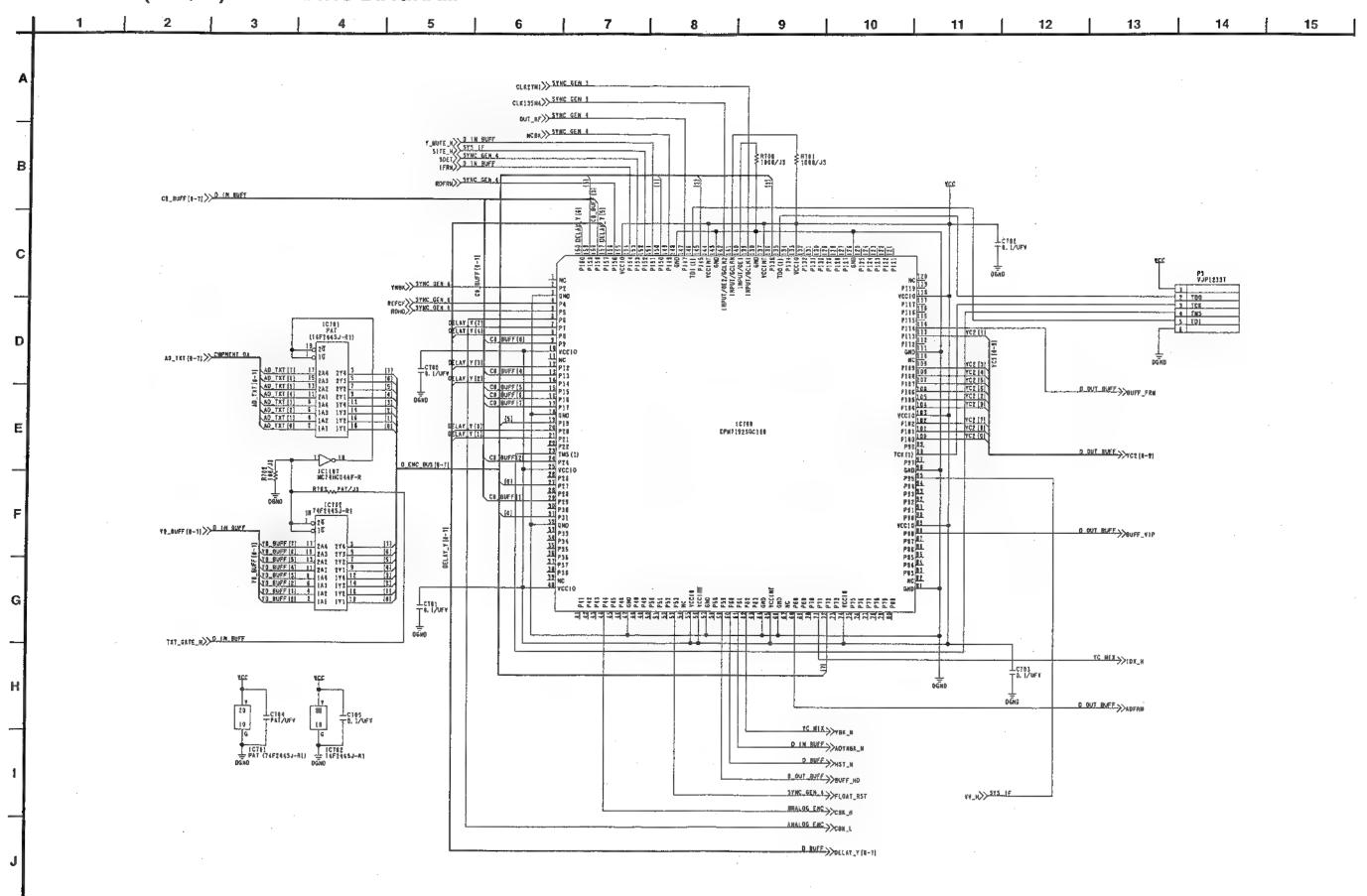
V OUT ANALOG ENC (F4 7/16) SCHEMATIC DIAGRAM



V OUT D OUT BUFF (F4 8/16) SCHEMATIC DIAGRAM



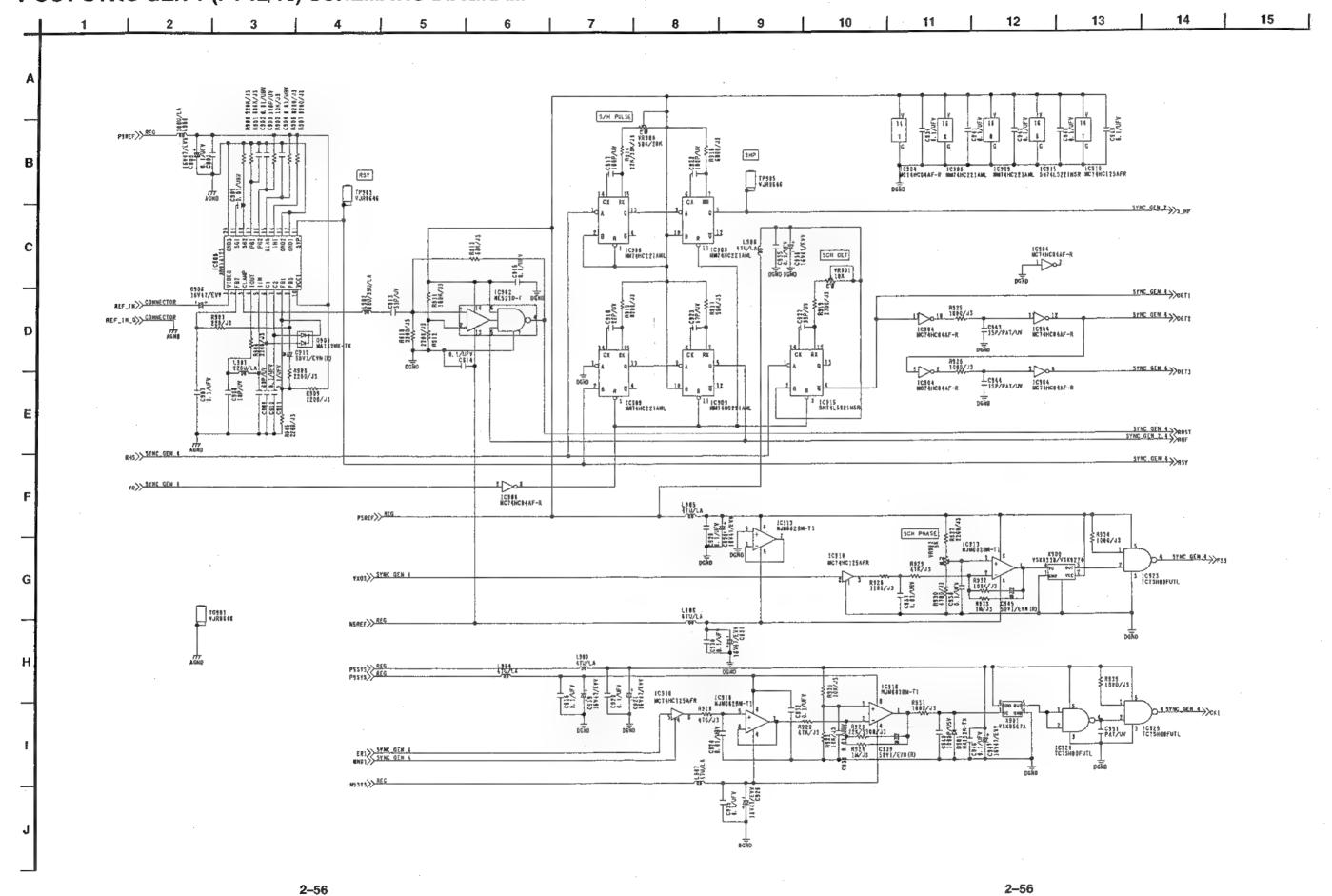
V OUT D ENC (F4 9/16) SCHEMATIC DIAGRAM



V OUT YC MIX (F4 10/16) SCHEMATIC DIAGRAM 14 15 13 11 12 6 9 10 0006 258709A-DRTX 184P_H>> 545 FF ENC_C>> SNAL DG_ENC QBB1 25B109A-ORFX PSEMC>> REG ENC_Y>> CMPNENT_DA 1.0829 T4. L/UFY 16410/EAA (8) ENC_Y_LEV - A CB 2 RBT1 PAT/J3 \$ R841 \$ 3300/J3 2805 8804 28/UV 100K/J3 777 4040 C827 ENC_YC_TIM O 2002 25064 I N-ART NSENC>> REGZ 0863 250801A-9RTX 4831 41/J3 25A1532-8CTX ENC_Y_FREQ 10834 T15P/UV VIDEG_SYMC CAPNENT BUFF CLAMP_N SYNC GEN L YBK_N D_BNC R#25 \$ 2260/J1 ₹ 830 1500/J3 QR301 MUM221311/PAT SETUP_AOD_H>> SYS 1F INDEX_V CHPNENT DA 10824 Til. 1/UFY TOX_H>>DENC TWO_DONNECTOR SENCE_OUT_C P7ENC>> REG 22U/LA 10K/J3 TCB14 SETUP_LEY | SETUP_LEY | AGND ICADS ADJ28AR-R R424 148/US 2004/PA1/VR3) AGNO 5_L 08882 MUH221111/PA1 0870 108/ R813 56K/J3 CMPSITE BUFF REMOTE_H>> SYS IF R# 21 8814 568/41 SET_UP CONNECTOR 3 8110 56K/J3 10111 NJM18284-71 R842 1201/J1 10313 40140538F-R \$RUII PAT/J1 10832 T9. 1/UFY

⊥c(1) 1 ⊤ 0.1/UF¥

V OUT SYNC GEN 1 (F4 12/16) SCHEMATIC DIAGRAM

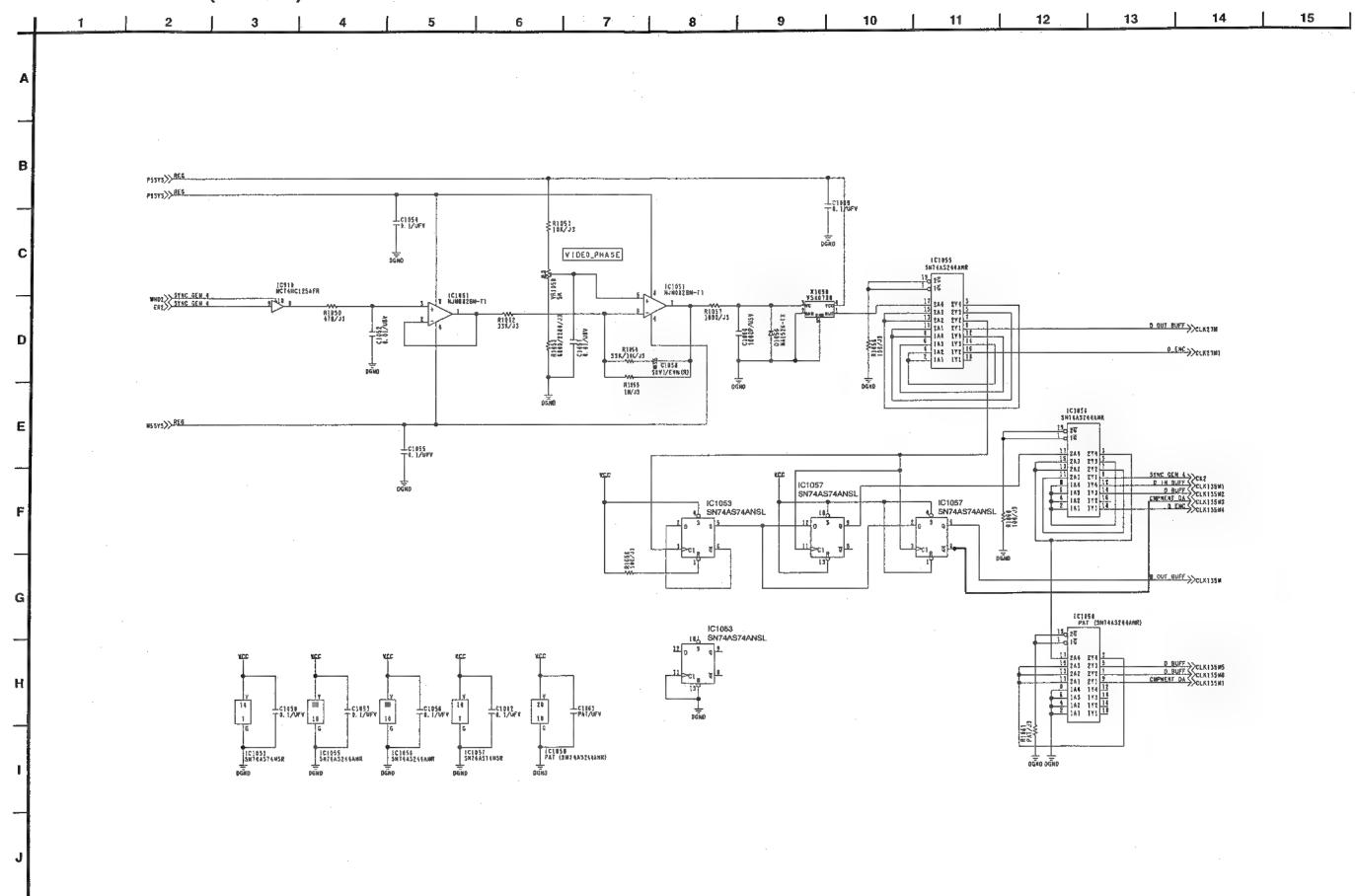


V OUT SYNC GEN 2 (F4 13/16) SCHEMATIC DIAGRAM ROF SYNC GEN 1 SHP SYNC GEN 1 SPET SYNC GEN 5 VHO I STANC GEN 4 IC1015 TC7SH00FUTL 16 MC1405)BF-R 10110 MC74MC125AFR 11094 47U/LA 101600 NES210-1 IC915 SMT4LS22(NSR LALTO SYNC GEN 4 STS_SC_OFF R1021 1004/33

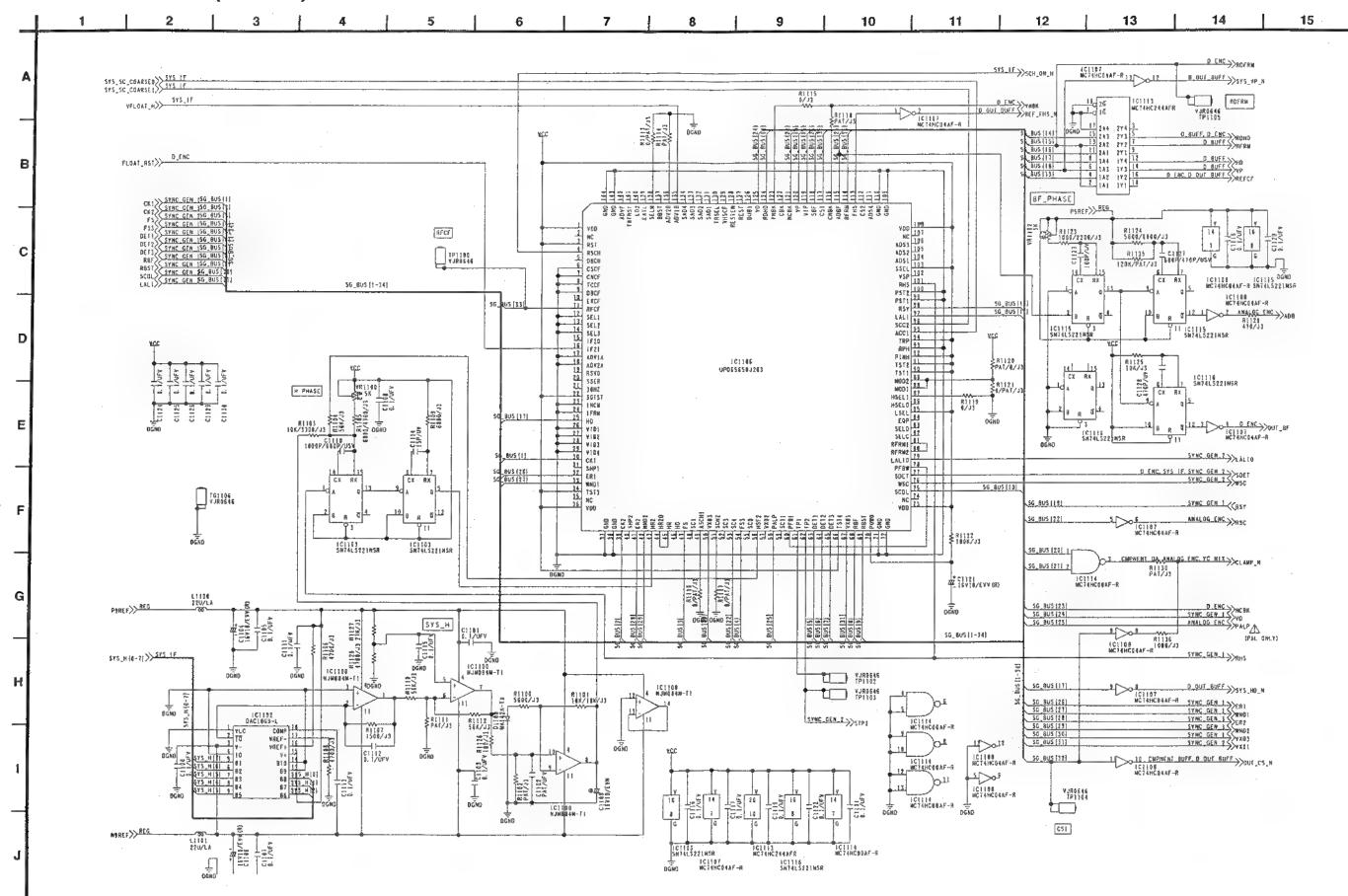
2-57

2-57

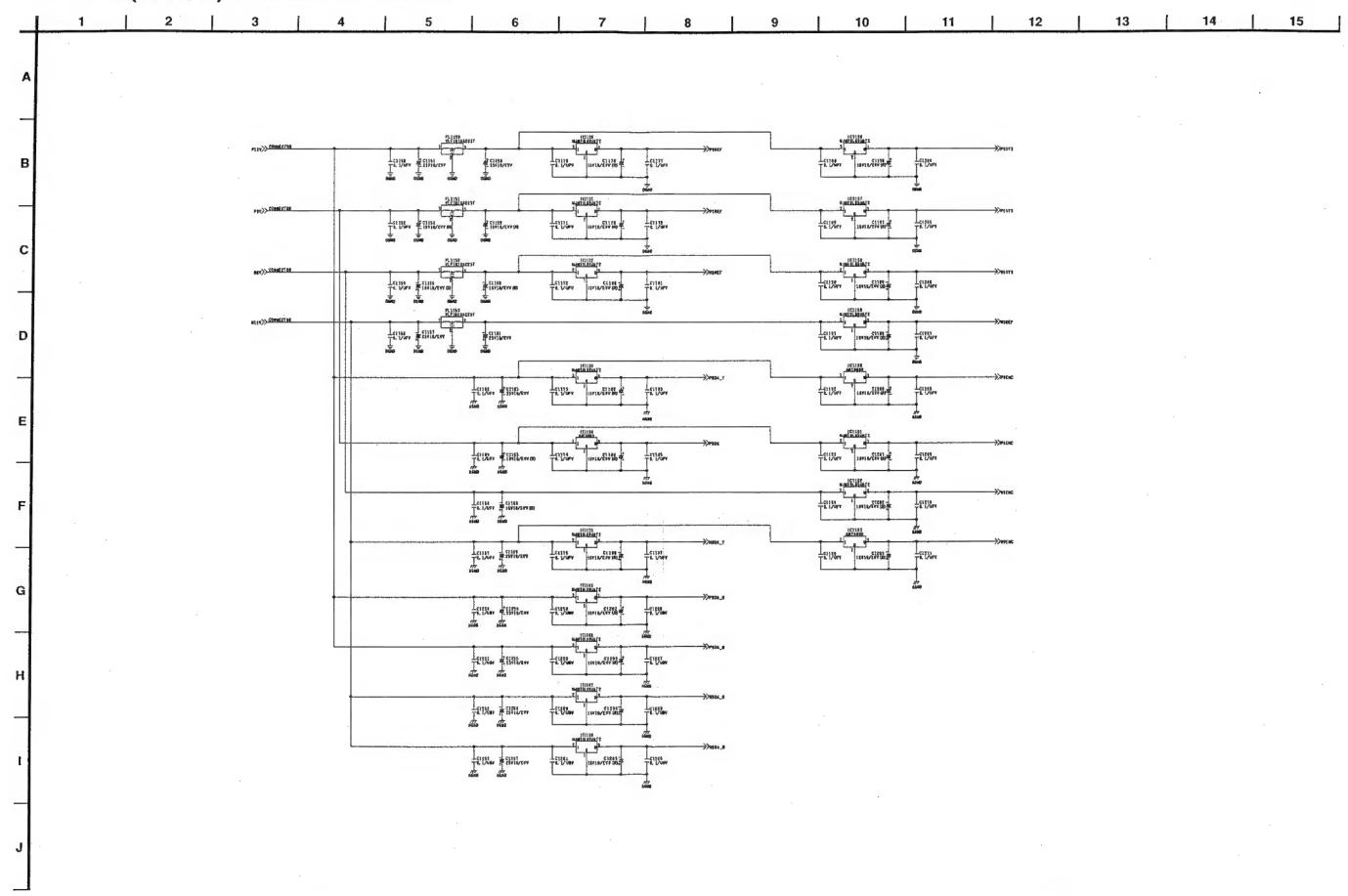
V OUT SYNC GEN 3 (F4 14/16) SCHEMATIC DIAGRAM



V OUT SYNC GEN 4 (F4 15/16) SCHEMATIC DIAGRAM



V OUT REG (F4 16/16) SCHEMATIC DIAGRAM

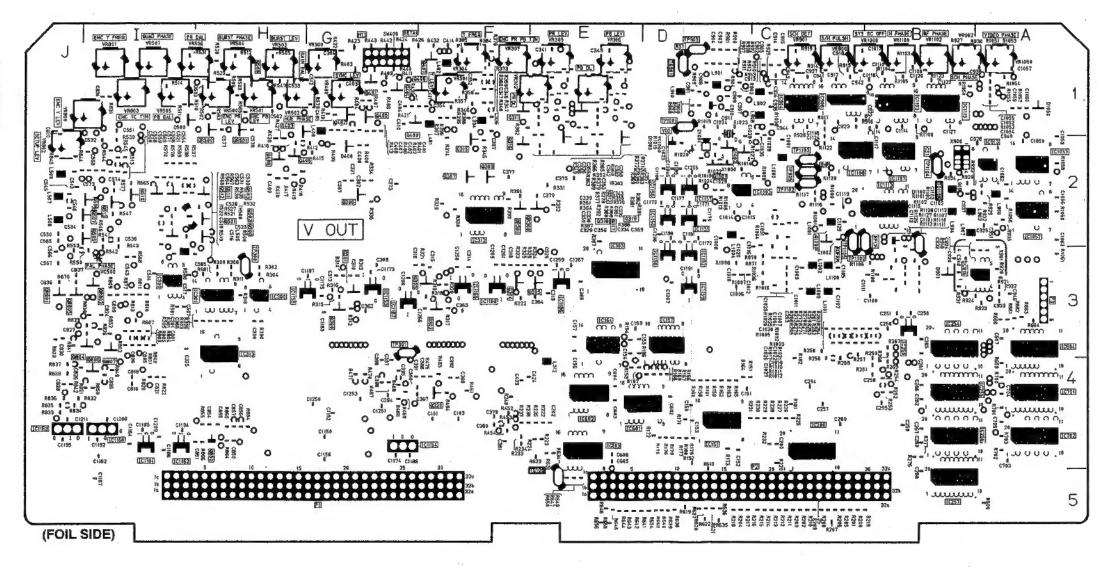


. 1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

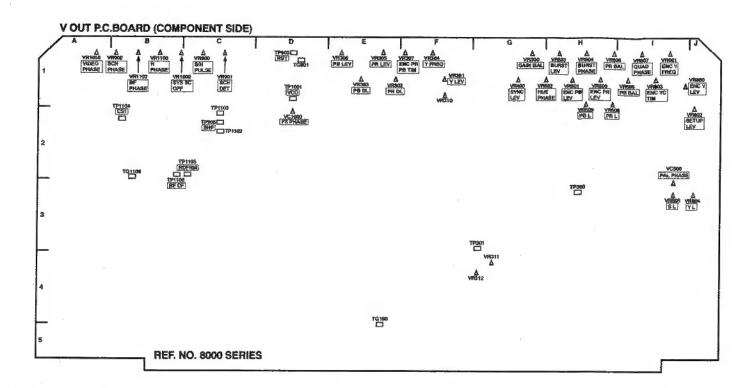
Ref. No.	AJ-D640P/AJ-D650P	AJ-D640/AJ-D650	AJ-D640E/AJ-D650E	Position
C327	ECUV1H120JCV	4		C-F-1
C331	ECUV1H120JCV	+-		C-E-1
C338	ECUV1H100DCV	+		F-E-1
C342	ECUV1H100DCV	÷		F-E-1
C405	ECUV1H180JCV	+	ECUV1H330JCV	F-G-1
C407	ECUV1H121JCV	+	ECUV1H271JCV	F-G-1
C408	ECUV1H120JCV	—		F-G-1
C540			ECUV1E104ZFV	C-J-2
C543			ECUV1E104ZFV	F-I-3
C544			ECUV1H120JCV	C-I-3
C545	ECUV1H330JCV		ECUV1H270JCV	F-J-2
C546	ECUV1H271JCV	+-	ECUV1H221JCV	F-J-2
C547	ECUV1H220JCV	+	L. G. G. T. T. L. G.	C-J-2
C548	ECUV1H680JCV	-	ECUV1H270JCV	C-J-2
C549	ECUV1H070DCV		COOVIIIEIOOOV	C-J-2
C550	ECUV1H121JCV	+	ECUV1H101JCV	F-J-2
C563	COUVINIZIOUV		ECUV1E104ZFV	F-1-2
C564	-		ECUV1E104ZFV	F-I-2
C565	 		ECUV1H100DCV	F-J-2
	ECUV1H100DCV		ECUV1H470JCV	F-J-3
C566 C567	E004 IN 100DCA		ECUV1H330JCV	F-J-3
C577	ECUV1E104ZFV		EGD4 (H3300C4	F-H-1
				F-H-1
C578	ECUV1E104ZFV			
C836	ECUV1E104ZFV			F-J-3
C837	ECUV1E104ZFV			F-J-3
C943	ECUV1H150JCV	+- ,		F-C-1
C944	ECUV1H150JCV	-	ECLINATION NO	F-C-1
C1110	ECUV1H102JV		ECUV1H681JV	F-8-1
C1127	ECUV1H681JV	+	ECUV1H471JCV	F-B-1
L400	VLQ0163J470	+	VLQ0163J101	F-G-1
L507	VLQ0163J270	+	VLQ0163J150	F-J-2
L508	VLQ0163J6Fl8		VLQ0163J5R6	F-J-2
L510	VLQ0163J5R6	+-	VLQ0163J6R8	C-J-3
L800	VLQ0163J120	4-	VLQ0163J150	C-I-3
L902	VLQ0163J680	4	VLQ0163J390	F-C-1
Q509			XN6501-TX	C-I-3
Q511			2SC2295-BCTX	F-I-2
Q512			2SC2295-BCTX	F-1-2
Q514			2SB709A-QRTX	C-I-2
F1250			ERJ3GEYJ103V	F-B-4
R251	ERJ3GEYJ103V	-		F-B-4
R263			ERJ3GEYJ103V	F-B-3
R264	ERJ3GEYJ103V	-		F-B-3
R352	ERJ3GEYJ152V	-	ERJ3GEYJ102V	F-E-1
F1356	ERJ3GEYJ152V	+	ERJ3GEYJ102V	F-E-1
R506			ERJ3GEYJ470V	F-H-1
R512	ERJ3GEYJ0R00V	←		F-H-1
R526	ERJ3GEYJ394V	4	ERJ3GEYJ124V	C-I-2
R542	ERJ3GEYJ181V	-	ERJ3GEYJ100V	F-I-2
R543	ERJ3GEYJ152V	+	ERJ3GEYJ102V	F-I-2
R551		- 1	ERJ3GEYJ102V	C-I-2
R552	ERJ3GEYJ102V	-		C-I-2
R553	ERJ3GEYJ0R00V	←		F-J-3
R554	-	.=	ERJ3GEYJ391V	C-I-2
R555			ERJ3GEYJ103V	C-J-2
R556			ERJ3GEYJ181V	C-I-2

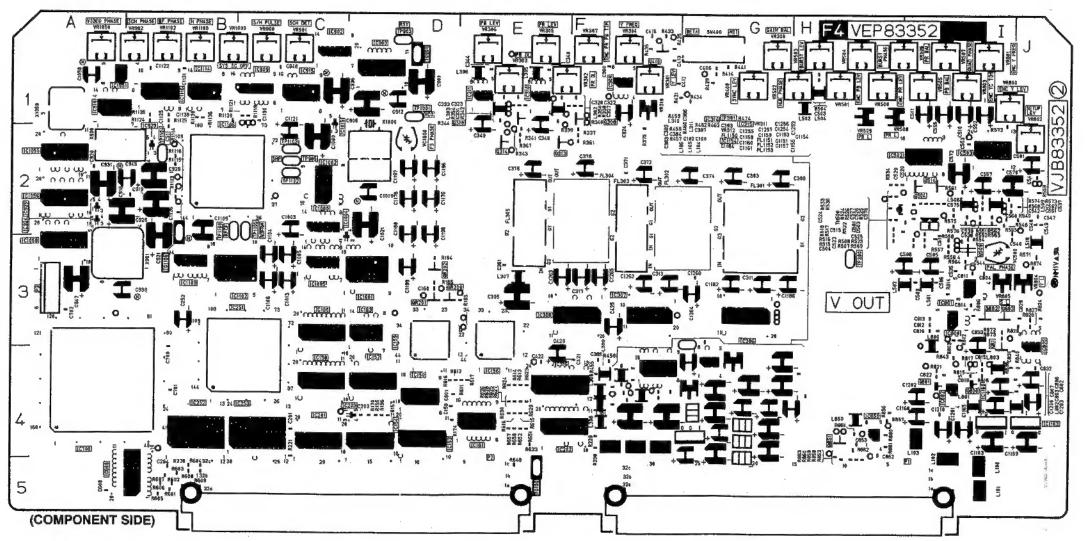
Ref. No.	AJ-D640P/AJ-D650P	AJ-D640/AJ-D650	AJ-D640E/AJ-D650E	Position
1557			ERJ3GEYJ181V	C-I-2
1558			ERJ3GEYJ561V	C-I-2
7562	ERJ3GEYJ102V	4		C-I-2
R563 .			ERJ3GEYJ102V	C-I-2
R564			ERJ3GEYJ561V	C-I-3
R566			ERJ3GEYJ103V	F-1-3
R567	- 04.4		ERJ3GEYJ562V	F-I-2
R568			ERJ3GEYJ562V	F-I-2
R569			ERJ3GEYJ223V	F-I-2
R570			ERJ3GEYJ223V	F-I-2
R575			ERJ3GEYJ152V	C-I-2
R576			ERJ3GEYJ470V	C-I-2
R578			ERJ3GEYJ223V	F-I-2
R583	ERJ3GEYJ152V		L100GL102204	F-H-1
R585	ERJ3GEYJ152V			F-H-1
R801	ERJ3GEYJ182V	←	ERJ3GEYJ271V	F-I-3
R871	ERJ3GEYJ103V	-	ERJ3GEYJ123V	F-C-3
R874			LNOSGETUIZOV	C-J-3
	ERJ3GEYJ332V ERJ3GEYJ563V			
R876 R877				F-J-3 F-I-3
	ERJ3GEYJ104V		ED IOCEVIOORY	
R914	ERJ3GEYJ223V	<u></u>	ERJ3GEYJ333V	F-C-1
R923	ERJ3GEYJ223V	<u></u>	ERJ3GEYJ332V	F-A-3
R1004	ERJ3GEYJ0R00V	-	55 565 (155 50)	F-C-3
R1005			ERJ3GEYJ0R00V	F-C-3
R1053	ERJ3GEYJ682V	+-	ERJ3GEYJ222V	F-A-1
R1054	ERJ3GEYJ333V	← .	ERJ3GEYJ103V	F-A-1
R1101	ERJ3GEYJ103V	-	ERJ3GEYJ183V	F-B-3
R1103	ERJ3GEYJ103V	-	ERJ3GEYJ332V	F-B-1
R1105	ERJ3GEYJ682V	←	ERJ3GEYJ472V	F-B-1
R1113	ERJ3GEYJ0A00V	+		C-B-2
R1116	ERJ3GEYJ0R00V	←		F-C-2
R1117	ERJ3GEYJ0R00V	←		F-C-2
R1120			ERJ3GEYJ0R00V	C-B-1
R1121	ERJ3GEYJ0R00V		7111	C-B-1
R1123	ERJ3GEYJ102V	+	ERJ3GEYJ222V	F-B-1
R1124	ERJ3GEYJ562V	-	ERJ3GEYJ6B2V	C-B-1
R1135	ERJ3GEYJ124V	- ,		C-B-1
X900	VSX0338	+	VSX0270	C-A-2
X1000	VSX0081	4	VSX0363	C-D-2
QR500	MUN2213T1			F-H-2
QR501	MUN2213T1			F-H-2
QR801	MUN2213T1	•		F-J-3
QR802	MUN2213T1			F-I-2
QR803	MUN2212T1			F-J-2
SW400	VSS0372	-		C-G-1
VC500			ECV1ZW30X53T	Ç-1-3
VR504	V-5		EVMEGSA00B13	C-H-1
VR508	VRV0113B203T			C-H-1
VR509	VRV0113B203T			C-H-1
VR804	VRV0113B203T			C-J-3
VR805	VAV0113B203T			C-I-3
VR900	EVMEGSA00B54	-	EVMEGSA00B24	C-C-1
IC700	VSI2499B	-	VSI2500B	C-A-4

Position.....C: Component Side F: Foil Side



V OUT (FOIL SIDE)							
Transistors		IC1003	D-2				
Q300	G-3	IC1053	A-2				
Q301	G-3	IC1057	A-2				
Q302	F-3	IC1103	8-1				
Q303	F-3	IC1107	B-2				
Q304	F-3	IC1108	C-2				
Q305	E-3	IC1113	B-2				
Ø306	G-2	IC1115	B-1				
Q307	F-2	IC1150	D-2				
C)309	F-2	IC1151	D-2				
Q311	F-1	IC1152	D-3				
Q312	E-1	IC1153	G-3				
Q315	F-2	IC1154	G-4				
Q316	F-2	JC1155	H-3				
Q317	E-2	IC1156	D-2				
Q318	E-2	IC1157	D-2				
Q319	E-2	IC1158	D-3				
Q320	F-4	IC1159	D-3				
Q400	G-1	IC1160	1-4				
Q401 ·	G-1	IC1161	1-4				
Q402	G-1	IC1162	1-4				
Q403	H-1	IC1163	J-4				
Q404	G-1	IC1165	F-3				
Q405	G-2	IC1166	E-3				
Q408	F-1	IC1167	G-3				
Q407	F-1	IC1168	F-3				
Q408	H-2	Test Points					
Q409	G-1	TG100	E-5				
Q500	H-2	TG901	D-1				
Q501	1-2	TG1106	B-2				
Q502	H-2	TP300	H-3				
Q503	H-2	TP301	G-3				
Q506	l-2	TP903	D-1				
Q506	I-2	TP905	C-2				
Q507	1-2	TP1001	D-1				
Q508	H-1	TP1100	B-2				
Q510	1-2	TP1102	C-2				
Q511	1-2	TP1103	C-2				
Q512	1-2	TP1104	8-2				
Q513	1-2	TP1105	C-2				
Q804	1-3	Adjustments					
Q806	1-4	VC500	I-3				
Q807	I-3	VC1000	D-2				
Q850	H-4	VR300	G-1				
Q1000	C-2	VR301	F-1				
Q1001	D-2	VR302	F-1				
Q1002	D-2	VR304	F-1				
Transistor & Re	sistors	VR305	E-1				
QR300	E-2	VR306	E-1				
QR301	F-2	VR307	F-1				
QR500	H-2	VR400	G-1				
QR501	H-1	VR500	H-1				
QR801	J-1	VR501	H-1				
QR802	I-3	VR502	H-1				
QR803	J-3	VR503	H-1				
Integrated Circu		VR504	H-1				
IC151	D-4	VR505	I-1				
IC157	D-3	VR506	H-1				
IC164	E-3	VA507	J-1				
IC200	C-4	VR600	J-1				
IC250	B-3	VR801	1-1				
IC254	B-3	VR802	J-2				
IC255	B-4	VR803	1-1				
IC266	B-4	VR900	. C-1				
IC257	8-5	VR901	C-1				
IC300	H-3	VR902	B-1				
IC301	H-3	VR1000	B-1				
IC312	F-2	VR1050	A-1				
IC313	H-3	VR1100	B-1				
₽C500	1-3	VR1102	8-1				
fC601	E-4	Connectors					
IC602	E-4	P1	G-5				
IC603	E-4	P2	C-5				
IC604	A-3	P3	A-3				
IG701	A-4						
10700	A-4						
IC702	1						
IC904	C-1						
	C-1 C-1						
IC904	1						





V OUT (COMPONENT SIDE)							
Transistors		IC1114	B-1				
Q308	F-1	IC1116	B-1				
Q310	É-1	IC1154	G-4				
Q3\$3	E-2	IC1160	1-4				
Q314	E-2	IC1163	. J-4				
Q410	F-1	Test Points					
Q504	I-2	TG100	E-5				
Q509	1-3	TG901	D-1				
Q514	1-2	TG1106	B-3				
Q800	1-4	TP300	H-3				
Q801	1-4	TP301	G-4				
Q802	1-2	TP903	D-1				
Q803	1-3	TP905	C-2				
Q851 Transistor & R	H-4	TP1001 TP1100	D-1				
QR1	I-3	TP1100	B-2 C-2				
QR200	D-3	TP1102	C-2				
QR201	D-3	TP1103	B-2				
QR202	D-3	TP1105	C-2				
Integrated Circ		Adjustments	1 0.2				
IC150	D-4	VC500	1-3				
IC152	D-4	- VC1000	D-2				
IC153	D-4	VFI300	G-1				
IC154	D-4	VR301	F-1				
IC156	E-3	VR302	F-1				
IC158	C-4	VR303	E-1				
IC159	D-3	VR304	F-1				
IC160	C-3	VR305	E-1				
IC161	E-4	VR306	Ę-1				
IC163	D-3	VR307	F-1				
IC201	C-4	VR310	F-1				
IC202	F-4	VR311	G-4				
IC203	D-4	VR312	G-4				
IC251	C-3	VR400	G-1				
IC252	B-4	VR500	H-1				
IC253	C-4	VR501	H-1				
1C306	G-3	VR502	G-1				
IC307	F-3	VH503	H-1				
IC308	E-3 F-1	VR504 VR505	H-1 I-1				
IC309	E-1	VR506	- 1-1				
iC311	E-1	VR507	1-1				
IC314	G-4	VR508	H-1				
IC315	G-4	VR509	H-1				
IC316	E-4	VR800	I-1				
IC502	1-2	VR801	1-1				
IC503	I-2	VR802	J-2				
IC600	B-5	VR803	1-1				
IC700	A-4	VR804	J-3				
IC800	I-3	VR805	1-3				
ICB01	1-3	VR900	C-1				
iC802	1-4	VR901	C-1				
IC805	J-3	VR902	B-1				
IC850	H-4	VR1000	B-1				
IC900	D-1	VR1050	A-1				
IC902	C-1	VR1100	B-1				
IC909	C-1	VR1102	B-1				
IC910		Connectors					
IC915	C-1	P1	1-4				
IC923	B-2	P2	E-4				
IC924	A-2	Pa	A-3				
IC925 IC1000	A-2		- 1				
IC1000	C-2 D-3						
IC1001	C-2		- 1				
IC1002	D-2		i				
IC1004	C-3						
IC1005	C-2						
IC1015	A-1	i	- 1				
IC1055	A-2		ı				
IC1056	A-2	1	- 1				
IC1058	A-3	ļ	- 1				
IC1100	8-3		- [
IC1102	C-3	1	1				
IC110E	00	- 1					

IC1106